INCH-POUND
MIL-M-38510/304C
10 February 2003
SUPERSEDING
MIL-M-38510/304B
9 August 1983

#### MILITARY SPECIFICATION

# MICROCIRCUITS, DIGITAL, BIPOLAR LOW-POWER SCHOTTKY TTL, AND-OR-INVERT GATES, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

## 1. SCOPE

- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, positive AND-OR-INVERT logic gating microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).
  - 1.2 Part number. The part number shall be in accordance with MIL-PRF-38535, and as specified herein.
  - 1.2.1 <u>Device types</u>. The device types shall be as follows:

Device type	<u>Circuit</u>
01	Dual, 2-wide, AND-OR-INVERT gate
02	4-wide, 3-2-2-3 input AND-OR-INVERT gate

- 1.2.2 <u>Device class</u>. The device class shall be the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 Case outlines. The case outlines shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Α	GDFP5-F14 or CDFP6-F14	14	Flat pack
В	GDFP4-14	14	Flat pack
С	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

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## 1.3 Absolute maximum ratings.

Cupply voltage renge

Supply voltage range	-0.5 V ac to +7.0 V ac
Input voltage range	-1.5 V dc at -18 mA to +5.5 V dc
Storage temperature range	-65° to +150°C
Maximum power dissipation, (P <sub>D</sub> ) 1/:	
Device type 01	16 mW dc
Device type 02	11 mW dc
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction to case ( $\theta_{JC}$ ):	
Cases A, B, C, D, and 2	(See MIL-STD-1835)
Junction temperature (T <sub>J</sub> ) $\underline{2}$ /	+175°C
Recommended operating conditions.	
Cumply voltage (\(\lambda_{-}\)	145 V do minimum to 155 V do

## 1.4

Supply voltage (V <sub>CC</sub> )	+4.5 V dc minimum to +5.5 V dc
	maximum
Minimum high level input voltage (V <sub>IH</sub> )	2.0 V
Maximum low level input voltage (V <sub>IL</sub> )	
Case operating temperature range (T <sub>C</sub> )	-55° to +125°C

## 2. APPLICABLE DOCUMENTS

### 2.1 Government documents.

2.1.1 Specifications and Standards. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

## **SPECIFICATION**

## DEPARTMENT OF DEFENSE

MIL-PRF-38535 -Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

## **STANDARDS**

## DEPARTMENT OF DEFENSE

Test Method Standard for Microelectronics. MIL-STD-883

MIL-STD-1835 Interface Standard Electronic Component Case Outlines

(Unless otherwise indicated, copies of the above specifications and standards are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

 $<sup>\</sup>underline{1}$ / Must withstand the added  $P_D$  due to short-circuit test (e.g.,  $I_{OS}$ ) at one output for 5 seconds duration..

<sup>2/</sup> Maximum junction temperature shall not be exceeded except in accordance with allowable short duration burn-in screening condition in accordance with MIL-PRF-38535.

#### 3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
- 3.3.1 <u>Terminal connections and logic diagrams.</u> The terminal connections and logic diagrams shall be as specified on figure 1.
  - 3.3.2 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.
- 3.3.3 <u>Schematic circuits.</u> The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.
  - 3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.
  - 3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements.</u> The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.
  - 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 8 (see MIL-PRF-38535, appendix A).

## 4. VERIFICATION

- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:
  - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
  - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
  - c. Additional screening for space level product shall be as specified in MIL-PRF-38535.
  - 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions	Device	Li	mits	Unit
		-55°C ≤ T <sub>C</sub> ≤ +125°C	types	Min	Max	
High level output voltage	V <sub>OH</sub>	$V_{CC} = +4.5 \text{ V}, V_{IN} = +0.7 \text{ V},$ $I_{OH} = -400 \mu\text{A}$	All	2.5		V
Low level output voltage	V <sub>OL</sub>	$V_{CC} = +4.5 \text{ V}, I_{OL} = +4 \text{ mA},$ $V_{IN} = +2.0 \text{ V}$	All		0.4	V
Input clamp voltage	V <sub>IC</sub>	$V_{CC} = +4.5 \text{ V}, I_{IN} = -18 \text{ mA},$ $T_{C} = +25^{\circ}\text{C}$	AII		-1.5	V
High level input current	I <sub>IH1</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +2.7 \text{ V}$	All		20	μА
	I <sub>IH2</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = +5.5 V	All		100	μΑ
Low level input current	I <sub>IL</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = +0.4 V	All	-30	-380	μА
Short circuit output current	Ios	V <sub>CC</sub> = +5.5 V <u>1</u> /	All	-15	-100	mA
High level supply current	I <sub>CCH</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = +0 V	All		1.6	mA
Low level supply current	I <sub>CCL</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +5.5 \text{ V}$	01 02		2.8	mA
Propagation delay time high to low level	t <sub>PHL</sub>	$V_{CC}$ = +5.0 V, $C_L$ = 50 pF, $R_L$ = 2 k $\Omega$	All	3	30	ns
Propagation delay time low to high level	t <sub>PLH</sub>	$V_{CC}$ = +5.0 V, $C_L$ = 50 pF, $R_L$ = 2 k $\Omega$	All	3	30	ns

 $<sup>\</sup>underline{1}/$  Not more than one output should be shorted at a time.

TABLE II. Electrical test requirements.

	Subgroups	(see table III)
MIL-PRF-38535	Class S	Class B
test requirements	devices	devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 9, 10, 11	1*, 2, 3, 9
Group A test requirements	1, 2, 3, 9, 10, 11	1, 2, 3, 9,
Group C end-point electrical parameters	1, 2, 3, 9, 10, 11	1, 2, 3
Additional electrical subgroup for Group C periodic inspections	N/A	10,11
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

<sup>\*</sup>PDA applies to subgroup 1.

- 4.4 <u>Technology Conformance inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
- 4.4.1 <u>Group A inspection.</u> Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
  - a. Tests shall be as specified in table II herein.
  - b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.
  - 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.
- 4.4.3 <u>Group C inspection.</u> Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. A subgroup shall be added to the group C inspection requirements for class B devices and shall consist of the tests, conditions, and limits specified for subgroups 10 and 11 of group A.
  - c. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
  - 4.5 Methods of inspection. Methods of inspection shall be specified and as follows.
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

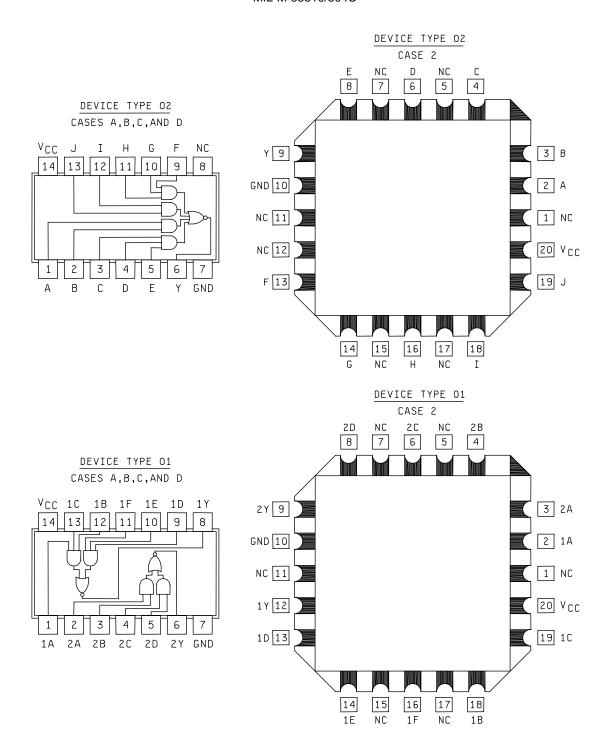


FIGURE 1. Terminal connections and logic diagrams.

Device type 01, gate 1

	Inputs													
1A	1B	1C	1D	1E	1F	1Y								
Н	Н	Н	*	*	*	L								
*	*	*	Н	Н	Н	L								

Device type 01, gate 2

	Inp	Output		
2A	2B	2C	2D	2Y
Н	Н	*	*	L
*	*	Н	Н	L

\* Either H or L
All other combinations of H and L at the input give H output.

Positive logic: 
$$1Y = \overline{(1A \cdot 1B \cdot 1C) + (1D \cdot 1E \cdot 1F)}$$

$$2Y = \overline{(2A \bullet 2B) + (2C \bullet 2D)}$$

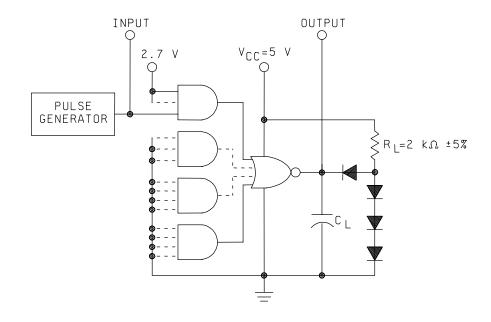
Device type 02

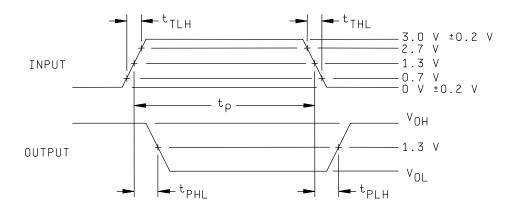
	Inputs														
Α	В	C	D	Е	F	G	Η	-	っ	Υ					
Н	Н	*	*	*	*	*	*	*	*	L					
*	*	Η	Н	Н	*	*	*	*	*	L					
*	*	*	*	*	Н	Н	Н	*	*	L					
*	*	*	*	*	*	*	*	Н	Н	L					

\* Either H or L - All other combinations of H and L at the input give H output.

Positive logic:  $Y = \overline{AB + CDE + FGH + IJ}$ 

FIGURE 2. Truth tables and logic equations.





## NOTES:

- 1. The pulse generator has the following characteristics: PRR  $\leq$  1 MHz,  $t_{TLH} \leq$  15 ns,  $t_{THL} \leq$  6 ns, tp = 0.5  $\mu s$ , and  $Z_{OUT} \cong 50\Omega.$
- 2.  $C_L = 50 \ pF \pm 10\%$ , including scope probe, wiring, and stray capacitance, without package in test fixture.
- 3. All diodes are 1N3064 or equivalent.
- 4. Voltage measurements are to be made with respect to network ground terminal.
- 5. Dashes refer to device type 02.

FIGURE 3. Switching time test circuit and waveforms.

TABLE III. Group A inspection for device type 01. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

							iliuliliolis			ited may											
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol		Case 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lin	nits	Unit
		memod	Test no.	1A	2A	2B	2C	2D	2Y	GND	1Y	1D	1E	1F	1B	1C	V <sub>cc</sub>	terrilliai	Min	Max	
1	1/	3006		0.7 V	5.5 V	5.5 V	5.5 V	5.5 V	21	GND			5.5 V	5.5 V	5.5 V	5.5 V	4.5 V	1Y	2.5	IVIAX	V
	V <sub>OH</sub>	3006	1		3.3 V	3.3 V	3.5 V	3.5 V		GND "	-400 μΑ	0.7 V					4.5 V		2.5		V
$Tc = 25^{\circ}C$			2	5.5 V		"				"		5.5 V	0.7 V	5.5 V	0.7V	5.5 V		1Y			
			3	"	"		"			"	"	- "	5.5 V	0.7 V	5.5 V	0.7 V		1Y			
			4	"	0.7 V	"	0.7 V	"	-400 μΑ	"			"	5.5 V	"	5.5 V	. "	2Y	"		
		"	5	"	5.5 V	0.7 V	5.5 V	0.7 V	-400 μΑ	-		"	"	5.5 V		5.5 V	"	2Y	•		"
	$V_{OL}$	3007	6	2.0 V	GND	GND	GND	GND		"	4 mA	GND	GND	GND	2.0 V	2.0 V	"	1Y		0.4	V
		"	7	GND	GND	GND	"	"		"	4 mA	2.0 V	2.0 V	2.0 V	GND	GND	"	1Y		"	"
		"	8	"	2.0 V	2.0 V	"	=	4 mA	"		GND	GND	GND	"	=	"	2Y		"	"
		"	9	"	GND	GND	2.0 V	2.0 V	4 mA	"		GND	GND	GND	"	"	"	2Y		"	"
	$V_{IC}$		10	-18 mA						"							"	1A		-1.5	"
			11							"					-18 mA		"	1B		"	"
			12							ű						-18 mA	"	1C		"	"
			13							"		-18 mA					"	1D		"	"
			14							"			-18 mA				"	1E		"	"
			15							ű				-18 mA			"	1F		"	"
			16		-18 mA					"							"	2A		"	"
			17			-18 mA				ii .							-	2B		"	"
			18				-18 mA			*							-	2C		"	"
			19					-18 mA		ii .							"	2D		"	"
	Ι <sub>ΙL</sub>	3009	20	0.4 V	5.5 V	5.5 V	5.5 V	5.5 V		"		5.5 V	1A	2/	2/	μΑ					
	2/	"	21	5.5 V	"	"	"	"		"		"	"	"	0.4 V	5.5 V	"	1B	"	"	"
		"	22	"	"	"	"	"		"		"	"	"	5.5 V	0.4 V	"	1C	"	"	"
		"	23	"	"	"	"			"		0.4 V	"	"	"	5.5 V	"	1D	"	"	"
		"	24	"	"	"	"			"		5.5 V	0.4 V	"	"		"	1E	"	"	"
		"	25	"	"	"	"	"		"		"	5.5 V	0.4 V	"	"	"	1F	"	"	"
		"	26	"	0.4 V	"	"	"		"		"	"	5.5 V	"	"	"	2A		"	"
		"	27	"	5.5 V	0.4 V	"			"		"	"	"	"		"	2B	"	"	"
		"	28	"	"	5.5 V	0.4 V			"		"	"	"	"		"	2C	"	"	"
		"	29	"	"	5.5 V	5.5 V	0.4 V		"		"	"	"	"		"	2D	"	"	"
	I <sub>IH1</sub>	3010	30	2.7 V	GND	GND	GND	GND		"		GND	GND	GND	GND	GND	"	1A		20	"
		"	31	GND	"	"	"			"		"	"	"	2.7 V	GND	"	1B		"	"
		"	32	"	"	"	"	-		**		"	"	"	GND	2.7 V	"	1C		"	"
		"	33	"	"	"	"	-		**		2.7 V	"	"		GND	"	1D		"	"
		"	34	"	"	"	"	"		tt		GND	2.7 V	"	"	"	"	1E		"	"
		"	35	"	"	"	"	"		tt		"	GND	2.7 V	"	"	"	1F		"	"
		"	36	"	2.7 V	"	"			"		"	"	GND	"		"	2A		"	"
		"	37	"	GND	2.7 V	"	"		"		"	"	"	"	"	"	2B		"	"
		"	38	"	"	GND	2.7 V	"		tt		"	"	"	"	"	"	2C		"	"
		"	39	"	"	"	GND	2.7 V		"		"	"	"	"	"	"	2D		"	"
	I <sub>IH2</sub>	"	40	5.5 V	"	"	"	GND		"		"	"	"	"	"	"	1A		100	"
		"	41	GND	"	"	"	"		"		"	"	"	5.5 V	"	"	1B		"	"
		"	42	"	"	"	"	"		"		"	"	"	GND	5.5 V	"	1C		"	"
		"	43	"	"	"	"	"		"		5.5 V	"	"	"	GND	"	1D		"	"
		"	44	"	"	"	"	"		"		GND	5.5 V	"	"	"	"	1E		"	"
		"	45	"	"	"	"			ii.		"	GND	5.5 V	"	"	"	1F		"	"
		"	46	"	5.5 V	"	"	"		"		"	"	GND	"	"	"	2A		"	"
		"	47	"	GND	5.5 V	"	"		"		"	"	"	"	"	"	2B		"	"
1		"	48	"	"	GND	5.5 V	"		=		"	"	"	"	"	"	2C		"	"
1																	1				

See footnotes at end of table.

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TABLE III. Group A inspection for device type 01 – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

					10	iiiiiiiai o	Jilaitionio	(Pirio ric	r acoign	aleu may	be mgm	<u>- 2.0 v,</u>	10W <u>3</u> 0.	<i>1</i> V, OI O	perij.						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	1A	2A	2B	2C	2D	2Y	GND	1Y	1D	1E	1F	1B	1C	V <sub>CC</sub>		Min	Max	I
1	Ios	3011	50	GND						GND	GND	GND	GND	GND	GND	GND	5.5 V	1Y	-15	-100	mA
Tc = 25°C		3011	51		GND	GND	GND	GND	GND	"							"	2Y	-15	-100	"
	I <sub>CCH</sub>	3005	52	GND	GND	GND	GND	GND		"		GND	GND	GND	GND	GND	"	V <sub>CC</sub>		1.6	"
	I <sub>CCL</sub>	3005	53	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V		"		5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	"	$V_{CC}$		2.8	"
2		ests, termina								tests are											
3	Same t	ests, termina	al conditions	and limits	s as for su	bgroup 1,	except To	= -55°C	and V <sub>IC</sub> to	ests are or	nitted.										
9	t <sub>PHL</sub>	3003	54	IN						GND	OUT	GND	GND	GND	2.7 V	2.7 V	5.0 V	1A to 1Y	3	22	ns
Tc = 25°C		Fig. 3	55	2.7 V						44	"	"	"	"	IN	2.7 V	"	1B to 1Y	"	"	"
		"	56	2.7 V						"	"	"	"	"	2.7 V	IN	"	1C to 1Y	"		"
		"	57	GND						"	"	IN	2.7 V	2.7 V	GND	GND	"	1D to 1Y	"		"
		"	58	"						"	"	2.7 V	IN	2.7 V	"	"	"	1E to 1Y	"	"	"
		"	59	"						"	"	2.7 V	2.7 V	IN	"	"	"	1F to 1Y	"	"	"
		"	60		IN	2.7 V	GND	GND	OUT	"							"	2A to 2Y		"	"
		"	61		2.7 V	IN	GND	GND	"	"							"	2B to 2Y	"	"	"
		"	62		GND	GND	IN	2.7 V	"	"							"	2C to 2Y	"	"	"
		"	63		GND	GND	2.7 V	IN	"	"							"	2D to 2Y	"	"	"
	t <sub>PLH</sub>	"	64	IN						"	OUT	GND	GND	GND	2.7 V	2.7 V	"	1A to 1Y		19	"
	1 211	"	65	2.7 V						"	"	"	"	"	IN	2.7 V	"	1B to 1Y	"	"	"
		"	66	2.7 V						"	"	"	"	"	2.7 V	IN	"	1C to 1Y	"	"	"
		"	67	GND						"	"	IN	2.7 V	2.7 V	GND	GND	"	1D to 1Y	"	"	"
		"	68	"						"	"	2.7 V	IN	2.7 V	"	"	"	1E to 1Y		"	"
		"	69	"						"	"	2.7 V	2.7 V	IN	"	"	"	1F to 1Y		"	"
		"	70		IN	2.7 V	GND	GND	OUT	"							"	2A to 2Y	"	"	"
		"	71		2.7 V	IN	GND	GND	"	"							"	2B to 2Y	"	"	"
		"	72		GND	GND	IN	2.7 V	"	"							"	2C to 2Y	"	"	"
		"	73		GND	GND	2.7 V	IN	"	"							"	2D to 2Y	"	"	"
10	t <sub>PHL</sub>	"	74	IN	0.12	0.12				"	OUT	GND	GND	GND	2.7 V	2.7 V	"	1A to 1Y	"	30	"
Tc = 125°C	PHL	"	75	2.7 V						"	"	"	"	"	IN	2.7 V	"	1B to 1Y	"	"	"
10 - 123 0			76	2.7 V						"	"	"	"	"	2.7 V	IN	"	1C to 1Y	"	"	"
			77	GND						"	"	IN	2.7 V	2.7 V	GND	GND		1D to 1Y	"		"
			78	"							"	2.7 V	IN	2.7 V	"	"	"	1E to 1Y		"	"
		,	79	"						"	"	2.7 V	2.7 V	IN	"	"	"	1F to 1Y			"
			80		IN	2.7 V	GND	GND	OUT	"		Z.1 V	Z.1 V	IIN			"	2A to 2Y			"
		,	81		2.7 V	IN	GND	GND	001				1				-	2B to 2Y	,		
		,	82		GND	GND	IN	2.7 V	"	"			1				"	2C to 2Y			"
		,	83		GND	GND	2.7 V	IN	"	"			1				"	2D to 2Y			"
	L	l	03	l	טאט	GIND	Z.1 V	IIN	<u> </u>	L	<u> </u>	l	L	İ	L		<b>I</b>	2D 10 2 f			

See footnotes at end of table.

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TABLE III. Group A inspection for device type 01 – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0$  V, low  $\leq 0.7$  V, or open).

		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case 2 1/	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lim	nits	Unit
		,	Test no.	1A	2A	2B	2C	2D	2Y	GND	1Y	1D	1E	1F	1B	1C	V <sub>CC</sub>		Min	Max	
10	t <sub>PLH</sub>	3003	84	IN						GND	OUT	GND	GND	GND	2.7 V	2.7 V	5.0 V	1A to 1Y	3	30	ns
Tc = 125°C		Fig. 3	85	2.7 V						"	"	"	"	"	IN	2.7 V	"	1B to 1Y	"	"	"
		"	86	2.7 V						"	=	=	"		2.7 V	IN	"	1C to 1Y		"	"
		"	87	GND						=	=	IN	2.7 V	2.7 V	GND	GND	"	1D to 1Y	"		"
		"	88							"	=	2.7 V	IN	2.7 V		-	"	1E to 1Y		"	"
		"	89	"						=	=	2.7 V	2.7 V	IN	"	"	"	1F to 1Y	"		"
		"	90		IN	2.7 V	GND	GND	OUT	"							"	2A to 2Y	"	"	"
		"	91		2.7 V	IN	GND	GND	"	"							"	2B to 2Y	"	"	"
		"	92		GND	GND	IN	2.7 V	"	"							"	2C to 2Y	=	"	"
		"	93		GND	GND	2.7 V	IN	"								"	2D to 2Y	"		"
11	Same te	ests, termina	conditions	and limits	as for sub	group 10	except T	c = -55° C	).												

See footnotes at end of table.

TABLE III. Group A inspection for device type 02. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

					I e	rminai co	nditions	(pins no	t designa	itea may	be high	≥ 2.0 V,	$10W \le 0$ .	/v, or o	pen).						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case 2 1/	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lir	nits	Unit
			Test no.	Α	В	С	D	Е	Υ	GND	NC	F	G	Н	ı	J	$V_{CC}$		Min	Max	
1	V <sub>OH</sub>	3006	1	0.7 V	5.5 V	0.7 V	5.5 V	5.5 V	-400 μA	GND		5.5 V	5.5 V	0.7 V	0.7 V	5.5 V	4.5 V	Υ	2.5		V
Tc = 25°C		"	2	5.5 V	0.7 V	5.5 V	0.7 V	5.5 V	"	-		5.5 V	0.7 V	5.5 V	5.5 V	0.7 V	"	"	=		"
1		"	3	5.5 V	0.7 V	5.5 V	5.5 V	0.7 V	"	"		0.7 V	5.5 V	5.5 V	5.5 V	0.7 V	"	"	"		íí
	Vol	3007	4	GND	GND	2.0 V	2.0 V	2.0 V	4 mA	"		GND	GND	GND	GND	GND	"	"		0.4	"
1	0.2	"	5	2.0 V	2.0 V	GND	GND	GND	"	"		"	"	"	GND	GND	"	"		"	"
1		"	6	GND	GND	"	"	"	"	"		"	"	"	2.0 V	2.0 V	"	"		"	íí
1		"	7	GND	GND	"	"	"	"	"		2.0 V	2.0 V	2.0 V	GND	GND	"	"		"	íí
	V <sub>IC</sub>		8	-18 mA						"							"	Α		-1.5	"
			9		-18 mA					"							"	В		"	"
1			10			-18 mA				"							"	С		"	íí
1			11				-18 mA			"							"	D		"	"
1			12					-18 mA		"							"	E		"	"
1			13							"		-18 mA					"	F		"	"
1			14							"			-18 mA				"	G		"	"
1			15							"				-18 mA			"	Н		"	íí
1			16							"					-18 mA		"	ı		"	íí
1			17							"						-18 mA	"	J		"	íí
	IIL	3009	18	0.4 V	5.5 V	5.5 V	5.5 V	5.5 V		u		5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	A	2/	2/	μА
1	<u>2</u> /	"	19	5.5 V	0.4 V	5.5 V	"	"		íí.		"	"	"	"	"	"	В	-	-	"
1		"	20	"	5.5 V	0.4 V	"	"		íí.		"	"	"	"	"	"	C	"	66	66
		"	21	"	"	5.5 V	0.4 V	"		"		"	"	"	"	"		D	"	"	"
1		"	22	"	"	"	5.5 V	0.4 V		"		"	"	"	"	"		E	"	"	"
1		"	23	"	"	"	"	5.5 V		"		0.4 V	"	"	"	"	"	F	"	"	"
1		"	24	"	"	"	"	"		"		5.5 V	0.4 V	"	"		"	G	"	"	"
1		"	25	"	"	"	"	"		"		"	5.5 V	0.4 V	"		"	Н	"	"	"
1		"	26	"	"	"	"	"		"		"	"	5.5 V	0.4 V		"	1	"	"	"
1		"	27	"	"	"	"	"		"		"	"	"	5.5 V	0.4 V	"	J	"	"	"
1	I <sub>IH1</sub>	3010	28	2.7 V	GND	GND	GND	GND		"		GND	GND	GND	GND	GND	"	A		20	"
1		"	29	GND	2.7 V	GND	"	"		"		"	"	"	"	"	"	В		"	"
1		"	30	"	GND	2.7 V	"	"		"		"	"	"	"	"	"	С		"	íí
1		"	31	"	"	GND	2.7 V	"		"		"	"	"	"	"	"	D		"	íí
		"	32	"	"	"	GND	2.7 V		"		"	"	"	"	"	"	E		"	íí
		"	33	"	"	"	"	GND		"		2.7 V	"	"	"	"	"	F		"	"
		"	34	"	"	"	"	"		"		GND	2.7 V	"	"	"	"	G		"	"
		"	35	"	"	"	"	"		"		"	GND	2.7 V	"	"	"	Н		"	cc cc
		"	36	"	"	"	"	"		ű		"	"	GND	2.7 V	"	"	I		"	cc cc
		"	37	"	"	"	"	"		"		"	"	"	GND	2.7 V	"	J		"	"
	I <sub>IH2</sub>	"	38	5.5 V	"	"	"	"		ű		"	"	"	"	GND	"	Α		100	cc cc
		"	39	GND	5.5 V	"	"	"		u		"	"	"	"	"	"	В		"	"
		"	40	"	GND	5.5 V	"	"		ű		"	"	"	"	"	"	С		"	"
		"	41	"	"	GND	5.5 V	"		ű		"	"	"	"	"	"	D		"	cc cc
		"	42	"	"	"	GND	5.5 V		ű		"	"	"	"	"	"	E		"	er.
		"	43	"	"	"	"	GND		"		5.5 V	"	"	"	"	"	F		"	"
		"	44	"	"	"	"	"		"		GND	5.5 V	"	"	"	"	G		"	"
[		"	45	"	"	"	"	"		"		"	GND	5.5 V	"	"	"	Н		"	"
1		"	46	"	"	"	"	"		"		"	"	GND	5.5 V	"	"	I		"	"
			47					-				<del> </del>			GND	5.5 V	.,	J			"

See footnotes at end of table.

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TABLE III. <u>Group A inspection for device type 02</u> – Continued. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

					ı er	minai co	naitions	(pins no	t designa	ated may	be nign	≥ 2.0 V,	$10W \le 0$ .	7 V, or o	pen).						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	Α	В	С	D	Е	Υ	GND	NC	F	G	Н	ı	J	Vcc		Min	Max	
1 Tc = 25°C	los	3011	48	GND	GND	GND	GND	GND	GND	GND		GND	GND	GND	GND	GND	5.5 V	Y	-15	-100	mA
	Іссн	3005	49	GND	GND	GND	GND	GND		"		GND	GND	GND	GND	GND	5.5 V	Vcc		1.6	"
	Iccl	3005	50	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V		"		5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	Vcc		2.0	"
2	Same te	ests, terminal	conditions a	and limits a	as for sub	group 1, e	xcept T <sub>C</sub>	= +125° C	, and V 10	tests are	omitted.										
3	Same te	ests, terminal	conditions a	and limits a	as for sub	group 1, e	xcept T <sub>C</sub>	= -55° C,	and V <sub>IC</sub>	tests are c	mitted.										
9	t <sub>PHL</sub>	3003	51	IN	2.7 V	GND	GND	GND	OUT	GND		GND	GND	GND	GND	GND	5.0 V	A to Y	3	22	ns
Tc = 25°C		Fig. 3	52	2.7 V	IN	GND	GND	GND	"	"		"	"	"	"	"	"	B to Y	**	££	"
		"	53	GND	GND	IN	2.7 V	2.7 V	"	"		"	"	"	"	"	"	C to Y	66	"	íí.
		"	54	"	"	2.7 V	IN	2.7 V	"	"		"	"	"	"	"	"	D to Y	66	££	"
		"	55	"	"	2.7 V	2.7 V	IN	"	"		"	"	"	"	"	"	E to Y	66	££	"
		"	56	"	"	GND	GND	GND	"	"		IN	2.7 V	2.7 V	"	"	"	F to Y	66	££	"
		"	57	"	"	=	"	-	"	"		2.7 V	IN	2.7 V	"	"	"	G to Y	"	"	"
		"	58	"	"	"	"	"	"	"		2.7 V	2.7 V	IN	"	"	"	H to Y	"	"	"
		"	59	"	"	"	=	-	"	"		GND	GND	GND	IN	2.7 V	"	I to Y	"	"	"
		"	60	"	"	"	"	"	"	"		"	"	"	2.7 V	IN	"	J to Y	"	"	"
	t <sub>PLH</sub>	"	61	IN	2.7 V	"	=	"	"	"		"	"	"	GND	GND	"	A to Y	"	22 <u>3</u> /	"
		"	62	2.7 V	IN	"	"	"	"	"		"	"	"	"	"	"	B to Y	66	££	"
		"	63	GND	GND	IN	2.7 V	2.7 V	"	"		"	"	"	"	"	"	C to Y	"	"	"
		"	64	"	"	2.7 V	IN	2.7 V	"	"		"	"	"	"	"	"	D to Y	"	"	"
		"	65	"	"	2.7 V	2.7 V	IN	"	"		"	"	"	"	"	"	E to Y	"	"	"
		"	66	"	"	GND	GND	GND	"	"		IN	2.7 V	2.7 V	"	"	"	F to Y	"	"	"
		"	67	"	"	"	=	"	"	"		2.7 V	IN	2.7 V	"	"	"	G to Y	"	"	"
		"	68	"	"	"	"	"	"	"		2.7 V	2.7 V	IN	"	"	"	H to Y	"	"	"
			69	"	"	"	=	-	"	"		GND	GND	GND	IN	2.7 V	"	I to Y	"	"	"
			70	"	"	=	"	-	"	"		"	"	"	2.7 V	IN	"	J to Y	"	"	"
10	t <sub>PHL</sub>		71	IN	2.7 V	=	=	-	"			"	"	"	GND	GND	"	A to Y	"	30	"
Tc = 125°C		"	72	2.7 V	IN		"	"	"	u		"	"	"	"	"	"	B to Y	66	££	"
		"	73	GND	GND	IN	2.7 V	2.7 V	"	"		"	"	"	"	"	"	C to Y	"	"	"
		"	74	"	"	2.7 V	IN	2.7 V	"	"		"	"	"	"	"	"	D to Y	"	"	"
		"	75	"	"	2.7 V	2.7 V	IN	"	"		"	"	"		"	"	E to Y	66	"	"
		"	76	"	"	GND	GND	GND	"	"		IN	2.7 V	2.7 V	"	"	"	F to Y	66	"	"
		"	77	"	"	=	-		"	"		2.7 V	IN	2.7 V		"	"	G to Y	66	"	"
		"	78	"	"	"	"	"	"	"		2.7 V	2.7 V	IN	"	"	"	H to Y	"	"	"
		"	79	"	"	"	"	"	"	"		GND	GND	GND	IN	2.7 V	и	I to Y	66	"	"
		"	80	"	"	=	"	"	"	"		"	"	"	2.7 V	IN	"	J to Y	"	"	"

See footnotes at end of table.

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TABLE III. <u>Group A inspection for device type 02</u> – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0 \text{ V}$ , low  $\leq 0.7 \text{ V}$ , or open).

		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case 2 1/	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	Α	В	С	D	Е	Υ	GND	NC	F	G	Н		J	V <sub>CC</sub>		Min	Max	
10	t <sub>PLH</sub>	3003	81	IN	2.7 V	GND	GND	GND	OUT	GND		GND	GND	GND	GND	GND	5.0 V	A to Y	3	30	ns
Tc = 125°C		Fig. 3	82	2.7 V	IN	GND	GND	GND	=	"		=		"	-	"	"	B to Y	"	"	"
		"	83	GND	GND	IN	2.7 V	2.7 V	=	"		=	"			-	"	C to Y	"	tt.	"
		"	84	"		2.7 V	IN	2.7 V		"			"	"	"		"	D to Y	"	"	"
		"	85	-	"	2.7 V	2.7 V	IN	"	"		"	=	"	=	"	"	E to Y	"	"	"
		"	86	"		GND	GND	GND		"		IN	2.7 V	2.7 V			"	F to Y	"	"	"
		"	87	"	"	=	"	"	=	"		2.7 V	IN	2.7 V	"	"	"	G to Y	"	"	"
		"	88	"	"	=	"	"	=	"		2.7 V	2.7 V	IN	"	"	"	H to Y	"		"
		"	89	"	"	"	=	=	"	"		GND	GND	GND	IN	2.7 V	"	I to Y	"	"	"
		"	90	"	"	=	"	"	=	=		=	-	=	2.7 V	IN	"	J to Y	"	=	"

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- $\underline{1}/$  Case 2 pins not referenced are NC.  $\underline{2}/$  I<sub>IL</sub> limits are as follows:

	Min/Max limits in mA for circuit								
Parameters	Α	В	С	D	E	F			
I <sub>IL</sub>	-120/-360	-30/-300	-150/-380	-120/-360	-105/-345	-150/-380			

 $\underline{3}/\ t_{\text{PLH}}$  max limit for CKT C is 25 ns.

#### 5. PACKAGING

5.1 <u>Packaging requirements.</u> For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

### 6. NOTES

(This section contains information of a general or explanatory nature which may be helpful, but is not mandatory.)

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
  - 6.2 Acquisition requirements. Acquisition documents should specify the following:
    - a. Title, number, and date of the specification.
    - b. Complete part number (see 1.2).
    - c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
    - d. Requirements for certificate of compliance, if applicable.
    - e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
    - Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
    - g. Requirements for product assurance options.
    - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
    - j. Requirements for "JAN" marking.
- 6.3 <u>Superseding information</u>. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.
- 6.4 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 <u>Abbreviations, symbols, and definitions.</u> The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential
I <sub>IN</sub>	Current flowing into an input terminal
V <sub>IN</sub>	Voltage level at an input terminal

- 6.6 <u>Logistic support.</u> Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming shall not affect the part number.
- 6.7 <u>Substitutability.</u> The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device	Generic-industry
type	type
01	54LS51
02	54LS54

6.8 <u>Manufacturers' designation.</u> Manufacturers' circuits which form a part of this specification are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturers' designations.

	Circuits								
Device	Α	В	С	D	Е	F			
type	Texas Instruments	Signetics Corp.	National Semiconductor Corp.	Raytheon Co.	Fairchild Semiconductor	Motorola Inc.			
01	Х	Х	X	Х	Х	Х			
02	X	X	X	X	X	X			

6.9 <u>Changes from previous issue.</u> Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians:

Army - CR

Navy - EC

Air Force - 11

DLA - CC

Preparing activity: DLA - CC

(Project 5962-1953)

Review activities:

Army - HD, MI, SM

Navy - AS, CG, MC, SH, TD

Air Force - 03, 19, 99

## STANDARDIZATION DOCUMENT IMPROVEMENT PROPOSAL

## INSTRUCTIONS

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3. DOCUMENT TITLE		•
MICROCIRCUITS, DIGITAL, BIPOLA	AR LOW-POWER SCHOTTKY TTL, AND	O-OR-INVERT GATES, MONOLITHIC SILICON
4. NATURE OF CHANGE (Identify paragrap	oh number and include proposed rewrite, if pos	sible. Attach extra sheets as needed.)
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