INCH-POUND

MIL-M-38510/313B

23 May 2003

SUPERSEDING

MIL-M-38510/313A

22 July 1983

## MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR LOW-POWER SCHOTTKY TTL, SCHMITT TRIGGER POSITIVE NAND GATES AND INVERTERS, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

# 1. SCOPE

- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, Schmitt trigger positive NAND gate and inverter microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).
  - 1.2 Part number. The part number should be in accordance with MIL-PRF-38535, and as specified herein.
  - 1.2.1 <u>Device types</u>. The device types should be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Dual 4 input, Schmitt trigger positive NAND gate
02	Hex Schmitt trigger inverter
03	Quad 2 input Schmitt trigger positive NAND gate

- 1.2.2 <u>Device class</u>. The device class should be the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 Case outlines. The case outlines should be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Α	GDFP5-F14 or CDFP6-F14	14	Flat pack
В	GDFP4-F14	14	Flat pack
С	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
Χ	CQCC2-N20	20	Square leadless chip carrier
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A <u>DISTRIBUTION STATEMENT A.</u> Approved for public release; distribution is unlimited.

## 1.3 Absolute maximum ratings.

Supply voltage range	-0.5 V dc to +7.0 V dc
Input voltage range	-1.5 V dc at -18 mA to +5.5 V dc
Storage temperature range	-65° to +150°C
Maximum power dissipation, (P <sub>D</sub> ) 1/	
Device type 01	38.5 mW dc
Device type 02	115.5 mW dc
Device type 03	
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction to case ( $\theta_{JC}$ ):	
Cases A, B, C, D, X, and 2	(See MIL-STD-1835)
Junction temperature (T <sub>J</sub> ) <u>2</u> /	+175°C

## 1.4 Recommended operating conditions.

Supply voltage (V <sub>CC</sub> )	+4.5 V dc minimum to +5.5 V dc
	maximum
Minimum high level input voltage (V <sub>IH</sub> )	+1.4 V dc
Maximum low level input voltage (V <sub>IL</sub> )	+1.0 V dc
Normalized fanout (each output) 3/	10 maximum
Case operating temperature range (T <sub>C</sub> )	-55° to +125°C

## 2. APPLICABLE DOCUMENTS

## 2.1 Government documents.

2.1.1 <u>Specifications and Standards</u>. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

## **SPECIFICATION**

## **DEPARTMENT OF DEFENSE**

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

## **STANDARDS**

## **DEPARTMENT OF DEFENSE**

MIL-STD-883 - Test Method Standard for Microelectronics.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Unless otherwise indicated, copies of the above specifications and standards are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence.</u> In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

 $<sup>\</sup>underline{1}$ / Must withstand the added P<sub>D</sub> due to short-circuit test (e.g.,  $I_{OS}$ ).

<sup>2/</sup> Maximum junction temperature shall not be exceeded except in accordance with allowable short duration burn-in screening condition in accordance with MIL-PRF-38535.

<sup>3/</sup> The device shall fanout in both high and low levels to the specified number of inputs of the same device type as that being tested.

#### 3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
- 3.3.1 <u>Terminal connections and logic diagrams.</u> The terminal connections and logic diagrams shall be as specified on figure 1.
  - 3.3.2 Truth tables. The truth tables shall be as specified on figure 2.
- 3.3.3 <u>Schematic circuits</u>. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.
  - 3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.
  - 3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements.</u> The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.
  - 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 8 (see MIL-PRF-38535, appendix A).

# 4. VERIFICATION

- 4.1 <u>Sampling and inspection.</u> Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:
  - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
  - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
  - c. Additional screening for space level product shall be as specified in MIL-PRF-38535.

TABLE I. <u>Electrical performance characteristics</u>.

Test	Symbol	Conditions	Device	Li	mits	Unit
		-55°C ≤ T <sub>C</sub> ≤ +125°C unless otherwise specified	types	Min	Max	
Positive going threshold voltage	V <sub>T+</sub>	V <sub>CC</sub> = +5.0 V	All	1.4	1.9	V
Negative going threshold voltage	V <sub>T</sub> -	V <sub>CC</sub> = +5.0 V	All	0.5	1.0	V
Hysteresis	Н	V <sub>CC</sub> = +5.0 V	All	0.4	1.4	V
High level output voltage	V <sub>OH1</sub>	$V_{CC} = +4.5 \text{ V}, I_{OH} =4 \text{ mA},$ $V_{IN} = +0.5 \text{ V}$	All	2.5		V
Low level output voltage	V <sub>OL1</sub>	$V_{CC} = +4.5 \text{ V}, I_{OL} = +4 \text{ mA},$ $V_{IN} = +1.9 \text{ V}$	All		0.4	V
High level output voltage	V <sub>OH2</sub>	$V_{CC} = +5.0 \text{ V}, I_{OH} =4 \text{ mA},$ $V_{IN} = +0.5 \text{ V}, \text{ then } +1.4 \text{ V}$	All	2.5		V
Low level output voltage	V <sub>OL2</sub>	$V_{CC} = +5.0 \text{ V}, I_{OL} = +4 \text{ mA},$ $V_{IN} = +1.9 \text{ V}, \text{ then } +1.0 \text{ V}$	All		0.4	V
Input clamp voltage	V <sub>IC</sub>	$V_{CC} = +4.5 \text{ V}, I_{IN} = -18 \text{ mA},$ $T_{C} = +25^{\circ}\text{C}$	All		-1.5	V
High level input current	I <sub>IH1</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +2.7 \text{ V}$	All		20	μА
	I <sub>IH2</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IH</sub> = +5.5 V	All		100	μΑ
Low level input current 1/	I <sub>IL</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = +0.4 V	All	12	36	mA
Short circuit output current	los	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = GND <u>2</u> /	All	-15	-100	mA
High level supply	I <sub>CCH</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = \text{GND}$	01		6.0	mA
current (total)			02		16.0	
			03		11.0	
Low level supply	I <sub>CCL</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = 5.5 \text{ V}$	01		7.0	mA
current (total)			02		21.0	
<u> </u>			03		14.0	
Propagation delay time high to low level	t <sub>PHL</sub>	$V_{CC} = +5.0 \text{ V},$ $R_L = 2 \text{ k}\Omega \pm 10\%$	All	5	52	ns
Propagation delay time low to high level	t <sub>PLH</sub>	$C_L = 50 \text{ pF} \pm 10\%,$	All	5	52	ns

 $<sup>\</sup>underline{1}/\,$  For CKT E, device type 03,  $I_{IL}$  limits are -.17 mA min and -.41 mA max.

 $<sup>\</sup>underline{2}$ / Not more than one output should be shorted at a time.

TABLE II. Electrical test requirements.

	Subgroups (	see table III)
MIL-PRF-38535	Class S	Class B
test requirements	devices	devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 9	1*, 2, 3, 9
Group A test requirements	1, 2, 3,	1, 2, 3, 9,
	9, 10, 11	10, 11
Group B electrical test parameters	1, 2, 3,	N/A
when using the method 5005 QCI option	9, 10, 11	
Group C end-point electrical parameters	1, 2, 3,	1, 2, 3
	9, 10, 11	
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

<sup>\*</sup>PDA applies to subgroup 1.

- 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.
- 4.4 <u>Technology Conformance inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
- 4.4.1 <u>Group A inspection.</u> Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
  - a. Tests shall be as specified in table II herein.
  - b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.
  - 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.
- 4.4.3 <u>Group C inspection.</u> Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
  - 4.5 Methods of inspection. Methods of inspection shall be specified and as follows.
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

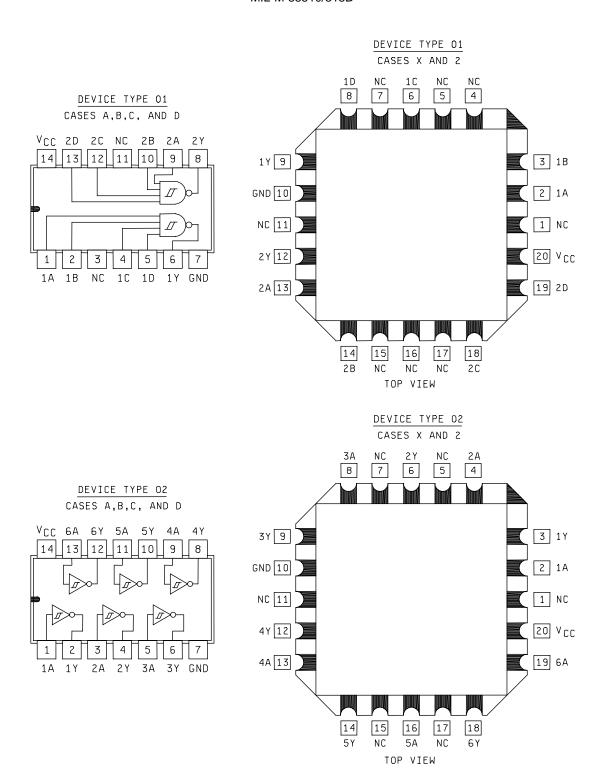


FIGURE 1. Terminal connections and logic diagrams.

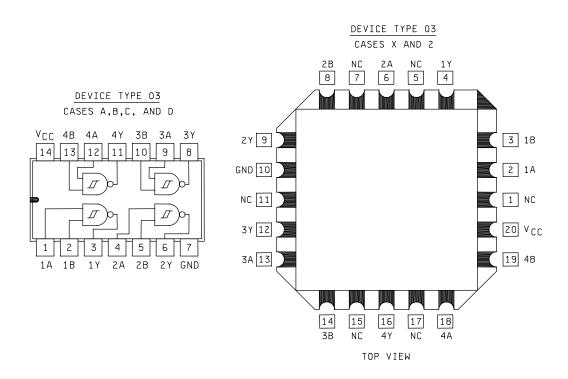


FIGURE 1. Terminal connections and logic diagrams - Continued.

Device type 01

		Truth	table	
	Inp	out		Output
Α	В	C	D	Υ
L	L	L	L	Н
Н	L	L	L	Н
L	Н	L	L	Н
Н	Н	L	L	Н
L	L	Н	L	Н
Н	L	Н	L	Н
L	Н	Н	L	Н
Н	Н	Н	L	Н
L	L	L	Н	Н
Н	L	L	Н	Н
L	Н	L	Н	Н
Н	Н	L	Н	Н
L	L	Н	Н	Н
Н	L	Н	Н	Н
L	Н	Н	Н	Н
Н	Н	Н	Н	L

Positive logic  $Y = \overline{ABCD}$ 

Device type 02

Truth table each gate												
Input	Output											
Α	Υ											
L	Н											
Н	L											

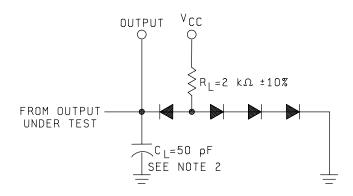
Positive logic  $Y = \overline{A}$ 

Device type 03

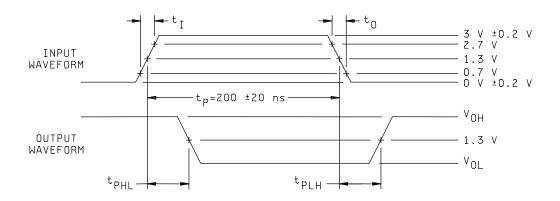
Truth	table	each gate
Inp	ut	Output
Α	В	Υ
L	L	Н
Н	L	Н
L	Н	Н
Н	Н	L

Positive logic  $Y = \overline{AB}$ 

FIGURE 2. Truth tables and logic equations.



LOAD FOR OUTPUT UNDER TEST



# NOTES:

- 1. Pulse generator characteristics: PRR  $\leq$  1 MHz,  $t_{I} \leq$  15 ns,  $t_{O} \leq$  6 ns, and  $Z_{OUT} \approx 50 \Omega$ .
- 2.  $C_L = 50 \ pF \pm 10\%$  including probe and jig capacitance. All diodes are 1N3064 or equivalent.

FIGURE 3. Switching time test circuit and waveforms.

TABLE III. Group A inspection for device type 01. 1/

		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Cases X, 2 <u>2</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal		nits	Unit
			Test no.	1A	1B	NC	1C	1D	1Y	GND	2Y	2A	2B	NC	2C	2D	$V_{CC}$		Min	Max	1
1	V <sub>OH1</sub>	3006	1	0.5 V	1.9 V		1.9 V	1.9 V	4 mA	GND							4.5 V	1Y	2.5		V
Tc = 25°C		"	2	1.9 V	0.5 V		1.9 V	-	"	"							"	1Y	"		"
		"	3	"	1.9 V		0.5 V	"	"	"							"	1Y	"		"
		"	4	"	1.9 V		1.9 V	0.5 V	"	"							"	1Y	"		"
		"	5							"	4 mA	0.5 V	1.9 V		1.9 V	1.9 V	"	2Y	"		"
		"	6							"	"	1.9 V	0.5 V		1.9 V	"	"	2Y	"		"
		"	7							"	"	"	1.9 V		0.5 V	"	"	2Y	"		"
		"	8							"	"	"	1.9 V		1.9 V	0.5 V	"	2Y	"		"
	V <sub>OL1</sub>	3007	9	1.9 V	1.9 V		1.9 V	1.9 V	4 mA	"							"	1Y		0.4	"
		3007	10							"	4 mA	1.9 V	1.9 V		1.9 V	1.9 V	"	2Y		0.4	"
	$V_{OH2}$	3006	11	3/	1.9 V		1.9 V	1.9 V	4 mA	"							5.0 V	1Y	2.5		"
		"	12	1.9 V	3/		1.9 V		"	"							"	1Y	"		"
		"	13	"	1.9 V		3/	"	"	"							"	1Y	"		"
		"	14	"	1.9 V		1.9 V	3/	"	"							"	1Y	"		"
		"	15							"	4 mA	3/	1.9 V		1.9 V	1.9 V	"	2Y	"		"
		"	16							"	"	1.9 V	3/		1.9 V	"	"	2Y	"		"
		"	17							"	"	"	1.9 V		3/	"	"	2Y	"		"
			18							"	"	"	1.9 V		1.9 V	3/	"	2Y	"		"
	V <sub>OL2</sub>	3007	19	4/	4/		4/	4/	4 mA	"							"	1Y		0.4	"
	OLZ	3007	20	_						"	4 mA	4/	4/		4/	4/	"	2Y		0.4	"
	V <sub>IC</sub>		21	-18 mA						"		_	_			_	4.5 V	1A		-1.5	"
	10		22		-18 mA					"							"	1B		"	"
			23				-18 mA			"							"	1C		"	"
			24					-18 mA		"							"	1D		"	"
			25							"		-18 mA					"	2A		"	"
			26							"			-18 mA				"	2B		"	"
			27							"					-18 mA		"	2C		"	"
			28							"						-18 mA	"	2D		"	"
	I <sub>IH1</sub>	3010	29	2.7 V	GND		GND	GND		"							5.5 V	1A		20	μΑ
	-1111	"	30	GND	2.7 V		GND	"		"							"	1B		"	"
		"	31	"	GND		2.7 V	"		"							"	1C		"	"
			32	"	GND		GND	2.7 V		"							"	1D		"	"
		"	33							"		2.7 V	GND		GND	GND	"	2A		"	"
		"	34							"		GND	2.7 V		GND	"	"	2B		"	"
		"	35							"		"	GND		2.7 V	"	"	2C		"	"
		"	36							"		"	GND		GND	2.7 V	"	2D			"
	I <sub>IH2</sub>	"	37	5.5 V	GND		GND	GND		"			0.10		0.15		"	1A		100	"
	·1H2	"	38	GND	5.5 V		GND	"		"							"	1B		. 50	"
		"	39	"	GND		5.5 V	"		"							"	1C			"
			40	"	GND		GND	5.5 V		"			1	1	<del>                                     </del>	1		1D			"
		"	41		0110		0.40	0.0 v		"		5.5 V	GND		GND	GND	"	2A		"	"
			42				<del>                                     </del>			"		GND	5.5 V	1	GND	"		2B			"
			43							"		"	GND		5.5 V	"	"	2C		"	"
			44				<b>-</b>			"		"	GND	-	GND	5.5 V	-	2D		"	"
L				i	l		L			L	L		GIVD	l	GIVD	J.J V	<u> </u>	20			

See footnotes at end of table.

 $\stackrel{\rightarrow}{\sim}$ 

TABLE III. Group A inspection for device type 01 – Continued. 1/

			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
		MIL-STD-	A,B,C,D																		in the second
Subgroup	Symbol	883	Cases	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured	Lim	its	Unit
		method	X, 2 <u>2</u> /															terminal			i
			Test no.	1A	1B	NC	1C	1D	1Y	GND	2Y	2A	2B	NC	2C	2D	$V_{CC}$		Min	Max	
1	LLL	3009	45	0.4 V	5.5 V		5.5 V	5.5 V		GND							5.5 V	1A	12	36	mΑ
Tc = 25°C		"	46	5.5 V	0.4 V		5.5 V	=		"							-	1B	"	•	"
		"	47		5.5 V		0.4 V	=		-							"	1C	"		-
		"	48		5.5 V		5.5 V	0.4 V		-							"	1D	"		"
		"	49							"		0.4 V	5.5 V		5.5 V	5.5 V	"	2A	"	"	"
		"	50							"		5.5 V	0.4 V		5.5 V	"	"	2B	"	"	"
		"	51							"		"	5.5 V		0.4 V	"	"	2C	"	"	"
		"	52							"		"	5.5 V		5.5 V	0.4 V	"	2D	"	"	"
	los	3011	53	GND	GND		GND	GND	GND	"							"	1Y	-15	-100	"
		3011	54							"	GND	GND	GND		GND	GND	"	2Y	-15	-100	
	I <sub>CCH</sub>	3005	55	GND	GND		GND	GND		"		GND	GND		GND	GND	"	V <sub>CC</sub>		6.0	
	I <sub>CCL</sub>	3005	56	5.5 V	5.5 V		5.5 V	5.5 V		íí		5.5 V	5.5 V		5.5 V	5.5 V	. "	$V_{CC}$		7.0	
2		ests, termina																			
3	Same to	ests, termina	l conditions	and limits	as for sul	bgroup 1,	except To	= -55°C a		ests are or	nitted.										
9	$t_{PHL}$	3003	57	IN	5.0 V		5.0 V	5.0 V	OUT	GND							5.0 V	1A to 1Y	5	32	ns
Tc = 25°C		Fig. 3	58							"	OUT	IN	5.0 V		5.0 V	5.0 V	"	2A to 2Y	"	**	"
	t <sub>PLH</sub>	"	59	IN	5.0 V		5.0 V	5.0 V	OUT	"							"	1A to1Y	"	"	"
		"	60							"	OUT	IN	5.0 V		5.0 V	5.0 V	"	2A to 2Y	"	"	=
10	t <sub>PHL</sub>	"	61	IN	5.0 V		5.0 V	5.0 V	OUT	"							"	1A to 1Y	"	52	"
Tc = 125°C		"	62							"	OUT	IN	5.0 V		5.0 V	5.0 V	"	2A to 2Y	"	"	**
	t <sub>PLH</sub>	"	63	IN	5.0 V		5.0 V	5.0 V	OUT	"							"	1A to1Y	"	"	"
	. 211	"	64							"	OUT	IN	5.0 V		5.0 V	5.0 V	"	2A to 2Y	"	"	"
11	Same to	ests, termina	l conditions	and limit	s as for su	ibaroup 1	0. except	Tc = -55°0	2.	ı							1				

 $<sup>\</sup>underline{1}/$  Terminal conditions (pins not designated may be H  $\geq$  1.4 V; L  $\leq$  1.0 V; or open).

<sup>2/</sup> Case X and 2 pins not referenced are NC.

<sup>3/</sup> Momentary 0.5 V, then 1.4 V without overshoot during test.

<sup>4/</sup> Momentary 1.9 V, then 1.0 V without undershoot during test.

TABLE III. Group A inspection for device type 02. 1/

		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol		Cases X, 2 <u>2</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	1A	1Y	2A	2Y	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	$V_{CC}$		Min	Max	i
1	V <sub>OH1</sub>	3006	1	0.5 V	4 mA					GND							4.5 V	1Y	2.5		V
Tc = 25°C	0	"	2			0.5 V	4 mA			"							"	2Y	"		"
1.0 20 0			3					0.5 V	4 mA	"							"	3Y	"		"
			4					0.0 1		"	4 mA	0.5 V						4Y			"
			5							"	.711171	0.0 V	4 mA	0.5 V			"	5Y			"
		"	6							"			<del></del> IIIA	0.5 V	4 mA	0.5 V	"	6Y	"		"
	V <sub>OL1</sub>	3007	7							"					4 mA	1.9 V	"	6Y		0.4 V	"
	V OL1	3007	8							"			4 mA	1.9 V	4 IIIA	1.9 V	"	5Y		U.4 V	"
			9							u	4 m A	1.9 V	4 IIIA	1.9 V			"	4Y			"
			10					1.9 V	4 mA	"	4 mA	1.9 V					"	3Y			"
			11			1.9 V	4 Λ	1.9 V	4 IIIA	"								2Y			
			12	4.0.1/	4 Λ	1.9 V	4 mA			"							"	1Y			"
	\ /		12	1.9 V	4 mA					-									0.5	-	
	$V_{OH2}$	3006	13	<u>3</u> /	4 mA	0/	4 4										5.0 V	1Y	2.5		V
			14			<u>3</u> /	4 mA	0/	4 4	"							"	2Y			"
			15					<u>3</u> /	4 mA	-								3Y			
			16							"	4 mA	<u>3</u> /					- "	4Y			
			17										4 mA	<u>3</u> /				5Y			
			18							"					4 mA	<u>3</u> /		6Y	"		
	$V_{OL2}$	3007	19												4 mA	<u>4</u> /	"	6Y		0.4 V	"
		"	20							"			4 mA	<u>4</u> /			"	5Y		"	"
		"	21							"	4 mA	<u>4</u> /					"	4Y		"	"
		"	22					<u>4</u> /	4 mA	"							"	3Y		"	"
		"	23			<u>4</u> /	4 mA			"							"	2Y		"	"
		"	24	<u>4</u> /	4 mA					"							"	1Y		"	"
	V <sub>IC</sub>		25	-18 mA						"							4.5 V	1A		-1.5	"
			26			-18 mA				"							"	2A		"	"
			27					-18 mA		"							"	3A			"
			28							"		-18 mA					"	4A		"	íí.
			29							"				-18 mA			-	5A		-	"
			30							"						-18 mA	-	6A			"
	$I_{\rm LH1}$	3010	31							"						2.7 V	5.5 V	6A		20	μΑ
		"	32							"				2.7 V			"	5A		"	"
		"	33							"		2.7 V					"	4A		"	cc cc
		"	34					2.7 V		"							"	3A		"	cc cc
		"	35			2.7 V				"							"	2A		"	tt.
		"	36	2.7 V						í,							"	1A		"	tt.
	I <sub>I H 2</sub>	"	37	5.5 V						í,							"	1A		100	tt.
		"	38			5.5 V				ıı							"	2A		"	í,
		"	39					5.5 V		íí							"	3A		"	"
		"	40							"		5.5 V					"	4A		"	tt.
		"	41							íí				5.5 V			"	5A		"	tt.
		"	42							u						5.5 V	"	6A		"	tt.
	IL	3009	43							"						0.4 V	"	6A	12	36	mA
	116	"	44							"				0.4 V		0.1 *	"	5A	"	.00	"
			45							í,		0.4 V		U.T V			"	4A	"	"	tt.
			46					0.4 V		í,		0. <del>+</del> v					"	3A	"	"	"
			47			0.4 V		U+ V		u							"	2A	"		"
			48	0.4 V		∪. <del>+</del> v				"							"	1A	"	"	"
L		-	48	U.4 V	l					L		l				<u> </u>		IΑ			

See footnotes at end of table.

TABLE III. Group A inspection for device type 02. 1/

			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
		MIL-STD-	A,B,C,D	-		4			_	40	40	40	4.4	40	40	40	00				
Subgroup	Symbol	883 method	Cases X, 2 2/	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lir	mits	Unit
			Test no.	1A	1Y	2A	2Y	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	$V_{CC}$		Min	Max	1
1	los	3011	49	GND	GND					GND							5.5 V	1Y	-15	-100	mA
Tc = 25°C		"	50			GND	GND			"							"	2Y	"	"	"
		"	51					GND	GND	"							"	3Y	"	"	"
		"	52							"	GND	GND					"	4Y	"	"	"
			53							"			GND	GND			"	5Y	- "	"	"
			54	0115		0115		0115		<del>- :</del>		0115		0115	GND	GND	- "	6Y			<del>- "-</del>
	I <sub>C CH</sub>	3005 3005	55 56	GND 5.5 V		GND		GND 5.5 V				GND 5.5 V		GND		GND	- "	Vcc		16	- "
	I <sub>C CL</sub>					5.5 V				<u> </u>		5.5 V		5.5 V		5.5 V		Vcc		21	
2		ests, terminal				<u> </u>															
3		ests, terminal				group 1, e	except I <sub>C</sub>	= -55° C,	and V <sub>IC</sub>		omitted.					1					т
9	t <sub>PHL</sub>	3003	57	IN	OUT					GND "							5.0 V	1A to 1Y	5	32	ns "
Tc = 25°C		Fig. 3	58			IN	OUT											2A to 2Y			
		"	59					IN	OUT	"							"	3A to 3Y	"	"	"
		"	60							"	OUT	IN					"	4A to 4Y	"	"	"
		"	61							"			OUT	IN			"	5A to 5Y	"	"	"
		"	62							"					OUT	IN	"	6A to 6Y	"	"	66
	t <sub>PLH</sub>	"	63	IN	OUT					"							"	1A to 1Y	"	"	66
		"	64			IN	OUT			"							"	2A to 2Y	"	"	66
		"	65					IN	OUT	"							"	3A to 3Y	"	"	"
		"	66							"	OUT	IN					"	4A to 4Y	"	"	"
		"	67							"			OUT	IN			"	5A to 5Y	"	"	66
		"	68							"					OUT	IN	"	6A to 6Y	"	"	66
10	t <sub>PHL</sub>	3003	57	IN	OUT					"							"	1A to 1Y	"	52	"
Tc = 125°C		Fig. 3	58			IN	OUT			"							"	2A to 2Y	"	"	"
		"	59					IN	OUT	"							"	3A to 3Y	"	"	"
		"	60							"	OUT	IN					"	4A to 4Y	"	"	66
		"	61							"			OUT	IN			"	5A to 5Y	"	"	"
		"	62							"					OUT	IN	"	6A to 6Y	66	"	66
	t <sub>PLH</sub>	"	63	IN	OUT					"							"	1A to 1Y	"	"	"
	YPLF	"	64			IN	OUT			"							"	2A to 2Y	66	66	66
		"	65					IN	OUT	"							"	3A to 3Y	"	"	"
		"	66					<u> </u>		"	OUT	IN					"	4A to 4Y	"	"	"
		"	67							"			OUT	IN			"	5A to 5Y	**	"	"
			68							"					OUT	IN	"	6A to 6Y	"	"	66
		l	00		1	l	l	l	1	1	1	l		l	001	11.4	1	3/11001	l		<u> </u>

 $<sup>\</sup>underline{1}$ / Terminal conditions (pins not designated may be H  $\geq$  1.4 V; L  $\leq$  1.0 V; or open).

Same tests, terminal conditions, and limits as for subgroup 10, except  $T_C = -55^{\circ}C$ .

3

<sup>2/</sup> Case X and 2 pins not referenced are NC.

 $<sup>\</sup>underline{3}\!/\!\!\!\!/$  Momentary 0.5 V, then 1.4 V without overshoot during test.

<sup>4/</sup> Momentary 1.9 V, then 1.0 V without undershoot during test.

TABLE III. Group A inspection for device type 03. 1/

		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Cases X, 2 <u>2</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lir	nits	Unit
			Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	V <sub>CC</sub>		Min	Max	
1	V <sub>OH1</sub>	3006	1	0.5 V	1.9 V	4 mA				GND							4.5 V	1Y	2.5		V
Tc = 25°C		"	2	1.9 V	0.5 V	4 mA				"							"	1Y	"		"
		"	3				0.5 V	1.9 V	4 mA	íí							"	2Y	"		"
		"	4				1.9 V	0.5 V	4 mA	"							"	2Y	"		"
		"	5							"	4 mA	0.5 V	1.9 V				"	3Y	"		"
		"	6							"	4 mA	1.9 V	0.5 V				"	3Y	"		"
		"	7							"		1.0 1	0.0 1	4 mA	0.5 V	1.9 V	"	4Y	"		"
		"	8							"				4 mA	1.9 V	0.5 V	"	4Y	"		"
	V <sub>OL1</sub>	3007	9							"				4 mA	1.9 V	1.9 V	"	4Y		0.4	"
	V OL1	3007	10							"	4 mA	1.9 V	1.9 V	7111/1	1.5 V	1.5 V	"	3Y		"	"
			11				1.9 V	1.9 V	4 mA		7 111/4	1.5 V	1.5 V					2Y			-
			12	1.9 V	1.9 V	4 mA	1.5 V	1.9 V	4 111/4									1Y			
	\/	3006	13	3/	1.9 V	4 mA											5.0 V	1Y	2.5		"
	$V_{OH2}$	3006	14	3/ 1.9 V	1.9 V	4 mA 4 mA			-	"	-		<del>                                     </del>				5.U V	1Y	Z.5 "	<b> </b>	-
				1.9 V	<u>3</u> /	4 MA	0/	4.0.1/	4 Λ	u											-
			15				<u>3</u> /	1.9 V	4 mA	"								2Y			
			16				1.9 V	<u>3</u> /	4 mA	- "	4 4	0/	4.0.1/				- "	2Y			"
			17							- "	4 mA	<u>3</u> /	1.9 V					3Y			<del>-</del>
-			18							"	4 mA	1.9 V	<u>3</u> /					3Y			
		"	19											4 mA	<u>3</u> /	1.9 V	"	4Y	"		"
		"	20							"				4 mA	1.9 V	<u>3</u> /	"	4Y	"		"
	$V_{OL2}$	3007	21							"				4 mA	<u>4</u> /	<u>4</u> /	"	4Y		0.4	"
		"	22							-	4 mA	<u>4</u> /	<u>4</u> /				"	3Y		"	"
		"	23				<u>4</u> /	<u>4</u> /	4 mA	"							"	2Y		"	"
		"	24	4/	4/	4 mA				"							"	1Y		"	"
	V <sub>IC</sub>		25	-18 mA						"							4.5 V	1A		-1.5	"
			26		-18 mA					"							"	1B		"	"
			27				-18 mA			"							"	2A		"	"
			28					-18 mA		"							"	2B		"	"
			29							"		-18 mA					"	3A		"	"
			30							"			-18 mA				"	3B		"	"
			31							"					-18 mA		"	4A		"	"
			32							"						-18 mA	"	4B		"	"
	I <sub>IH1</sub>	3010	33	2.7 V	GND					u							5.5 V	1A		20	μΑ
		"	34	GND	2.7 V					"							"	1B		"	"
		"	35				2.7 V	GND		"							"	2A		"	"
		"	36				GND	2.7 V		"							"	2B		"	"
			37				0110			"		2.7 V	GND				"	3A		"	"
			38							"		GND	2.7 V				"	3B		"	"
			39							u		OIND	Z.1 V		2.7 V	GND	"	4A		"	"
			40			1			1	"					GND	2.7 V		4A 4B		"	
			40	5.5 V	GND					"	-		-		GIND	Z./ V	-	1A		100	
	I <sub>IH2</sub>								-	"	-		<del>                                     </del>				-			100	-
			42	GND	5.5 V		E E \ /	CND		"			-					1B			-
			43				5.5 V	GND		"							-	2A		- "	
			44				GND	5.5 V		"		551	OND					2B		- "	<del>_</del>
			45									5.5 V	GND					3A			<del></del>
			46							"		GND	5.5 V					3B			
		"	47							"					5.5 V	GND		4A		"	"
		п	48							"					GND	5.5 V	"	4B		- "	

See footnotes at end of table.

TABLE III. Group A inspection for device type 03 – Continued. 1/

			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
		MIL-STD-	A,B,C,D																		i
Subgroup	Symbol	883	Cases	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured	Lim	its	Unit
"	,	method	X, 2 <u>2</u> /															terminal			i
			Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	V <sub>cc</sub>		Min	Max	i
1	I <sub>IL</sub>	3009	49	0.4 V	5.5 V					GND							5.5 V	1A	12	36	mA
Tc = 25°C	<u>5</u> /	"	50	5.5 V	0.4 V					"							"	1B	"	"	"
		"	51				0.4 V	5.5 V		=							"	2A	"	"	"
		"	52				5.5 V	0.4 V		"							"	2B	"	"	"
		"	53							=		0.4 V	5.5 V				"	3A	"	"	"
		"	54							"		5.5 V	0.4 V				"	3B	"	"	"
			55							-					0.4 V	5.5 V	"	4A	"	"	"
			56							-					5.5 V	0.4 V	- "	4B			
	los	3011	57	GND	GND	GND	0110	0115	0115	-							- "	1Y	-15	-100	
			58				GND	GND	GND	-	OND	OND	OND				"	2Y	- "		
			59 60							-	GND	GND	GND	GND	GND	GND	"	3Y 4Y			
		3005	61	GND	GND		GND	GND		"		GND	GND	GND	GND	GND	"		-	11.0	
	I <sub>CCH</sub>	3005	62	5.5 V	5.5 V		5.5 V	5.5 V		"		5.5 V	5.5 V		5.5 V	5.5 V	"	V <sub>CC</sub>		14.0	
2	I <sub>CCL</sub>	ests, termina				haroup 1			` and \/	tooto oro	omittad	3.5 V	3.3 V		5.5 V	5.5 V		v <sub>CC</sub>		14.0	
3		ests, termina																			
9		3003	63	IN	5.0 V	OUT	ехсері т	= -33 C i	and vic te	GND	milleu.						5.0 V	10 40 11/	5	32	
_	t <sub>PHL</sub>			IIN	5.0 V	001	IN	501/	OUT	GND "							5.U V	1A to 1Y	<u> </u>	32	ns "
Tc = 25°C		Fig. 3	64				IIN	5.0 V	001	"								2A to 2Y	-		
			65							-	OUT	IN	5.0 V					3A to 3Y			-
			66											OUT	IN	5.0 V	"	4A to 4Y		"	
	t <sub>PLH</sub>	"	67	IN	5.0 V	OUT				"							"	1A to 1Y	"	"	"
		"	68				IN	5.0 V	OUT	"							"	2A to 2Y	"	"	"
		"	69							"	OUT	IN	5.0 V				"	3A to 3Y	"	"	"
		"	70							-				OUT	IN	5.0 V	"	4A to 4Y	"	"	
10	t <sub>PHL</sub>	"	71	IN	5.0 V	OUT				"							"	1A to 1Y	"	52	"
Tc = 125°C		"	72				IN	5.0 V	OUT	"							"	2A to 2Y	"	"	"
		"	73							"	OUT	IN	5.0 V				"	3A to 3Y	"	"	"
		"	74							"				OUT	IN	5.0 V	"	4A to 4Y	"	"	"
	t <sub>PLH</sub>	"	75	IN	5.0 V	OUT				"							"	1A to 1Y	"	"	"
	YPLH	"	76		0.0 1		IN	5.0 V	OUT	"							"	2A to 2Y	"	66	"
		"	77					0.0 V		"	OUT	IN	5.0 V				"	3A to 3Y	"	"	"
		"	78							"			0.0 v	OUT	IN	5.0 V	"	4A to 4Y	"	"	"
11	Same te	ests, termina		, and limit	s as for su	ıbgroup 1	0, except	T <sub>C</sub> = -55°0	).			1	ı			0.0 1	1			l l	

- $\underline{1}/$  Terminal conditions (pins not designated may be H  $\geq$  1.4 V; L  $\leq$  1.0 V; or open).
- 2/ Case X and 2 pins not referenced are NC.
- $\underline{3}\!/$  Momentary 0.5 V, then 1.4 V without overshoot during test.
- 4/ Momentary 1.9 V, then 1.0 V without undershoot during test.
- $\underline{5}/$  For CKT E  $I_{\rm IL}$  limits are as follows, -0.17 to -0.41 mA.

## 5. PACKAGING

5.1 <u>Packaging requirements.</u> For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

## 6. NOTES

(This section contains information of a general or explanatory nature which may be helpful, but is not mandatory.)

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
  - 6.2 Acquisition requirements. Acquisition documents should specify the following:
    - a. Title, number, and date of the specification.
    - b. Complete part number (see 1.2).
    - c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
    - d. Requirements for certificate of compliance, if applicable.
    - e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
    - Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
    - g. Requirements for product assurance options.
    - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
    - j. Requirements for "JAN" marking.
- 6.3 <u>Superseding information</u>. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.
- 6.4 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential
I <sub>IN</sub>	Current flowing into an input terminal
V <sub>IN</sub>	Voltage level at an input terminal

- 6.6 Logistic support. Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.
- 6.7 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device	Generic-industry
type	type
01	54LS13
02	54LS14
03	54LS132

6.8 Manufacturers' designation. Manufacturers' circuits which form a part of this specification are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturers' designations.

	Circuits										
Device	Α	В	С	D	Е						
type	Texas Instruments	Signetics Corp.	Motorola Inc.	Raytheon Co.	National Semiconductor Corp.						
01	Х	Х	Χ	Х							
02	X	Х	Χ	X	X						
03	X	Χ	Χ	X	X						

6.9 Changes from previous issue. Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians:

Army - CR

Air Force - 11

Navy - EC DLA - CC

Preparing activity: DLA - CC

(Project 5962-1962)

Review activities:

Army - MI, SM

Navy - AS, CG, MC, SH, TD

Air Force - 03, 19, 99

# STANDARDIZATION DOCUMENT IMPROVEMENT PROPOSAL

# INSTRUCTIONS

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	tual requirements.	institute of imply author	ization to waive any po	THOM OF THE TELETERIC	ed document(s) of to amend
IREC	OMMEND A CHANGE:	1. DOCUMENT N MIL-M-38510			T DATE (YYYYMMDD) 3-05-23
MICF	JMENT TITLE ROCIRCUITS, DIGITAL, LOW-PO RTER, MONOLITHIC SILICON	OWER SCHOTTKY	TTL, SCHMITT TRIC	GGER POSITIVE	NAND GATE AND
4. NATU	RE OF CHANGE (Identify paragrap)	h number and include բ	proposed rewrite, if pos	sible. Attach extra s	rheets as needed.)
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