INCH-POUND

MIL-M-38510/316E

14 July 2003

SUPERSEDING

MIL-M-38510/316D

10 December 1987

### MILITARY SPECIFICATION

# MICROCIRCUITS, DIGITAL, BIPOLAR, LOW-POWER SCHOTTKY TTL, CASCADABLE LATCHES, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

### 1. SCOPE

- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, latches. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).
  - 1.2 Part number. The part number should be in accordance with MIL-PRF-38535, and as specified herein.
  - 1.2.1 <u>Device types</u>. The device types should be as follows:

Device type	<u>Circuit</u>
01	4 - bit cascadable bistable latch
02	Quad cascadable $\overline{S} - \overline{R}$ latch
03	8 - bit cascadable addressable latch
04	4 - bit cascadable bistable latch
05	8 - bit cascadable addressable latch

- 1.2.2 <u>Device class</u>. The device class should be the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 Case outlines. The case outlines should be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Е	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack
Χ	CQCC2-N20	20	Square leadless chip carrier
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

# 1.3 Absolute maximum ratings.

Supply voltage range Input voltage range Storage temperature range Maximum power dissipation ( $P_D$ ) $1/$ Device types 01 and 04 Device type 02 Device type 03 Lead temperature (soldering, 10 seconds) Thermal resistance, junction to case ( $\theta_{JC}$ ):	-1.5 V at -18 mA to 5.5 V -65° to +150°C 66 mW 38.5 mW 198 mW
Cases E, F, X, and 2	(See MIL-STD-1835)
Junction temperature (T <sub>J</sub> ) <u>2</u> /	+175°C
1.4 Recommended operating conditions.	
Supply voltage (V <sub>CC</sub> )	
Minimum high level input voltage (V <sub>IH</sub> )	
Maximum low level input voltage (V <sub>IL</sub> )	
Normalized fanout (each output)	
Case operating temperature range (T <sub>C</sub> )	-55°C to +125°C
Setup time, t <sub>(SETUP)</sub> :	
Data to enable: Device types 01 and 04	20 na minimum
**	20 ns minimum
Data to enable ↑:	
Device type 03	
Device type 05	24 ns minumum
Address to enable ↓:	
Device type 03	
Device type 05	0 ns minimum
Input hold time, t <sub>(HOLD)</sub> :  Data to enable:	
Device type 01 and 04	o ns minimum
Data to enable ↑:	
Device type 03	
Device type 05	0 ns minimum
Address to enable ↓:	
Device type 03	
Device type 05	0 ns minimum

NOTE: Refers to rising  $\uparrow$  or falling  $\downarrow$  edge of the enable pulse.

<sup>1/</sup> Must withstand the added P<sub>D</sub> due to short-circuit test (e.g., I<sub>OS</sub>).
2/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening conditions in accordance with MIL-PRF-38535.

#### 2. APPLICABLE DOCUMENTS

### 2.1 Government documents.

2.1.1 <u>Specifications and Standards</u>. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

### **SPECIFICATION**

### DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

#### **STANDARDS**

### DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard for Microelectronics.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Unless otherwise indicated, copies of the above specifications and standards are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence.</u> In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### 3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
  - 3.3.1 Terminal connections. The terminal connections shall be as specified on figure 1.
  - 3.3.2 Truth table. The truth table shall be as specified on figure 2.
- 3.3.3 <u>Schematic circuits</u>. The schematic circuits shall be\_maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.
  - 3.3.4 <u>Case outlines.</u> The case outlines shall be as specified in 1.2.3.

- 3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements</u>. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.
  - 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 10 (see MIL-PRF-38535, appendix A).

#### 4. VERIFICATION

- 4.1 <u>Sampling and inspection.</u> Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:
  - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
  - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
  - c. Additional screening for space level product shall be as specified in MIL-PRF-38535, appendix B.
  - 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.
- 4.4 <u>Technology Conformance Inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
  - 4.4.1 Group A inspection. Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
    - a. Tests shall be as specified in table II herein.
    - b. Subgroups 4, 5, and 6 shall be omitted.

TABLE I. <u>Electrical performance characteristics</u>.

Test	Symbol	Conditions	Device	Lim	its	Unit
		-55°C ≤ T <sub>C</sub> ≤ +125°C	type	Min	Max	
High level output voltage	V <sub>OH</sub>	$V_{CC} = 4.5 \text{ V}, V_{IH} = 2.0 \text{ V}$	All	2.5		V
		$V_{IL} = 0.7 \text{ V}, I_{OH} = -400 \mu\text{A}$				
Low level output voltage	VoL	V <sub>CC</sub> = 4.5 V, V <sub>IH</sub> = 2.0 V	All		0.4	V
		$V_{IL} = 0.7 \text{ V}, I_{OL} = 4 \text{ mA}$				
Input clamp voltage	VIC	$V_{CC} = 4.5 \text{ V}, I_{IN} = -18 \text{ mA},$	All		-1.5	V
		T <sub>C</sub> = +25°C				
Low level input current:	I <sub>IL1</sub>	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V}$	01	03	-0.42	mA
At data	_		04	0005	-0.4	
At $\overline{S}$ , $\overline{R}$			02	0	-0.4	
At all inputs			03, 05	005	-0.72	
Low level input current	I <sub>IL2</sub>	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V}$	01	06	-1.6	mA
enable .			04	0	-1.6	
High level input current:	I <sub>IH1</sub>	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.7 \text{ V}$	01, 04		20	μΑ
At data						•
${At\;S\;R}$			02		20	
At all inputs	<u> </u>		03, 05		20	
High level input current	I <sub>IH2</sub>	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.7 \text{ V}$	01, 04		80	μА
enable	יוחב	700 = 3.3 T, T <sub>IIV</sub> = 2.11 T	01, 01		00	μιτ
High level input current:	I <sub>IH3</sub>	$V_{CC} = 5.5 \text{ V}. V_{IN} = 5.5 \text{ V}$	01, 04		100	μΑ
At data	-1115		0.,0.		.00	μι
At $\overline{S}$ , $\overline{R}$	1		02		100	
	1					
At all inputs		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	03, 05		100	•
High level input current	I <sub>IH4</sub>	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V}$	01, 04		400	μΑ
enable  Short circuit output ourrent	1	V <sub>CC</sub> = 5.5 V 1/	All	-15	-100	mΛ
Short circuit output current	los	V <sub>CC</sub> = 5.5 V <u>I</u> /	All	-15	-100	mA
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 5.5 V	01, 04		12	mA
очрріу сипені	icc	VCC = 3.3 V	02		7	ША
			03, 05		36	
Low to high level, from	t <sub>PLH1</sub>	$C_L = 50 \text{ pF} \pm 10\%, R_L = 2 \text{ k}\Omega \pm 5\%$	01, 04	3	42	ns
D input to Q output	YFLITI	$V_{CC} = 5.0 \text{ V}$	01, 01		'-	110
High to low level, from	t <sub>PHL1</sub>	100 - 0.0 1	01, 04	3	29	ns
D input to Q output			0.,0.			
Low to high level, from	t <sub>PLH2</sub>		01, 04	3	32	ns
D input to Q output			,			
High to low level, from	t <sub>PHL2</sub>	1	01, 04	3	26	ns
	IPHL2		01, 04	3	20	113
D input to Q output		-			4.5	
Low to high level, from	t <sub>PLH3</sub>		01, 04	3	42	ns
enable input to Q output		-	04.5:			
High to low level, from	t <sub>PHL3</sub>		01, 04	3	39	ns
enable input to Q output						

 $<sup>\</sup>underline{1}$ / Not more than one output should be shorted at one time.

TABLE I. <u>Electrical performance characteristics</u> - Continued.

Test	Symbol	Conditions	Device	Lim	nits	Unit
		-55°C ≤ T <sub>C</sub> ≤ +125°C	type	Min	Max	
Low to high level, from	t <sub>PLH4</sub>	$C_L = 50 \text{ pF } \pm 10\%, R_L = 2 \text{ k}\Omega \pm 5\%$	01, 04	3	46	ns
enable input to Q output		V <sub>CC</sub> = 5.0 V				
High to low level, from	t <sub>PHL4</sub>		01, 04	3	26	ns
enable input to Q output						
Low to high level, from	t <sub>PLH1</sub>		02	3	35	ns
S input to Q output						
High to low level, from	t <sub>PHL1</sub>		02	3	26	ns
S input to Q output						
High to low level, from	t <sub>PHL2</sub>		02	3	42	ns
R input to Q output						
High to low level, from	t <sub>PHL1</sub>		03	3	42	ns
clear input to Q output			05	3	30	
Low to high level, from	t <sub>PLH2</sub>		03	3	48	ns
data input to Q output			05	3	46	
High to low level, from	t <sub>PHL2</sub>		03	3	34	ns
data input to Q output			05	3	33	
Low to high level, from	t <sub>PLH3</sub>		03	3	56	ns
address input to Q output			05	3	42	
High to low level, from	t <sub>PHL3</sub>		03	3	44	ns
address input to Q output			05	3	42	
Low to high level, from	t <sub>PLH4</sub>		03	3	52	ns
enable input to Q output			05	3	38	
High to low level, from enable input to Q output	t <sub>PHL4</sub>		03, 05	3	38	ns

TABLE II. Electrical test requirements.

	Subgroups	(see table III)
MIL-PRF-38535	Class S	Class B
test requirements	devices	devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 7, 9, 10, 11	1*, 2, 3, 7, 9
Group A test requirements	1, 2, 3, 7, 8, 9, 10, 11	1, 2, 3, 7, 8, 9, 10, 11
Group B electrical test parameters when using method 5005 QCI option	1, 2, 3, 9, 10, 11	N/A
Group C end-point electrical parameters	1, 2, 3, 9, 10, 11	1, 2, 3
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

<sup>\*</sup>PDA applies to subgroup 1.

- 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II MIL-PRF-38535.
- 4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
  - 4.5 Methods of inspection. Methods of inspection shall be specified and as follows:
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

	Pin s	ymbol	Pin sy	ymbol	Pin sy	mbol	Pin sy	mbol	Pin s	ymbol
	Device	type 01	Device	type 02	Device t	ype 03	Device t	type 04	Device	type 05
Pin	Case	Case	Case	Case	Case	Case	Case	Case	Case	Case
number	2, X	E, F	2, X	E, F	2, X	E, F	2, X	E, F	2, X	E, F
1	NC	1 Q	NC	1 R	NC	Α	NC	1D	NC	Α
2	1 Q	1D	1 R	1 <del>S</del> 1	Α	В	1D	1 Q	Α	В
3	1D	2D	1 <del>S</del> 1	1 S 2	В	С	1 Q	1Q	В	С
4	2D	ENBL 3-4	1 <del>S</del> 2	1Q	С	Q0	1Q	ENBL 1-2	С	Q0
5	ENBL 3-4	$V_{CC}$	1Q	2R	Q0	Q1	ENBL 1-2	2Q	Q0	Q1
6	NC	3D	NC	2 <del>S</del>	NC	Q2	NC	2 Q	NC	Q2
7	V <sub>CC</sub>	4D	2 R	2Q	Q1	Q3	2Q	2D	Q1	Q3
8	3D	4 Q	2 <del>-</del> S	GND	Q2	GND	2 Q	2 GND		GND
9	4D	4Q	2Q	3Q	Q3	Q4	2D	3D	Q3	Q4
10	4 \( \overline{Q} \)	3Q	GND	3R	GND	Q5	GND	3 <del>Q</del>	GND	Q5
11	NC	3 Q	NC	3 <del>-</del> 3 1	NC	Q6	NC	3Q	NC	Q6
12	4Q	GND	3Q	3 <del>-</del> 2	Q4	Q7	3D	ENBL 3-4	Q4	Q7
13	3Q	ENBL 1-2	3 R	4Q	Q5	DATA IN	3 \( \overline{Q} \)	4Q	Q5	DATA IN
14	3 Q	2 Q	3 <del>-</del> 5 1	4 R	Q6	ENBL	3Q	4 Q	Q6	ENBL
15	GND	2Q	3 <del>-</del> 2	4 S	Q7	CLR	ENBL 3-4	4D	Q7	CLR
16	NC	1Q	NC	Vcc	NC	Vcc	NC	Vcc	NC	Vcc
17	ENBL 1-2		4Q		DATA IN		4Q		DATA IN	
18	2Q		4 R		ENBL		4 Q		ENBL	
19	2Q		4 S		CLR		4D		CLR	
20	1Q		V <sub>CC</sub>		V <sub>CC</sub>		V <sub>CC</sub>		V <sub>CC</sub>	

FIGURE 1. <u>Terminal connections</u>.

Device type 01 and 04

Inp	uts	Outputs				
D	Enable	Q	IQ			
L	Н	L	Н			
Н	Н	Н	L			
Х	L	Q0	$\overline{Q}$ 0			

H = high level, L = low level, X = irrelevant Q0 = the level of Q before the high-to-low transition of enable

Device type 02

Inp	Inputs						
s†							
Н	Н	Q0					
L	Н	Н					
Н	L	L					
L	L	H*					

- H = high level, L = low level, Q0 = the level of Q before the indicated input conditions were established.
- \* This output level is pseudo stable; that is, it may not persist when the S and R inputs return to their inactive (high) level.
- † For latches with double  $\overline{S}$  inputs:

 $H = both \overline{S}$  inputs high

L =one or both  $\overline{S}$  inputs low

FIGURE 2. Truth tables.

# Device type 03 and 05

Inp	outs	Output of addressed	Each other	Function
Clear	Enable	latch	output	
Н	L	D	Qio	Addressable latch
Н	Н	Qio	Qio	Memory
L	L	D	L	8-line demultiplexer
L	Н	L	L	Clear

Latch Selection Table

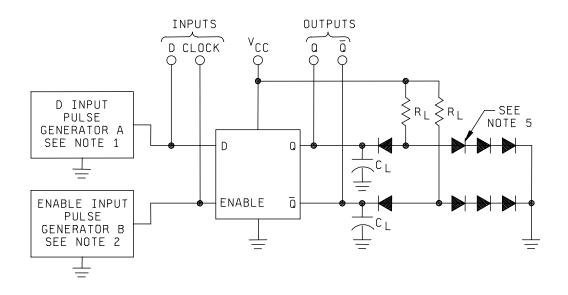
S	Select inputs							
С	В	Α	addressed					
L	L	L	0					
L	L	Ι	1					
L	Н	L	2					
L	Н	Ι	3					
Н	L	L	4					
Н	L	Н	5					
Н	Н	L	6					
Н	Н	Н	7					

H = high level, L = low level

Q = the level at the data input

Qio = the level of Qi (i = 0,1,...,7, as appropriate) before the indicated steady-state input conditions were established.

FIGURE 2. Truth tables - Continued.



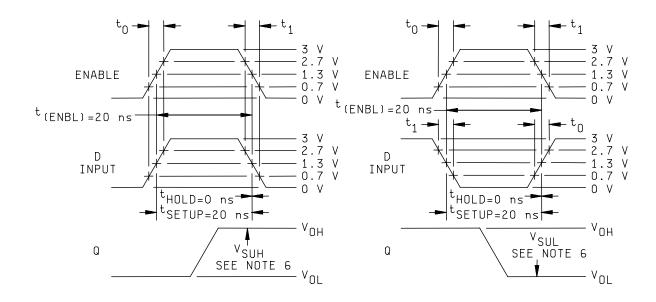
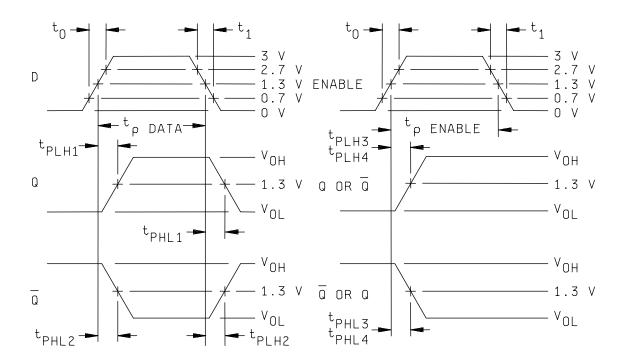


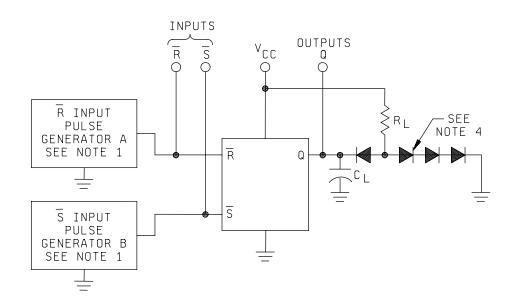
FIGURE 3. Switching test circuit and waveforms for device types 01 and 04.

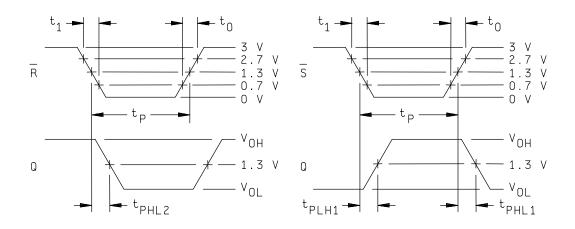


### NOTES:

- 1. The D input pulse generator has the following characteristics:  $V_{GEN} = 3 \text{ V}$ ,  $t_0 \le 15 \text{ ns}$ ,  $t_1 \le 6 \text{ ns}$ ,  $t_P = 30 \text{ ns}$ , and  $Z_{OUT} = 50\Omega$  except when measuring  $V_{SETUP}$ .
- 2. The enable pulse generator is identical to the D input pulse generator.
- 3.  $C_L = 50 \text{ pF} \pm 10\%$  and includes probe and jig capacitance.
- 4.  $R_L = 2 k\Omega \pm 5$  percent.
- 5. All diodes are 1N3064 or equivalent.
- 6.  $V_{\text{SETUP}}$  is to be measured 500 ns minimum after input transitions to assure that the device has latched with minimum setup and maximum hold conditions applied to inputs.

FIGURE 3. Switching test circuit and waveforms for device types 01 and 04 - Continued.

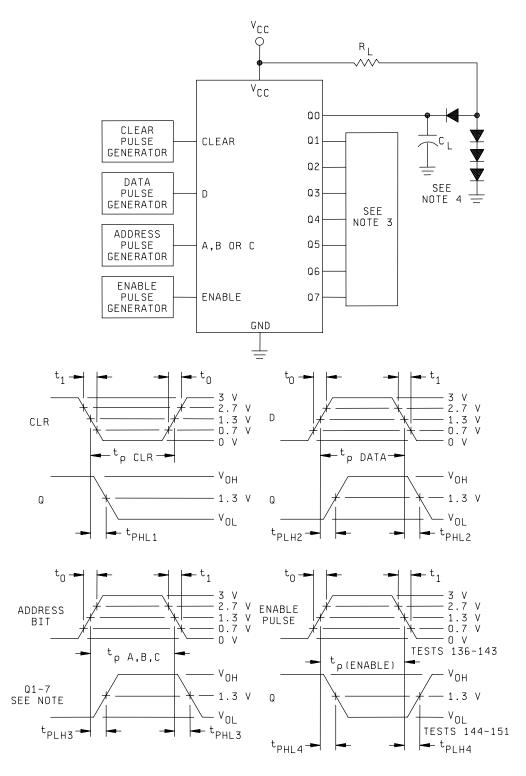




# NOTES:

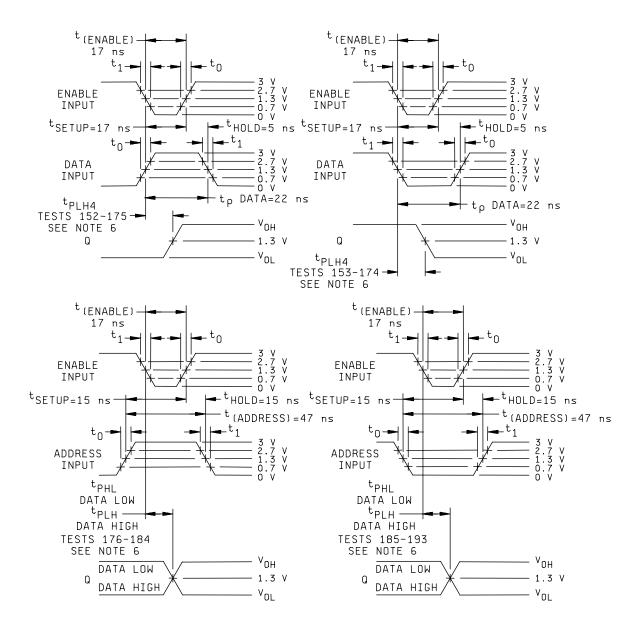
- 1.  $\overline{R}$  and  $\overline{S}$  pulse generator has the following characteristics:  $t_P = 40 \pm 10$  ns,  $t_0 \le 15$  ns,  $t_1 \le 6$  ns, and PRR  $\le 1.0$  MHz.
- 2.  $C_L = 50 \text{ pF} \pm 10\%$  and includes probe and jig capacitance.
- 3.  $R_L = 2 k\Omega \pm 5 \text{ percent.}$
- 4. All diodes are 1N3064 or equivalent.

FIGURE 4. Switching test circuit and waveforms for device type 02.



NOTE: For Q0 output waveform is inverted and  $t_{PLH3}$  and  $t_{PHL3}$  are interchanged.

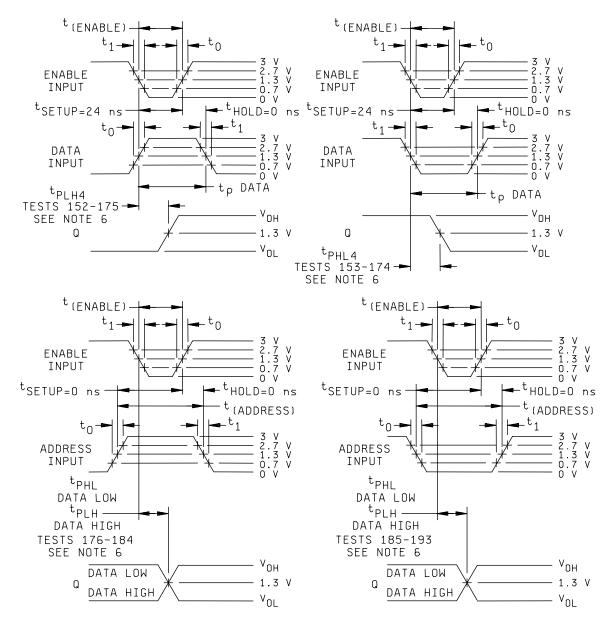
FIGURE 5. Switching test circuit and waveforms for device types 03 and 05



### NOTES:

- 1.  $R_L = 2 \text{ k}\Omega \pm 5 \text{ percent.}$
- 2.  $C_L = 50 \text{ pF} \pm 10\%$  and includes probe and jig capacitance.
- 3. All loads are the same as the  $Q_0$  load.
- 4. All diodes are 1N3064 or equivalent.
- 5. The clear, enable, data, and address pulse generator have the following characteristics:  $V_{GEN} = 3 \text{ V}$ ,  $t_0 \le 15 \text{ ns}$ ,  $t_1 \le 6 \text{ ns}$ ,  $t_P = 30 \text{ ns}$ , and PRR  $\le 1 \text{ MHz}$  except when measuring test nos. 152 thru 193,  $t_{P(ENABLE)} = 17 \text{ ns}$ ,  $t_{P(DDTA)} = 22 \text{ ns}$ ,  $t_{P(ADDRESS)} = 47 \text{ ns}$ , and  $t_{SETUP}$  and  $t_{HOLD}$  are as specified on the waveforms above.
- 6. Immediately prior to test 152, all outputs shall be cleared low; then beginning with test 152, test 152 thru 193 are to be performed in sequence with a wait of 500 ns minimum between each test. These tests are to assure latchup of the outputs under worst case setup and hold input conditions.

FIGURE 6. Switching test circuit and waveforms for device type 03 - Continued.



### NOTES:

- 1.  $R_L = 2 k\Omega \pm 5$  percent.
- 2.  $C_L = 50 \text{ pF} \pm 10\%$  and includes probe and jig capacitance.
- 3. All loads are the same as the  $Q_0$  load.
- 4. All diodes are 1N3064 or equivalent.
- 5. The clear, enable, data, and address pulse generator have the following characteristics:  $V_{GEN} = 3 \text{ V}$ ,  $t_0 \le 15 \text{ ns}$ ,  $t_1 \le 6 \text{ ns}$ ,  $t_P = 30 \text{ ns}$ , and PRR  $\le 1 \text{ MHz}$  except when measuring test nos. 152 thru 193,  $t_{P(ENABLE)} = 24 \text{ ns}$ ,  $t_{P(DATA)} = 24 \text{ ns}$ ,  $t_{P(ADDRESS)} = 24 \text{ ns}$ , and  $t_{SETUP}$  and  $t_{HOLD}$  are as specified on the waveforms above.
- 6. Immediately prior to test 152, all outputs shall be cleared low; then beginning with test 152, test 152 thru 193 are to be performed in sequence with a wait of 500 ns minimum between each test. These tests are to assure latchup of the outputs under worst case setup and hold input conditions.

FIGURE 5. Switching test circuit and waveforms for device type 05 - Continued.

TABLE III. Group A inspection for device type 01 and 04. Terminal conditions (pins not designated may be high  $\geq$  2.0 V: low  $\leq$  0.7 V: or open).

Section   Sect							Te	erminal d	conditio	ns (pins	not des	signated	l may b	e high ≥	2.0 V; ld	$5w \le 0.7$	V; or op	en).						
Subgraph				Type 01	1	2													15	16				
Subgroup   Symbol				Cases																				
Subgroup					_	_		_	-		_	40	40	40		45	47	40	40	00				
No. of the content					2	3	4	5	/	8	9	10	12	13	14	15	17	18	19	20				
Subgroup							_	40	4.0	_			40		4.0				_					
Subgroup			MIL STD		2	1	/	12	16	9	15	14	13	11	10	8	4	6	5	3				
Signor   S			WIIL-STD-																					
Tester   T	Subaroup	Symbol	883		3	2	9	15	20	12	19	18	17	14	13	10	5	8	7	4	Measured	Lim	its	Unit
Tr - 20°C    Tr - 20°C   Tr -	3 ,	-,					-				-	-				-	-							- '
Tr - 20°C    Tr - 20°C   Tr -				Test no.	. =	1D	2D	EN 3-4	Vcc	3D	4D	.=	4Q	3Q		GND	EN 1-2		2Q	1Q		Min	Max	
C = 25°C												4 Q	-		3 Q			2 Q						
TC = 29°C	1	V <sub>OH</sub>	3006	1	-0.4 mA	0.7 V			4.5 V							GND	<u>1</u> /				1 Q	2.5		V
	Tc = 25°C		"	2		2.0 V			"											-0.4 mA				
A	20 0		"				0.7 V		"									-0.4 mA		*******				"
Company			_															-						
S			. "				2.0 V		- :	0.71/					0.4.4				-0.4 mA					
Vol.   3007   9   4 mA   2.0 V			"	5				<u>1</u> /		0.7 V					-0.4 mA						3 Q			
Vol. 3007 9 4 mA 20 V			"	6				"	"	2.0 V				-0.4 mA							3Q	"		"
Vot   3007   9			"	7				"	"		0.7 V	-0.4 mA									40	"		"
Vo. 3007 9 4 4mA 2.0V				0							201/		0.4 == 1			-						-		
10		V	2007		4 m A	201/					2.0 V		-0.4 MA				1/					<u> </u>	0.4	
11		V OL	3007	9	4 111/4	2.0 V											1/				1 Q		0.4	
12			"			0.7 V										-	-			4 mA				"
12			"	11			2.0 V		"							"	"	4 mA			20			"
13				12			0.7.1/												4 m/\			-		
14							0.7 V	1/	"	2 0 V					4 mA				4111/1					
15								-3																
In								"	"	0.7 V				4 mA								<u> </u>	"	"
International Content of the conte			"	15				"			2.0 V	4 mA									4 Q			
International Content of the conte			"	16				"	"		0.7 V		4 mA								4Q			"
18		I <sub>II 1</sub>	3009			0.4 V			5.5 V							"	4.5 V				1D	4/	4/	mA
1 <sub>82</sub>   20				18			0.4 V		-							-	4.5 V				2D	"	-	"
1			"							0.4 V											3D	"		"
1			"					4.5 V			0.4 V					- "								
Hart   3010   23   2.7 V		I <sub>IL2</sub>				4.5 V	4.5 V	0.417	- :	4.5.1	451/						0.4 V				EN 1-2	<u> </u>		
1812   24		-	2010			271/		0.4 V	"	4.5 V	4.5 V										EN 3-4		20	
1		IH1	3010			Z.1 V	271/															-	20	μA "
No.				25			Z.1 V		"	27V											3D			
			"	26					"		2.7 V										4D			"
1		I <sub>IH2</sub>	"						"							"	2.7 V						80	"
1			"	28				2.7 V	"							"					EN 3-4		80	"
Note		I <sub>IH3</sub>	"			5.5 V															1D		100	
Note			"				5.5 V													ļ	2D	ļ'		"
I										5.5 V	5.5.7											<b> </b>		"
No.		-	- "			<del>                                     </del>					5.5 V				1		5 E \/	<del>                                     </del>		1		<del>                                     </del>		m^
Ios   3011   35   GND   GND		IH4			1	-	1	55\/		1		1			}		5.5 V	-		}		<del>                                     </del>		IIIA "
36		los	3011		GND	GND		J.J V	"								4.5 V					-15		
37		.03			0.10																			
38			"			4.5 V														GND		"	"	"
38			"	37			GND		"							"	"	GND			2 Q	"		"
39			"	38			4.5 V		"										GND	<u> </u>		-	"	"
40			"					4.5 V	"	GND					GND	"			-			"		"
" 41 " " GND GND " 4 Q " " " " 42				40				<u> </u>		4517			ļ	01:5						ļ				
" 42 " " 4.5 V GND " 4Q " " "						<del>                                     </del>		"		4.5 V	CND	CND		GND	1	-		<del>                                     </del>		1			- 1	-
				41								GIND										1		
I <sub>CC</sub>   3005   43   GND   GND   GND   GND   GND   " GND   " GND   V <sub>CC</sub> " 12 "			"					"	"				GND			"						"	"	"
		I <sub>cc</sub>	3005	43		GND	GND	GND		GND	GND						GND				V <sub>cc</sub>	"	12	

See footnotes at end of device type 01 and 04.

Terminal conditions	(nine not	vem hatennizah t	he high >	$2.0 \text{ V} \cdot \text{low} < 0.7 \text{ V} \cdot \text{or open}$

						16	illilliai (	Jonandio	na (bina	HOL UES	signatec	i illay b	e nign ≥	2.0 v, it	$JW \geq U.I$	v, or op	en).						
			Type 01 Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
			Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20				
		MIL-STD-	Type 04 Cases E, F	2	1	7	12	16	9	15	14	13	11	10	8	4	6	5	3				
Subgroup	Symbol	883 method	Cases 2, X	3	2	9	15	20	12	19	18	17	14	13	10	5	8	7	4	Measured terminal	Lim	nits	Unit
			Test no.	1 Q	1D	2D	EN 3-4	V <sub>CC</sub>	3D	4D	4 Q	4Q	3Q	3 Q	GND	EN 1-2	2 Q	2Q	1Q		Min	Max	
1	VIc		44		-18 mA			4.5 V							GND					1D		-1.5	V
Tc = 25°C			45			-18 mA		"												2D			
			46					- "	-18 mA	40. 4					- "					3D			
			47							-18 mA						40. 4				4D			
			48 49				40 4	- "								-18 mA				EN 1-2		-	
2	Come t			المسط المساد		1	-18 mA	10E0C and	11/ 40040											EN 3-4			
3		ests, termina																					
7 <u>2</u> /	Truth	ests, termina 3014	50	, and limit	s as subgr A	Oup 1, exc	ept I <sub>C</sub> = -:	5.0 V	V <sub>IC</sub> tests a	re omitted A	L	Н	Н	L	GND	А		Н	Н		2/		
Tc = 25°C	table	3014	51	-	A	A	В	3.0 V	A	A	-	"		-	UND "	В	-		-		<u>3</u> /		
1C = 25 C	tests		52		В	В	В		В	В				"		В			"				
	เยรเร		53	Н	В	В	A		В	В	Н	L		Н		A	Н	-	-				ļ
8	Samo to	sts and tern						C and Tal.		ь				- "		_ ^	- ''						
9	t <sub>PLH1</sub>	3003	54	0115 45 101	IN	7 ехсері і	C = +123	5.0 V	= -55 C.		1				GND	4.5 V			OUT	1D to 1Q	3	32	ns
Tc = 25°C	t <sub>PHL2</sub>	Fig. 4	55	OUT	IN			3.0 V							"	7.5 V			001	1D to 1 Q	"	20	"
	t <sub>PI H1</sub>	"	56			IN		"										OUT		2D to 2Q		32	
	t <sub>PHL2</sub>	"	57			IN		"							"	"	OUT			2D to 2 Q	"	20	
							451/						OUT										
	t <sub>PLH1</sub>		58 59				4.5 V		IN				OUT	OUT	-					3D to 3Q		32	
	t <sub>PHL2</sub>		59						IN					OUT	-					3D to 3 Q		20	
	t <sub>PLH1</sub>	"	60				"	"		IN		OUT								4D to 4Q		32	
	t <sub>PHL2</sub>	"	61				"	"		IN	OUT									4D to 4 Q	"	20	
	t <sub>PHL1</sub>		62		IN											4.5 V			OUT	1D to 1Q		22	
	t <sub>PLH2</sub>		63	OUT	IN															1D to 1 Q		25	
	t <sub>PHL1</sub>	"	64			IN		"										OUT		2D to 2Q		22	
	t <sub>PLH2</sub>	"	65			IN		"								"	OUT			2D to 2 Q		25	
	t <sub>PHL1</sub>		66				4.5 V	- "	IN				OUT		- "					3D to 3Q	-	22	
	t <sub>PHL2</sub>	"	67				"		IN					OUT						3D to 3 Q		25	
	t <sub>PHL1</sub>	"	68				"	"		IN		OUT								4D to 4Q		22	
	t <sub>PLH2</sub>	"	69				"	"		IN	OUT			1		1				4D to 4 Q		25	
	t <sub>PLH3</sub>		70		4.5 V	4.5.1		"							-	IN		O: :=	OUT	EN to 1Q		32	-
			71			4.5 V			4.53:				O. I.T.			IN		OUT		EN to 2Q	- "	- "-	-
			72				IN		4.5 V	4517		01.17	OUT							EN to 3Q			"
		"	73		OND		IN			4.5 V		OUT				INI			OUT	EN to 4Q	-		
	t <sub>PHL3</sub>	,	74		GND	GND			-		-					IN IN		OUT	OUT	EN to 1Q		30	-
			75 76	<b>-</b>	1	GND	INI	"	GND		<b> </b>		OUT			IIN		UUI		EN to 2Q EN to 3Q		-	
		,,	76		-		IN IN	"	GND	GND		OUT	001							EN to 3Q EN to 4Q			
	t	"	78	OUT	GND		IIN	"		GND		001				IN						35	
	t <sub>PLH4</sub>		78	001	טאט	GND		"								IN	OUT			EN to 1 Q		33	
						GND	INI	,	GND					OUT		IIN	001			EN to 2 Q			
			80				IN		GND					OUT						EN to 3 Q			
		"	81				IN	"		GND	OUT				"					EN to 4 Q	"		"

See footnotes at end of device types 01 and 04.

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TABLE III. Group A inspection for device type 01 and 04 - Continued.

Terminal conditions (pins not designated may be high  $\ge 2.0 \text{ V}$ : low  $\le 0.7 \text{ V}$ : or open).

						16	erminai	conaitio	ns (pins	s not ae:	signated	a may c	e nign ≥	2.0 V; I	ow ≤ 0.7	v; or op	en).						
			Type 01 Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
			Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20				
		MIL-STD-	Type 04 Cases E, F	2	1	7	12	16	9	15	14	13	11	10	8	4	6	5	3				
Subgroup	Symbol	883 method	Cases 2, X	3	2	9	15	20	12	19	18	17	14	13	10	5	8	7	4	Measured terminal	Lim	iits	Unit
			Test no.	1 Q	1D	2D	EN 3-4	V <sub>CC</sub>	3D	4D	4 Q	4Q	3Q	3 Q	GND	EN 1-2	2 Q	2Q	1Q		Min	Max	
9	t <sub>PHL4</sub>	3003	82	OUT	4.5 V			5.0 V							GND	IN				EN to 1 Q	3	20	ns
Tc = 25°C		Fig. 3	83			4.5 V		•							"	IN	OUT			EN to 2 Q			"
		"	84				IN	"	4.5 V					OUT	"					EN to 3 Q			"
		"	85				IN	•		4.5 V	OUT				"					EN to 4 Q	•		"
	$V_{SUH}$		86		IN										"	IN			OUT	1Q	2.5		V
			87 88			IN	INI	"	IN				OUT		-	IN		OUT		2Q 3Q	-		"
		"	89				IN IN		IIN	IN		OUT	001							4Q			"
	V <sub>SUL</sub>	"	90		IN		IIN	"		IIN		001				IN			OUT	1Q		0.4	"
	V SUL	"	91			IN		"							"	IN		OUT	001	2Q		"	"
		"	92				IN	"	IN				OUT		"					3Q			"
		"	93				IN	"		IN		OUT			"					4Q			"
10	t <sub>PLH1</sub>																				3	42	ns
T <sub>C</sub> =125°C	t <sub>PHL1</sub>																				"	29	"
	t <sub>PLH2</sub>																					32	"
	t <sub>PHL2</sub>	Same tes	ts and termi	nal conditi	ons as for	subgroup	9, except	$T_{\rm C} = +125$	°C and tes	t limits as	shown											26	"
	t <sub>PLH3</sub>																					42	"
	t <sub>PHL3</sub>																					39	"
	t <sub>PLH4</sub>	ļ																			- :	46	
	t <sub>PHL4</sub>	ŀ																			0.5	26	V
	V <sub>SUH</sub>																				2.5	0.4	V
11	001	oto tormino	Loonditions	and limita	on for au-	aroup 10	oveent T	_ EE°C														0.4	V
	Same te	ests, termina	CONTRICTORS	and iiiiits	as iui sud	group 10,	except I <sub>C</sub>	= -55-0.															

- 1/ Apply 0V/3V 5V/0V momentary pulse 500 ns minimum prior to measurement.
- <u>2</u>/ A = 2.4 V, B = 0.4 V.
- 3/ H ≥ 1.5 V, L ≤ 1.5 V.
- $\underline{4}/\ I_{IL}$  limits are as follows:

		Min/	max limits (m.	A)	
Test	Circuit A, B	Circuit C	Circ	uit D	Circuit E
			Device 01	Device 04	
I <sub>IL1</sub>	16/40	0005/40	03/40	16/40	19/42
I <sub>IL2</sub>	64/-1.60	0/-1.20	12/-1.20	64/-1.60	75/-1.60

TABLE III. <u>Group A inspection for device type 02</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminai d	conaitio	ns (pins	not des	signated	a may b	e high ≥	2.0 V; K	0.7	v; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	1 R	1 <del>-</del> 1	1 S 2	1Q	2 R	2 S	2Q	GND	3Q	3 R	3 <del>-</del> 5 1	3 <del>-</del> 2	4Q	4 R	4 S	V <sub>CC</sub>		Min	Max	
1	V <sub>OH</sub>	3006	1								GND					-0.4 mA	2.0 V	1/	4.5 V	4Q	2.5		V
Tc = 25°C		"	2								"	-0.4 mA	2.0 V	2.0 V	<u>1</u> /				"	3Q			"
			3	0.01/	0.01/	41	0.4 4	2.0 V	<u>1</u> /	-0.4 mA	"								- "	2Q			-
	V <sub>OL</sub>	3007	<u>4</u> 5	2.0 V	2.0 V 2.0 V	<u>1</u> / 2.0 V	-0.4 mA 4 mA				"									1Q 1Q	-	0.4	
	• OL	"	6	<u> </u>	2.0 1	2.0 1		1/	2.0 V	4 mA	"								"	2Q		"	"
		"	7								"	4 mA	<u>1</u> /	2.0 V	2.0 V				"	3Q		"	"
		3009	<u>8</u> 9	0.4 V		GND					"					4 mA	<u>1</u> /	2.0 V	5.5 V	4Q	4/	<u>4</u> /	mA
	I <sub>IL1</sub>	3009																	3.5 V	1 R	<u>4</u> /	4/	IIIA
			10	GND	0.4 V	4.5 V					"									1 <del>S</del> 1			
		"	11	GND	4.5 V	0.4 V					"									1 S 2			"
			12					0.4 V	GND										"	2 R			
		"	13					GND	0.4 V		"									2 \$			"
		"	14								"		0.4 V		GND				"	3 R		"	
		"	15								"		GND	0.4 V	4.5 V					3 S 1			
		"	16								"		GND	4.5 V	0.4 V				"	3 <del>S</del> 2	"	"	"
		"	17								"						0.4 V	GND	"	4 R		"	"
		"	18								"						GND	0.4 V	"	4 S			
	I <sub>IH1</sub>	3010	19	2.7 V							"								"	1 R		20	μΑ
		"	20		2.7 V						"								"	1 <del>-</del> 1		"	"
		"	21			2.7 V					"									1 S 2		"	"
		"	22					2.7 V			"								"	2 R		"	
		"	23						2.7 V		"									2 <del>-</del> S		"	"
		"	24								"		2.7 V						"	3 R		"	
		"	25								"			2.7 V					"	3 <del>-</del> 1		"	"
		"	26								"				2.7 V				"	3 <del>S</del> 2		"	"
		"	27								"						2.7 V		"	4 R		"	"
		"	28								"							2.7 V	"	4 S		"	
	I <sub>IH3</sub>	"	29	5.5 V							"								"	1 R		100	
		"	30		5.5 V						"								"	1 S 1		"	
		"	31			5.5 V					"								"	1 S 2		"	
		"	32					5.5 V			"								"	2 R		"	"
		"	33						5.5 V		"								"	2 S		"	"
1		"	34								"		5.5 V						"	3 <del>-</del> R		"	"
1		"	35								"			5.5 V					"	3 <del>S</del> 1		"	"
		"	36								"				5.5 V				"	3 <del>-</del> 2		"	"
1		"	37								"						5.5 V		"	4 R		"	"
		"	38								"							5.5 V	"	4 <del>S</del>		"	"

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TABLE III. <u>Group A inspection for device type 02</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminai (								$5w \le 0.7$		en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	iits	Unit
			Test no.	1 R	1 <del>S</del> 1	1 S 2	1Q	2 R	2 S	2Q	GND	3Q	3 R	3 <del>-</del> 1	3 <del>-</del> 5 2	4Q	4 R	4 S	V <sub>CC</sub>		Min	Max	
1	Ios	3011	39	5.0 V	GND	GND	GND				GND								5.5 V	1Q	-15	-100	mΑ
Tc = 25°C		"	40					5.0 V	GND	GND	"								"	2Q		"	"
		"	41								"	GND	5.0 V	GND	GND				"	3Q		"	"
		"	42								"					GND	5.0 V	GND	"	4Q		"	"
	$I_{CC}$	3005	43	GND	4.5 V	4.5 V		GND	4.5 V		"		GND	4.5 V	4.5 V		GND	4.5 V	"	V <sub>cc</sub>		7	
	V <sub>IC</sub>		44	-18 mA	40. 4						"								4.5 V	1 R		-1.5	V
			45 46		-18 mA	-18 mA														1 S 1			
			47			-16 IIIA		10 1												1 S 2			
			48					-18 mA	-18 mA		"									2 R			
			49						-1011114		"		-18 mA							2 S			
			50								"		-10 IIIA	-18 mA						3 R			
			51								"			1011111	-18 mA					3 S 1			
			52												-10111A		-18 mA			3 S 2			
			53								"						-10 IIIA	-18 mA		4 R 4 S			
																		1011111		4 S			
2		ests, termina																					
3		ests, termina																					
7 <u>2</u> /	Truth	3014	54	В	В	В	Н	В	В	Н	GND	Н	В	В	В	Н	В	В	5.0 V		<u>3</u> /		
Tc = 25°C		"	55	В	Α	Α	L	В	Α	L	"	L	В	Α	Α	L	В	Α	"				
	tests	"	56	Α	Α	"	L	Α	Α	L	"	L	Α	Α	"	L	Α	Α					
		"	57	"	В	"	Н	"	В	Н	"	Н		В	"	Н		В	"				
		"	58	"	Α	В	Н	"	В	Н	"	Н	"	Α	В	Н	"	В	"				
8	Same te t <sub>PLH1</sub>	ests and term 3003	ninal condition	ons as for GND	subgroup IN	7 except 7	$\Gamma_{\rm C} = +125^{\circ}$ OUT	C and T <sub>C</sub> :	= -55°C.		GND								5.0 V	1 S 1 to 1Q	3	27	ns
Tc = 25°C		Fig. 4	60	GND	4.5 V	IN	OUT				"												
		"	61					GND	IN	OUT	"									1 S 2 to 1Q			
		"	62								"	OUT	GND	IN	4.5 V					2 S to 2Q			
		"	63								"	OUT	GND	4.5 V	IN					3 S 1 to 3Q			
		"	64								"					OUT	GND	IN		3 S 2 to 3Q	-		
	t <sub>PHL1</sub>	"	65	GND	IN	4.5 V	OUT				"						-			4 S to 4Q	-	20	
	11161	"	66	GND	4.5 V	IN	OUT	-			"									1 S 1 to 1Q			
		"	67					GND	IN	OUT	"									1 S 2 to 1Q			
			68			<del>                                     </del>	<del>                                     </del>				"	OUT	GND	IN	4.5 V					2 S to 2Q			
			69			-	-	-			"	OUT	GND	4.5 V	IN					3 S 1 to 3Q			
			70			-	-	-								OUT	GND	IN		3 S 2 to 3Q 4 S to 4Q			
1																							

# TABLE III. Group A inspection for device type 02.

Terminal conditions	(nine not designate	d may be high $> 2.0$	V: low $\leq 0.7$ V: or open).
reminal conditions	tullis flut desidifate	u iliav be iliuli 2 2.0	V. 10W ≥ 0.7 V. 01 0DC117.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	1 R	1 <del>-</del> 1	1 <del>S</del> 2	1Q	2 R	2 <del>S</del>	2Q	GND	3Q	3 R	3 <del>-</del> 5 1	3 <del>-</del> 2	4Q	4 R	4 S	V <sub>CC</sub>		Min	Max	
9	t <sub>PHL2</sub>	3003	71	IN	4.5 V	4.5 V	OUT				GND								5.0 V	1 R to 1Q	3	32	ns
c = 25°C		Fig. 4	72					IN	4.5 V	OUT	"									2 R to 2Q			
		"	73								"	OUT	IN	4.5 V	4.5 V				"	3 R to 3Q	"	"	
		"	74								"					OUT	IN	4.5 V	"	4 R to 4Q		"	
10	t <sub>PLH1</sub>							•								•	•	•				35	"
Γ <sub>C</sub> =125°C	t <sub>PHL1</sub>	Same test	ts and termin	nal conditi	ions as for	subgroup	9, except	$T_{\rm C} = +125$	°C and tes	t limits as	shown									ĺ		26	"
	t <sub>PHI 2</sub>																			Ĭ		42	-

<u>2</u>/ A = 2.4 V, B = 0.4 V.

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3/ H ≥ 1.5 V, L ≤ 1.5 V.

 $\underline{4}/\ I_{IL}$  limits are as follows:

			Mi	n/max limits (m/	<del>\</del> )		
Test	Circ	uit A	Circuit B	Circuit C	Circu	uit D	Circuit E
	Tests	Limits			Tests	Limits	
F	9, 12,	0/-0.2	16/40	135/370	9, 12,	001/15	16/40
	14, 17				14, 17		
	10, 11, 13	0/-0.2			10, 11, 13	03/30	
	15, 16, 18				15, 16, 18		

TABLE III. <u>Group A inspection for device type 03</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminai o	conditio	ns (pins	not des	signated	may b	e high ≥	2.0 V; I	$5W \le 0.7$	v; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	its	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>		Min	Max	
1	V <sub>OH</sub>	3006	1	0.7 V	0.7 V	0.7 V	-0.4 mA				GND					2.0 V	<u>1</u> /	2.0 V	4.5 V	Q0	2.5		V
$Tc = 25^{\circ}C$		"	2	2.0 V	0.7 V	"		-0.4 mA			"					"	"	"	"	Q1			"
		"	3	0.7 V	2.0 V	"			-0.4 mA		"					"	"		"	Q2			
			4	2.0 V	2.0 V	"				-0.4 mA	"						"		"	Q3			-
			5	0.7 V	0.7 V	2.0 V					- "	-0.4 mA								Q4			
			6	2.0 V	0.7 V	- "							-0.4 mA			- "		- '		Q5			
			7	0.7 V	2.0 V						- "			-0.4 mA	0.4 1	-				Q6			
		3007	8 9	2.0 V 0.7 V	2.0 V 0.7 V	0.7 V	4 mA				-				-0.4 mA	0.7 V		-		Q7 Q0		0.4	
	V <sub>OL</sub>	3007	10	2.0 V	0.7 V	U.7 V	4 IIIA	4 mA								U.7 V				Q0 Q1		"	-
		"	11	0.7 V	2.0 V			4 IIIA	4 mA											Q2		"	-
		"	12	2.0 V	2.0 V				7111/1	4 mA										Q3		"	"
		"	13	0.7 V	0.7 V	2.0 V						4 mA					"		"	Q4		"	"
		"	14	2.0 V	0.7 V								4 mA				"	-		Q5		"	"
		"	15	0.7 V	2.0 V	"					"			4 mA		"	"	"	"	Q6		"	"
		"	16	2.0 V	2.0 V										4 mA		"			Q7		"	"
	I <sub>IL1</sub>	3009	17	0.4 V							"								5.5 V	Α	<u>4</u> /	<u>4</u> /	mA
		"	18		0.4 V														"	В			"
		"	19			0.4 V					"								"	С			"
		"	20													0.4 V			"	DATA IN			
		"	21								"						0.4 V			ENBL			"
		"	22								"							0.4 V	"	CLR	"		"
	I <sub>IH1</sub>	3010	23	2.7 V							"								"	Α		20	μΑ
	·IIII	"	24		2.7 V						"									В		"	"
		"	25			2.7 V					"								"	C			
		"	26													2.7 V			"	DATA IN			
		"	27								"						2.7 V			ENBL			
		"	28								"							2.7 V	"	CLR			"
	_	"	29	5.5 V																A		100	-
	I <sub>IH3</sub>		30	3.5 V	5.5 V															В		100	
		"	31		J.J V	5.5 V					"									C			-
		"	32			0.0 1					"					5.5 V			"	DATA IN			
		"	33								"						5.5 V		"				-
																				ENBL			
			34															5.5 V		CLR		-	
	V <sub>IC</sub>		35	-18 mA							"								4.5 V	Α		-1.5	V
			36		-18 mA						"								"	В			"
			37			-18 mA					"								-	С			"
			38								"					-18 mA			"	DATA IN		"	
			39								"						-18 mA		. "	ENBL			
			40															-18 mA	"	CLR			
	Ios	3011	41	GND	GND	GND	GND									5.0 V	GND	5.0 V	5.5 V	Q0	-15	-100	mA
	.00	"	42	5.0 V	GND	"		GND								"	"	"	"	Q1	"	"	"
		"	43	GND	5.0 V				GND								"	"	"	Q2		"	"
		"	44	5.0 V	5.0 V	"				GND	"					"	"	"	"	Q3		"	"
		"	45	GND	GND	5.0 V					"	GND				"	"		"	Q4		"	"
		"	46	5.0 V	GND								GND			"	"	"	"	Q5	"	"	"
		"	47	GND	5.0 V	-					-		-	GND		"	"	-	"	Q6		"	"
		"	48	5.0 V	5.0 V										GND		"		"	Q7		"	"
	Icc	3005	49	GND	GND	GND								l		GND		GND	"	V <sub>CC</sub>		36	"

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TABLE III. <u>Group A inspection for device type 03</u>.

Terminal conditions (pins not designated may be high > 2.0 V; low < 0.7 V; or open).

						Τe	erminal o	conditio	ns (pins	not des	signated	l may b	e high ≥	2.0 V; lo	$w \le 0.7$	V; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lii	nits	Unit
			Test no.	А	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>	-	Min	Max	
2	Same te	ests, termina	I conditions,	, and limits	as for sub	group 1 e	xcept T <sub>C</sub> =	+125°C a	and V <sub>IC</sub> tes	sts are om	itted.						•		•	. "			
3		ests, termina					xcept T <sub>C</sub> =	-55°C an	d V <sub>IC</sub> tests	are omitt	ed.												
7 <u>2</u> /	Truth	3014	50	В	В	В	L	<u> </u>	L L	L	GND	L.	L	L L	L.	Α	A	В	5.0 V		<u>3</u> /		
Tc = 25°C			51	В			H	-		-:-				"		-	В	-:-	-:-				
	tests		52 53	A		-	_ L	Н	- "		- "			- "		-	A	-					
			54	A B	Α			L	"	"	"			"			B A						
			55	В	A	"	"	-	Н	"	"			"			В						
			56	A	"	"	"	"	ï	"	"			"			A						
			57	A	"	"	"	"	-	Н	"			"			В						
			58	В	В	Α	"	"	"	L	"			"			A						
			59	В	"	"	"	"	"	"	"	Н	"	"	"	"	В	"	"				
			60	Α	"		"		"	"	"	L		"			Α		"				
			61	Α	"	"	"	"	"	"	"		Н	"			В		"				
		"	62	В	Α	"	"	"	"	"	"	-	L	"		"	Α						
		"	63	В	"	"	"	"	"	"	"	"	-	Н		"	В	-	"				
		"	64	Α	"	"	"	"	"	"	"	"	-	L			Α		"				
			65	"		- "		- "	- "	"	- "			"	Н	- "	В	- "	- "				
			66		- "	- "	- "	- "	- "	- "	- "	- "		- "			A	A	- :				
			67 68	В		-		-	-		- "		-			В		-	-				
			69	A B	B B	,,	"	,,	"	-	-		"	"		-			-				
			70	A	A	В				"	"												
			71	В	A	"	"	"	"	"	"		"	"									
			72	A	В	"	"	"	"	"	"		"	"									
			73	В	"	"	"	"	"	"	"		"	"		Α							
			74	"	"	"	Н	"	"	"	"	"	"	"		"	В						
		"	75	"	"	"	"	"	"	"	"	"	"	"	"	"	Α	"	"				
			76	Α	"	"	"	"	"	"	"		"	"			Α		"				
		"	77	-	-	-	"	Н	"	-	"	-	-	-			В						
		"	78	"	"	"	"	"	"	"	"	"	"	"	"	"	Α		"				
			79	В	Α	"	"	"	"	"	"	"		"			Α		"				
		".	80	"			- "		H				- "			- "-	В	- :	⊢ "				
			81	- "		- "	- "	- "	- "	- "	- "		- "	- "			A						
			82 83	Α "	"	,	"	,	"	H	-		"	"			A B						
			83	"					-		-			"		-	A		-				
			85	В	В	Α	"	"	"	"				"			A						
		"	86	"	"		"	"	"	"	"	Н	"	"			В						
		"	87	"	"	"	"	"	"	"	"	"	"	"			A		-				
		"	88	Α	"	"	"	"	"	"	"	"	"	"	"		A		-				
		"	89	"	"	"	"	"	"	"	"	"	Н	"	"	"	В	"	"				
		"	90	"	"		"		"	"	"	"	"	"		"	Α	-	"				
		"	91	В	Α	"	"	"	"	"	"	"	"	"	"	"	Α	"	"				
		"	92	"			"		"	"	"			Н	"	"	В						
		"	93	"	"	"	"	"	"	"	"	"	"	"		"	Α						
			94	A	"		- "			- "		"					A						
	_		95	Α						"	"	"	"	"	"	. "	В	"	. "				
8	Same te	ests and tern	ninal condition	ons as for	subgroup	7 except 1	$\Gamma_{\rm C} = +125^{\circ}$	C and T <sub>c</sub> :	= -55°C.														

TABLE III. <u>Group A inspection for device type 03</u>.

Terminal conditions (pins not designated may be high > 2.0 V; low < 0.7 V; or open).

						Τe	erminal	conditio	ns (pins	not des	signated	l may b	e high ≥	2.0 V; lo	50.7	V; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>cc</sub>		Min	Max	
9	t <sub>PLH1</sub>	3003	96	GND	GND	GND	OUT				GND					4.5 V	<u>6</u> /	IN	5.0 V	CLR to Q0	3	32	ns
Tc = 25°C		Fig. 5	97	4.5 V	GND	"		OUT			"					"			"	CLR to Q1	"	"	"
		"	98	GND	4.5 V	"			OUT		"					"	"		"	CLR to Q2	"	"	"
		"	99	4.5 V	4.5 V	"				OUT	"					"	"		"	CLR to Q3	"	"	"
		"	100	GND	GND	4.5 V						OUT				"			"	CLR to Q4			
		"	101	4.5 V	GND	"					"		OUT			"	"		"	CLR to Q5	"	"	"
		"	102	GND	4.5 V									OUT		"			"	CLR to Q6			
		"	103	4.5 V	4.5 V										OUT		"		"	CLR to Q7		"	
	t <sub>PLH2</sub>		104 105	GND 4.5 V	GND GND	GND "	OUT	OUT			-					IN "	GND "	GND "	- "	DI to Q0 DI to Q1		37	
		"	106	GND	4.5 V			001	OUT		"								"	DI to Q2		"	"
		"	107	4.5 V	4.5 V					OUT	"					"	"	-		DI to Q3	"	"	"
		"	108	GND	GND	4.5 V					"	OUT				"	"	"	"	DI to Q4	"	"	"
			109	4.5 V	GND								OUT	OUT		- "			- "	DI to Q5			
			110 111	GND 4.5 V	4.5 V 4.5 V	-								OUT	OUT	- "		-	- "	DI to Q6 DI to Q7		"	
	t	"	112	GND	GND	GND	OUT								OUT				"	DI to Q7		26	"
	t <sub>PHL2</sub>	"	113	4.5 V	GND	UND "	001	OUT												DI to Q1		20	"
		"	114	GND	4.5 V				OUT											DI to Q2		"	"
		"	115	4.5 V	4.5 V					OUT	"									DI to Q3		"	"
		"	116	GND	GND	4.5 V					"	OUT				"	"	"	"	DI to Q4	"	"	"
		"	117	4.5 V	GND	-							OUT				"	-		DI to Q5		"	"
			118	GND	4.5 V						"			OUT		"	"	"	"	DI to Q6	"	"	"
			119	4.5 V	4.5 V										OUT					DI to Q7		"	
	t <sub>PLH3</sub>	"	120 121	IN IN	GND GND	GND	OUT	OUT								4.5 V		-	- "	A to Q0 A to Q1		43	
		"	121	GND	IN			001	OUT											B to Q2	-	"	
		"	123	IN	4.5 V				001	OUT										A to Q3		"	"
		"	124	GND	GND	IN				001		OUT				"	"			C to Q4		"	"
		"	125	IN	GND	4.5 V					"		OUT			"	"	"	"	A to Q5	"	"	"
		"	126	GND	IN						"			OUT		"	"	-	"	B to Q6	"	"	"
		"	127	IN	4.5 V	-									OUT					A to Q7	"	"	"
	t <sub>PHL3</sub>		128	IN	GND	GND	OUT				"					"	"		"	A to Q0		34	"
			129	IN	GND	- :		OUT	OUT									-		A to Q1		- "	
		"	130 131	GND IN	IN 4.5 V				OUT	OUT										B to Q2 A to Q3	-	"	"
		"	132	GND	GND	IN				001		OUT								C to Q4		"	"
		"	133	IN	GND	4.5 V						001	OUT							A to Q5	-	"	-
		"	134	GND	IN	"					"			OUT						B to Q6		"	"
		"	135	IN	4.5 V						"				OUT	"	"	"	"	A to Q7	"	"	"
	t <sub>PLH4</sub>	"	136	GND	GND	GND	OUT				"					"	IN	-	"	ENBL to Q0	"	39	
		"	137	4.5 V	GND	"		OUT			"					"	"		"	ENBL to Q1	"	"	"
			138	GND	4.5 V				OUT	OUT										ENBL to Q2			
			139	4.5 V	4.5 V	451/				OUT		OUT				"	"	-	"	ENBL to Q3			
			140	GND 4.5 V	GND GND	4.5 V						OUT	OUT					-		ENBL to Q4	-		
		"	141	4.5 V GND	4.5 V						"		001	OUT						ENBL to Q5			
			142	4.5 V	4.5 V									001	OUT	"	"		"	ENBL to Q6		"	
	1		143	7.5 V	7.5 V	1	1	1	I	l	I	1	1	I	1 001	1			1	ENBL to Q7		1	

See footnotes at end of device type 05.

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TABLE III. <u>Group A inspection for device type 03</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminal	conditio	ns (pins	not des	signated	i may b	e high ≥	2.0 V; K	0.7	v; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>	-	Min	Max	
9	t <sub>PHL4</sub>	3003	144	GND	GND	GND	OUT				GND					4.5 V	IN	GND	5.0 V	ENBL to Q0	3	29	ns
T <sub>C</sub> = 25°C		Fig 6	145	4.5 V	GND	"		OUT			"						"	"	"	ENBL to Q1		"	"
		"	146	GND	4.5 V	"			OUT		"					"	"	"	"	ENBL to Q2		"	"
		"	147	4.5 V	4.5 V	"				OUT	"						"	"	"	ENBL to Q3	"	"	"
		"	148	GND	GND	4.5 V					"	OUT					"	"	"	ENBL to Q4	"	"	"
		"	149	4.5 V	GND	"							OUT					"	"	ENBL to Q5	"	"	"
		"	150	GND	4.5 V	"					"			OUT			"	"	"	ENBL to Q6			"
		"	151	4.5 V	4.5 V	"					"				OUT		"		"	ENBL to Q7			"
	t <sub>PLH4</sub>	" <u>5</u> /	152	GND	GND	GND	OUT				"					IN	IN	4.5 V	"	ENBL to Q0		39	"
	t <sub>PHL4</sub>	"	153	"	"	"	"				"						"		"	ENBL to Q0		29	"
	t <sub>PLH4</sub>	"	154	"	"	"	"				"							"	"	ENBL to Q0		39	"
	t <sub>PLH4</sub>	"	155	4.5 V	"	"		OUT			"							"	"	ENBL to Q1		39	"
	t <sub>PHL4</sub>	"	156	"	"	"		"			"							"	"	ENBL to Q1		29	"
	t <sub>PLH4</sub>	"	157	"	"	"		"			"							"	"	ENBL to Q1		39	"
	t <sub>PLH4</sub>	"	158	GND	4.5 V	"			OUT		"							"	"	ENBL to Q2		39	"
	t <sub>PHL4</sub>	"	159	"	"	"			"		"							"	"	ENBL to Q2		29	"
	t <sub>PLH4</sub>	"	160	"	"	"			"		"							"	"	ENBL to Q2		39	"
	t <sub>PLH4</sub>	"	161	4.5 V	"	"				OUT	"						"	"	"	ENBL to Q3		39	"
	t <sub>PHL4</sub>	"	162	"	"	"				"	"						"		"	ENBL to Q3		29	"
	t <sub>PLH4</sub>	"	163	"	"	"				"	"						"		"	ENBL to Q3		39	"
	t <sub>PLH4</sub>	"	164	GND	GND	4.5 V					"	OUT				"	"	"	"	ENBL to Q4		39	"
	t <sub>PHL4</sub>	"	165	"	"	"					"	"				"	"	"	"	ENBL to Q4		29	"
	t <sub>PLH4</sub>	"	166	"	"	"					"	"					"		"	ENBL to Q4		39	"
	t <sub>PLH4</sub>	"	167	4.5 V	"	"					"		OUT				"		"	ENBL to Q5		39	"
	t <sub>PHL4</sub>	"	168	"	"	"					"		"			"	"	"	"	ENBL to Q5		29	"
	t <sub>PLH4</sub>	"	169	"	"	"					"		"			"	"		"	ENBL to Q5		39	"
	t <sub>PLH4</sub>	"	170	GND	4.5 V	"					"			OUT			"	"	"	ENBL to Q6		39	"
	t <sub>PHL4</sub>	"	171	"	"	"					"			"		"	"	"	"	ENBL to Q6		29	"
	t <sub>PLH4</sub>	"	172	"	"	"					"			"		"	"	"	"	ENBL to Q6		39	"
	t <sub>PLH4</sub>	"	173	4.5 V	"	"					"				OUT	"	"	"	"	ENBL to Q7		39	"
	t <sub>PHL4</sub>	"	174	"	"	"					"				"	"	"	"	"	ENBL to Q7		29	"
	t <sub>PLH4</sub>	"	175	"	"	"					"				"	"	"	"	"	ENBL to Q7		39	"
	t <sub>PHL4</sub>	"	176	IN	GND	GND		OUT			"					GND	"	"	"	ENBL to Q1		29	"
	t <sub>PLH4</sub>	"	177	"	"	"		"			"					4.5 V	"	"	"	ENBL to Q1		39	"
	t <sub>PHL4</sub>	"	178	"	"	"		"			"					GND	"	"	"	ENBL to Q1	"	29	"

TABLE III. Group A inspection for device type 03.

									IABLE	III. <u>Gro</u>	up A ins	pection	n for dev	ice type	<u>03</u> .								
						Te	erminal o	conditio	ns (pins	not des	signated	l may b	e high ≥	2.0 V; I	ow ≤ 0.7	V; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	its	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>cc</sub>		Min	Max	
9	t <sub>PHL4</sub>	3003	179	GND	IN	GND			OUT		GND					GND	IN	4.5 V	5.0 V	ENBL to Q2	3	29	ns
$T_C = 25^{\circ}C$	t <sub>PLH4</sub>	Fig 6	180		"	"			"		"					4.5 V	"	"	"	ENBL to Q2		39	"
	t <sub>PHL4</sub>		181	"	"	"			"		"					GND	"	"	"	ENBL to Q2		29	"
	t <sub>PHL4</sub>		182	"	GND	IN						OUT				GND	"	"	"	ENBL to Q4		29	"
	t <sub>PLH4</sub>		183	"	"	"					"	"				4.5 V	"	"	"	ENBL to Q4		39	"
	t <sub>PHL4</sub>		184	"	"	"					"	"				GND	"	"		ENBL to Q4		29	"
	t <sub>PHL4</sub>		185	IN	"	GND	OUT				"					GND			"	ENBL to Q0		29	"
	t <sub>PLH4</sub>		186	"	"	"	"				"					4.5 V	"	"		ENBL to Q0		39	
	t <sub>PHL4</sub>		187		"	"					"					GND				1		29	
	t <sub>PHL4</sub>		188	4.5 V	IN	4.5 V							OUT			GND			"	ENBL to Q0		29	"
			189	"	"	"					"		"	1		4.5 V				ENBL to Q5		39	
	t <sub>PLH4</sub>		190	,,	,,	,,										GND				ENBL to Q5		29	
	t <sub>PHL4</sub>				451/	INI				OUT										ENBL to Q5			
	t <sub>PHL4</sub>		191		4.5 V	IN				OUT						GND				ENBL to Q3		29	
	t <sub>PLH4</sub>		192													4.5 V				ENBL to Q3		39	
	t <sub>PHL4</sub>		193	"	"	"				"	. "					GND		"		ENBL to Q3		29	
10	t <sub>PHL1</sub>																					42	- "-
T <sub>C</sub> =125°C		Same tes	ts and termi	nal conditi	ons as for	subgroup	9, except	$I_{\rm C} = +125$	°C and tes	st limits as	shown										-	48 34	
	t <sub>PHL2</sub>																					56	-
	t <sub>PHL3</sub>	1																				44	"
	t <sub>PLH4</sub>																					52	"

See footnotes at end of device type 05.

Same tests, terminal conditions and limits as for subgroup 10, except T<sub>C</sub> = -55°C.

TABLE III. Group A inspection for device type 05.

Terminal conditions (pins not designated may be high  $\ge 2.0 \text{ V}$ : low  $\le 0.7 \text{ V}$ ; or open).

Subgroup S		MIL-STD-	Cases	1	2	3	4						e high ≥										
Subgroup 5		WILL-SID-	E, F			3	4	5	6	7	8	9	10	11	12	13	14	15	16				
	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	А	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>		Min	Max	
1	V <sub>OH</sub>	3006	1	0.7 V	0.7 V	0.7 V	-0.4 mA				GND					2.0 V	<u>1</u> /	2.0 V	4.5 V	Q0	2.5		V
Tc = 25°C		"	2	2.0 V	0.7 V	=		-0.4 mA			=					"	"		"	Q1			
		"	3	0.7 V	2.0 V	-			-0.4 mA		"					-		"		Q2	-		"
		"	4	2.0 V	2.0 V	"				-0.4 mA	"					"	"	"	"	Q3	"		"
		"	5	0.7 V	0.7 V	2.0 V						-0.4 mA				"	"			Q4			
		" "	6	2.0 V	0.7 V	- :					- :		-0.4 mA							Q5			<del>-</del>
		"	7	0.7 V	2.0 V									-0.4 mA	0.4.4	"	- "			Q6			<del>-</del>
I	\ /		8	2.0 V	2.0 V		4 4				-				-0.4 mA		-			Q7		0.4	<del>-</del>
	$V_{OL}$	3007	9 10	0.7 V 2.0 V	0.7 V 0.7 V	0.7 V	4 mA	4 mA			-					0.7 V	-			Q0 Q1		0.4	<del></del>
		,,	11	0.7 V	2.0 V			4 mA	4 mA											Q2		"	"
		"	12	2.0 V	2.0 V				4 IIIA	4 mA						"				Q2 Q3		"	"
			13	0.7 V	0.7 V	2.0 V				71101		4 mA							"	Q4		"	"
		"	14	2.0 V	0.7 V	"							4 mA			"	"		"	Q5		"	"
		"	15	0.7 V	2.0 V									4 mA		"	"		"	Q6		"	"
		"	16	2.0 V	2.0 V						"				4 mA	"	"		"	Q7		"	"
	I <sub>IL1</sub>	3009	17	0.4 V							"								5.5 V	Α	<u>4</u> /	<u>4</u> /	mA
		"	18		0.4 V						-								"	В		"	"
		"	19			0.4 V					-								"	С	-	"	
		"	20								-					0.4 V			"	DATA IN	-	"	"
		"	21								"						0.4 V		"	ENBL	"	"	
		"	22								"							0.4 V	"	CLR		"	"
⊢	I <sub>IH1</sub>	3010	23	2.7 V																A		20	μА
	'IH1	3010	24	Z.1 V	2.7 V															В		20	μA "
		"	25		Z.1 V	2.7 V					"									C		"	-
		"	26			Z.1 V					"					2.7 V			"	DATA IN		"	
		"	27								"						2.7 V					"	"
		"	28															2.7 V	"	ENBL		"	
-	_		29	5.5 V																CLR A		100	
	I <sub>IH3</sub>		30	5.5 V	5.5 V															В		100	-
		"	31		5.5 V	5.5 V					"									С		"	-
		"	32			0.0 V					"					5.5 V			"	DATA IN			-
		"	33								"					0.0 V	5.5 V					"	
																				ENBL			<u> </u>
		"	34								"							5.5 V		CLR		"	1 "
<del> </del>	V <sub>IC</sub>		35	-18 mA							"								4.5 V	Α		-1.5	V
	*10		36	1011111	-18 mA						"								"	В		"	
			37			-18 mA					"								"	С		"	"
			38								"					-18 mA				DATA IN		"	"
			39								"						-18 mA			ENBL		"	
			40								"							-18 mA					
-		0044		CND	OND	ONE	ONE									5.01/	OND		\ ·	CLR		400	<del></del>
	los	3011	41	GND	GND	GND "	GND	CND			-					5.0 V	GND	5.0 V	5.5 V	Q0	-15	-100	mA "
		,,	42 43	5.0 V	GND			GND	GND							"		-	-	Q1 Q2		"	- "
		"	43	GND 5.0 V	5.0 V 5.0 V				GND	GND						"			"	Q2 Q3		"	-
		"	45	GND	GND	5.0 V				GIND		GND								Q3 Q4		"	-
		"	46	5.0 V	GND	J.U V						GIVD	GND			"	"		"	Q5		"	"
		"	47	GND	5.0 V								CIND	GND		"			"	Q6		"	-
		"	48	5.0 V	5.0 V									0110	GND	"			"	Q7		"	"
	Icc	3005	49	GND	GND	GND									0.10	GND		GND	"	V <sub>CC</sub>		36	

TABLE III. Group A inspection for device type 05.

Terminal conditions (pins not designated may be high  $\ge 2.0 \text{ V}$ : low  $\le 0.7 \text{ V}$ ; or open).

						Ιe	erminal d	conditio	ns (pıns	not des	signated	may b	e high ≥	2.0 V; Id	0.7	V; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Li	mits	Unit
			Test no.	А	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>		Min	Max	
2	Same te	sts, termina	I conditions,	, and limits	s as for sub	group 1 e	xcept T <sub>C</sub> =	+125°C a	nd V <sub>IC</sub> tes	sts are om	itted.												
		sts, termina					xcept T <sub>C</sub> =	-55°C and	d V <sub>IC</sub> tests	are omitt													
7 <u>2</u> /	Truth	3014	50	В	B "	В	L	L	L	L	GND	L	L	L "	L	Α "	A	B "	5.0 V		<u>3</u> /		
Tc = 25°C	table	"	51	В		"	Н			"			"	"	"		В		"				
	tests		52	A		- :	L .		- :	- :	- "			- "		-:-	A		- "				
			53	A				Н-	- "	- "	- "				- ;		В	-	-				
			54 55	B B	Α "	"	"	L "	Н	"	"			"		-	A B	-	-				
			56	A		"	"			"	"			"			A						
			57	Ā	"	"	"	"	-	Н	"			"			В	"	"				
		"	58	В	В	Α	"	"	"	ï	"			"			A						
		"	59	В	"	"	"	"	"	"	"	Н		"			В		"				
		"	60	A	"	"	"	"	"	"	"	L		"	-		A	"	"				
		"	61	Α	"	"	"	"	"	"	"		Н	"	"	"	В	"	"				
		"	62	В	Α	"	"	"	=	"	"		L	"	-		Α	"	"				
		"	63	В	"	"	"	-	=	"	"		-	Н	-		В	-	"				
		"	64	Α	"	"	"	"	"	"	"		"	L	"	"	Α	"	"				
			65	"	"	"	"	"	"	"	"		"	"	Н	"	В	"	"				
			66					- "			"	-	- "		- "		Α	A	- "				
			67	В		- "	- "		- :		- "		- "			В			<b>-</b> :-				
			68	A B	B B								-			-		-	-				
			69 70	A	A	В	"	"						"									
			71	В	A	B "	"		"	"	"		"	"									
			72	A	В	"	"	"	"	"	"		"	"				"	"				
		"	73	В	"	"	"		"	"	"		"	"		Α			"				
		"	74	"	"	"	Н	"	"	"	"		"	"	"	"	В	"	"				
			75	"	"	"	"		"	"	"		"	"			A	"					
		"	76	Α	"	"	"	"	"	"	"		"	"	"	"	Α	"	"				
		"	77	"	"	"	"	Н	=	"	"		"	"	-		В	"	"				
		"	78	"	"	"	"	-	=	"	"		=		-		Α	-	"				
		"	79	В	Α	"	"	=	-	"	"	-	-	"	-	"	Α	"	"				
			80	"	"	"	"	"	Н	- "	"	-		"			В						
		"	81		- "	"	"	"		- "	"	-	"	"		- "	A		- "				
			82	Α "	- "	"	"	"		- "		-			-		A		-				
			83 84	"	"	"	"	"	"	H "	"		"	"			B A	,	-				
		,,	84 85	В	В	A	"	"		"	"						A						
		,,	86	D #	D	* "	"	"	"	"	"	Н	"	"			В						
		"	87	"	"	"	"	"	"	"	"	"	"	"			A	"	-				
		"	88	Α	"	"	"	"	"	"	"		"	"			A						
		"	89	"	"	"	"	"	"	"	"		Н	"			В	"	"				
		"	90	"	"	"	"	"	"	"	"		"	"		"	A	"	"				
		"	91	В	Α	"	"	"	"	"	"		"	"	"	"	Α	"	"				
		"	92	"	"	"	"	"	"	"	"	"	"	Н	"	"	В	"	"				
		"	93	"	"	"	"	"	"	"	"	"	"	"		"	Α		"				
		. "	94	Α	"	"	"	"	"	"	"		"	"	"	"	Α		"				
			95	Α	. "	"	"		"	"	"		"	"	"		В	"	. "		- "		
8	Same te	ests and tern	nınal conditi	ons as for	subgroup	7 except T	$_{\rm C} = +125^{\circ}$	C and $T_{ m C}$ :	= -55°C.														

TABLE III. <u>Group A inspection for device type 05</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminal	conditio	ns (pins	not des	signated	l may b	e high ≥	2.0 V; Id	$5W \le 0.7$	v; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>		Min	Max	
9	t <sub>PHL1</sub>	3003	96	GND	GND	GND	OUT				GND					4.5 V	<u>6</u> /	IN	5.0 V	CLR to Q0	3	23	ns
Tc = 25°C		Fig. 5	97	4.5 V	GND	"		OUT			"					"	"	"	"	CLR to Q1	"	"	"
		"	98	GND	4.5 V	"			OUT		"					"	"	"	"	CLR to Q2	"	"	"
		"	99	4.5 V	4.5 V	"				OUT	"					"	"	"	"	CLR to Q3	"	"	"
		"	100	GND	GND	4.5 V					"	OUT				"	"		"	CLR to Q4		"	"
		"	101	4.5 V	GND	"					"		OUT			"		"	"	CLR to Q5	"	"	"
		"	102	GND	4.5 V	"					"			OUT		"			"	CLR to Q6		"	
		"	103	4.5 V	4.5 V	"					"				OUT	"	"	"	"	CLR to Q7	"	"	"
	t <sub>PLH2</sub>	"	104	GND	GND	GND	OUT				"					IN	GND	GND	"	DI to Q0		35	"
		"	105	4.5 V	GND	"		OUT			"					"	"	"	"	DI to Q1	"	"	
			106	GND	4.5 V	"			OUT		"								"	DI to Q2	-		
			107	4.5 V	4.5 V					OUT	- "					- :		- '	- "	DI to Q3		<u>"</u>	
			108	GND	GND	4.5 V					- "	OUT	OUT			-	-	-		DI to Q4	-	- "	
			109 110	4.5 V GND	GND 4.5 V								001	OUT		-			-	DI to Q5 DI to Q6	-	-	
		"	111	4.5 V	4.5 V									001	OUT					DI to Q6	-		
	t <sub>PHL2</sub>	"	112	GND	GND	GND	OUT								001				"	DI to Q0		25	"
	VPHL2	"	113	4.5 V	GND	UIVD "	001	OUT												DI to Q1		"	"
		"	114	GND	4.5 V			- 001	OUT											DI to Q2		"	"
		"	115	4.5 V	4.5 V	"				OUT	"						"	"	"	DI to Q3		"	"
		"	116	GND	GND	4.5 V					"	OUT					"	"	"	DI to Q4		"	"
		"	117	4.5 V	GND						"		OUT						"	DI to Q5		"	"
		"	118	GND	4.5 V	"					"			OUT			-	-	"	DI to Q6	"	"	"
		"	119	4.5 V	4.5 V										OUT		-	-	"	DI to Q7		"	"
	t <sub>PLH3</sub>	"	120	IN	GND	GND	OUT				"					4.5 V	-		"	A to Q0		32	"
		"	121	IN	GND	"		OUT			"					"	"	"	"	A to Q1		"	
		"	122	GND	IN	"			OUT		"							"	"	B to Q2			
		"	123	IN	4.5 V	"				OUT	"					"		"	"	A to Q3		"	
			124	GND	GND	IN					"	OUT							"	C to Q4	-		
			125	IN	GND	4.5 V							OUT				- "		- "	A to Q5			
			126	GND	IN									OUT	OUT				-:-	B to Q6		<del></del>	<del></del>
	4	"	127	IN	4.5 V	CND	OUT				- "				OUT	-	-	-		A to Q7	-	- "	
	t <sub>PHL3</sub>	"	128 129	IN IN	GND GND	GND "	OUT	OUT												A to Q0 A to Q1		"	
		"	130	GND	IN			501	OUT		"									B to Q2		"	"
		"	131	IN	4.5 V	"		1	- 551	OUT				1						A to Q3		"	"
		"	132	GND	GND	IN						OUT							-	C to Q4		"	"
		"	133	IN	GND	4.5 V					"		OUT						"	A to Q5		"	-
		"	134	GND	IN	"							-	OUT		"	"	"		B to Q6		"	"
		"	135	IN	4.5 V	"					"				OUT	"		"	"	A to Q7	"	"	"
	t <sub>PLH4</sub>	"	136	GND	GND	GND	OUT				"					"	IN	"	"	ENBL to Q0		29	"
		"	137	4.5 V	GND	"		OUT			"					"			"	ENBL to Q1			"
		"	138	GND	4.5 V				OUT								"	"	"	ENBL to Q2	"		"
		"	139	4.5 V	4.5 V	"				OUT	"					"	"	"	"	ENBL to Q3			"
		"	140	GND	GND	4.5 V					"	OUT				"	"	"	"	ENBL to Q4		"	"
			141	4.5 V	GND	"					"		OUT				"		"	ENBL to Q5			
			142	GND	4.5 V									OUT	0.12		-			ENBL to Q6	-		
		"	143	4.5 V	4.5 V										OUT				"	ENBL to Q7			. "

TABLE III. <u>Group A inspection for device type 05</u>. Terminal conditions (pins not designated may be high  $\geq$  2.0 V; low  $\leq$  0.7 V; or open).

						16	erminai	conditio	ns (pins	not des	signated	i may b	e high ≥	2.0 V; I	0.7	v; or op	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lin	nits	Unit
			Test no.	A	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>CC</sub>	-	Min	Max	
9	t <sub>PHL4</sub>	3003	144	GND	GND	GND	OUT				GND					4.5 V	IN	GND	5.0 V	ENBL to Q0	3	29	ns
T <sub>C</sub> = 25°C		Fig 5	145	4.5 V	GND	"		OUT			"					"	"	"	"	ENBL to Q1	"	"	
		"	146	GND	4.5 V	"			OUT		"					"	"	"	"	ENBL to Q2	"	"	
		"	147	4.5 V	4.5 V	"				OUT	"					"	"	"	"	ENBL to Q3	"	"	
		"	148	GND	GND	4.5 V					"	OUT				"	"	"	"	ENBL to Q4	"	"	
		"	149	4.5 V	GND	"					"		OUT			"			"	ENBL to Q5	"	"	
		"	150	GND	4.5 V	"					"			OUT		"	"	"	"	ENBL to Q6	"		
		"	151	4.5 V	4.5 V	"					"				OUT	"			"	ENBL to Q7	"	"	
	t <sub>PLH4</sub>	" <u>5</u> /	152	GND	GND	GND	OUT				"					IN	IN	4.5 V	"	ENBL to Q0	"	"	
	t <sub>PHL4</sub>	"	153	"	"	"	"				"					"	"	"	"	ENBL to Q0	"	"	
	t <sub>PLH4</sub>	"	154	"	"	"	"				"					"		"	"	ENBL to Q0	"		
	t <sub>PLH4</sub>	"	155	4.5 V	"	"		OUT			"					"		"	"	ENBL to Q1	"	"	
	t <sub>PHL4</sub>	"	156	"	"	"		"			"					"			"	ENBL to Q1	"		
	t <sub>PLH4</sub>	"	157	"	"	"		"			"					"		"	"	ENBL to Q1	"		
	t <sub>PLH4</sub>	"	158	GND	4.5 V	"			OUT		"					"			"	ENBL to Q2	"		
	t <sub>PHL4</sub>	"	159	"	"	"			"		"					"			"	ENBL to Q2	"		
	t <sub>PLH4</sub>	"	160	"	"	"			"		"					"		"	"	ENBL to Q2	"	"	
	t <sub>PLH4</sub>	"	161	4.5 V	"	"				OUT	"					"		"	"	ENBL to Q3	"		
	t <sub>PHL4</sub>	"	162	"	"	"				"	"					"		"	"	ENBL to Q3	"	"	
	t <sub>PLH4</sub>	"	163	"	"	"				"	"					"		"	"	ENBL to Q3	"		
	t <sub>PLH4</sub>	"	164	GND	GND	4.5 V					"	OUT				"	"	"	"	ENBL to Q4	"	"	
	t <sub>PHL4</sub>	"	165	"	"	"					"	"				"	"	"	"	ENBL to Q4	"	"	
	t <sub>PLH4</sub>	"	166	"	"	"					"	"				"	"	"	"	ENBL to Q4	"	"	
	t <sub>PLH4</sub>	"	167	4.5 V	"	"					"		OUT			"		"	"	ENBL to Q5	"		
	t <sub>PHL4</sub>	"	168	"	"	"					"		"			"	"	"	"	ENBL to Q5	"	"	
	t <sub>PLH4</sub>	"	169	"	"	"					"		"			"	"	"	"	ENBL to Q5	"	"	
	t <sub>PLH4</sub>	"	170	GND	4.5 V	"					"			OUT			"		"	ENBL to Q6	"	"	
	t <sub>PHL4</sub>	"	171	"	"	"					"			"		"			"	ENBL to Q6	"	"	
	t <sub>PLH4</sub>	"	172	"	"	"					"			"					"	ENBL to Q6	"	"	
	t <sub>PLH4</sub>	"	173	4.5 V	"	"					"				OUT	"	"		"	ENBL to Q7	"	"	
	t <sub>PHL4</sub>	"	174	"	"	"	<del>                                     </del>	<del>                                     </del>			"				"			"	"	ENBL to Q7	"	"	
	t <sub>PLH4</sub>	"	175	"	"	"	<del>                                     </del>	<del>                                     </del>			"				"	"			"	ENBL to Q7	"	"	
	t <sub>PHL4</sub>	"	176	IN	GND	GND		OUT			"					GND	"		"	ENBL to Q1		"	
	t <sub>PLH4</sub>	"	177	"	"	"		"			"					4.5 V			"	ENBL to Q1		"	
	t <sub>PHL4</sub>	"	178	"	"	"		"			"					GND			"	ENBL to Q1	"	"	

# TABLE III. Group A inspection for device type 05.

						Τe	erminal o						e high ≥			V: or on	en).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16				
Subgroup	Symbol	883 method	Cases 2, X	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Lim	iits	Unit
			Test no.	Α	В	С	Q0	Q1	Q2	Q3	GND	Q4	Q5	Q6	Q7	DATA IN	ENBL	CLR	V <sub>cc</sub>		Min	Max	
9	t <sub>PHL4</sub>	3003	179	GND	IN	GND			OUT		GND					GND	IN	4.5 V	5.0 V	ENBL to Q2	3	29	ns
T <sub>C</sub> = 25°C	t <sub>PLH4</sub>	Fig 5	180		"	"			"		"					4.5 V	"	"	"	ENBL to Q2	"		"
	t <sub>PHL4</sub>		181	"	"	"			"		"					GND	"	"	"	ENBL to Q2	"	"	"
	t <sub>PHL4</sub>		182	"	GND	IN						OUT				GND	"		"	ENBL to Q4			
	t <sub>PLH4</sub>		183	"	"	"					"	"				4.5 V	"	"	"	ENBL to Q4			"
	t <sub>PHL4</sub>		184	"	"	"					"	"				GND		"	"	ENBL to Q4		"	
	t <sub>PHL4</sub>		185	IN	"	GND	OUT				"					GND			"	ENBL to Q0		"	"
	t <sub>PLH4</sub>		186	"	"	"	"				"					4.5 V	"		"	ENBL to Q0	"	"	"
	t <sub>PHL4</sub>		187	"	"						"					GND			"	ENBL to Q0		"	
	t <sub>PHL4</sub>		188	4.5 V	IN	4.5 V							OUT			GND	"	"	"	ENBL to Q5		"	"
	t <sub>PLH4</sub>		189	"	"	"					"					4.5 V		"	"	ENBL to Q5			
	t <sub>PHL4</sub>		190	"	"	"					"		"			GND		"	"	ENBL to Q5		"	
	t <sub>PHL4</sub>		191	"	4.5 V	IN				OUT	"					GND		"	"	1		"	"
	t <sub>PLH4</sub>		192	"	"	"				"	"					4.5 V			"	ENBL to Q3		"	
	t <sub>PHL4</sub>		193	"	"	"				"	"					GND			"	ENBL to Q3		"	
10	t <sub>PHL1</sub>																			ENBL to Q3		30	
T <sub>C</sub> =125°C		Same test	ts and termi	nal conditi	ons as for	subgroup	9, except	T <sub>C</sub> = +125	°C and tes	t limits as	shown									į.		46	
-	t <sub>PHL2</sub>					- '		-													"	33	"
	t <sub>PLH3</sub>																				"	42	-

Same tests, terminal conditions and limits as for subgroup 10, except T<sub>C</sub> = -55°C. 1/ Apply a 3V/0V/3V momentary pulse 500 ns minimum prior to measurements.

- <u>2</u>/ A = 2.4 V, B = 0.4 V.
- $\underline{3}/ H \ge 1.5 V, L \le 1.5 V.$
- $\underline{4}/\ I_{IL}$  limits are as follows:

		Min/max li	mits (mA)	
Symbol	Circuit	B, C	С	ircuit A
	Devic	e 03	De	evice 05
I <sub>IL1</sub>	12/	36	=.	16/40
	Test 21	005/72	Test 21	005/-0.20

5/ See note 6 of figure 5.

\_\_\_ 4.5 v momentary pulse prior to each test. 6/ Apply

#### 5. PACKAGING

5.1 <u>Packaging requirements</u>. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

### 6. NOTES

(This section contains information of a general or explanatory nature which may be helpful, but is not mandatory.)

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
  - 6.2 Acquisition requirements. Acquisition documents should specify the following:
    - a. Title, number, and date of the specification.
    - b. Complete part number (see 1.2).
    - c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
    - d. Requirements for certificate of compliance, if applicable.
    - e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
    - Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
    - g. Requirements for product assurance options.
    - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
    - j. Requirements for "JAN" marking.
- 6.3 <u>Superseding information</u>. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.
- 6.4 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 <u>Abbreviations, symbols, and definitions.</u> The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential.
I <sub>IN</sub>	Current flowing into an input terminal.
V <sub>IN</sub>	Voltage level at an input terminal.
V <sub>SUH</sub>	Setup high
Vsui	Setup low

- 6.6 <u>Logistic support.</u> Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.
- 6.7 <u>Substitutability.</u> The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device	Generic-industry
type	type
01	54LS75
02	54LS279
03	54LS259
04	54LS375
05	54LS259B

6.8 <u>Manufacturers' designation.</u> Manufacturers' circuits, which form a part of this specification, are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturer's designator.

			CIRCUITS		
	Α	В	С	D	Е
Device type	Texas Instruments	Motorola Inc.	Fairchild Co.	Signetics Corp.	National Semconductor
01	Х	Х		Х	Х
02	Х	Х	Х	Х	Х
03	Χ	X	X		X
04	X	Х	X	X	
05	X				-

6.9 <u>Changes from previous issue.</u> Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians: Army - CR Navy - EC Air Force - 11 DLA - CC

Preparing activity: DLA - CC

(Project 5962-1965)

Review activities: Army - MI, SM Navy - AS, CG, MC, SH, TD Air Force - 03, 19, 99

# STANDARDIZATION DOCUMENT IMPROVEMENT PROPOSAL

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