

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add device type 02. Editorial changes throughout.	92-09-09	M. A. FRYE
B	Changes in accordance with N.O.R. 5962-R051-95.	94-12-23	M. A. FRYE
C	Drawing updated to reflect current requirements. Redrawn. - ro	05-12-15	R. MONNIN
D	Update drawing as part of 5 year review. - jt	12-12-10	C. SAFFLE

THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED.

REV																				
SHEET																				
REV																				
SHEET																				

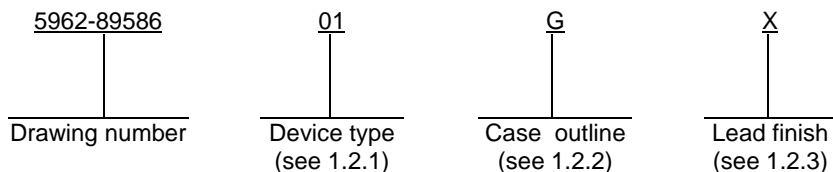
REV STATUS	REV	D	D	D	D	D	D	D	D	D	D	D				
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10					

PMIC N/A	PREPARED BY RICK C. OFFICER	<p align="center"><b>DLA LAND AND MARITIME</b>  <b>COLUMBUS, OHIO 43218-3990</b>  <a href="http://www.landandmaritime.dla.mil">http://www.landandmaritime.dla.mil</a></p>													
<p align="center"><b>STANDARD MICROCIRCUIT DRAWING</b></p> <p>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY CHARLES E. BESORE														
	APPROVED BY WILLIAM J. JOHNSON	<p align="center"><b>MICROCIRCUIT, LINEAR, SWITCHING REGULATOR, MONOLITHIC SILICON</b></p>													
	DRAWING APPROVAL DATE 89-09-25														
	REVISION LEVEL D	<table border="1"> <tr> <td>SIZE A</td> <td>CAGE CODE <b>67268</b></td> <td><b>5962-89586</b></td> </tr> </table>	SIZE A	CAGE CODE <b>67268</b>	<b>5962-89586</b>										
SIZE A	CAGE CODE <b>67268</b>	<b>5962-89586</b>													
		SHEET	1 OF 10												

1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device types. The device types identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	LM1578	Switching regulator
02	LM1578A	Switching regulator

1.2.2 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
G	MACY1-X8	8	Can

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

1.3 Absolute maximum ratings.

Total supply voltage (V <sub>IN</sub> ):	
Device type 01 .....	+40 V dc
Device type 02 .....	+50 V dc
Collector output to ground:	
Device type 01 .....	-0.3 V dc to +40 V dc
Device type 02 .....	-0.3 V dc to +50 V dc
Emitter output to ground:	
Device type 01 .....	-1 V dc to +40 V dc
Device type 02 .....	-1 V dc to +50 V dc
Output current .....	750 mA
Storage temperature range .....	-65°C to +150°C
Lead temperature (soldering, 10 seconds) .....	+260°C
Junction temperature (T <sub>J</sub> ) .....	+150°C
Power dissipation (P <sub>D</sub> ) .....	Internally limited
Thermal resistance, junction-to-case (θ <sub>JC</sub> ) .....	See MIL-STD-1835
Thermal resistance, junction-to-case (θ <sub>JA</sub> ) .....	20°C/W
	70°C/W (400 LFPM air flow flush mount)
	170°C/W (still air-flush mount)

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>2</b>

1.4 Recommended operating conditions.

Total supply voltage ( $V_{IN}$ ):

Device types 01 and 02 ..... +40 V dc

Ambient operating temperature range ( $T_A$ ) ..... -55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outlines. The case outline shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>3</b>

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T <sub>A</sub> ≤ +125°C unless otherwise specified		Group A subgroups	Device type	Limits		Unit
						Min	Max	
Oscillator frequency	F <sub>OSC</sub>	V <sub>IN</sub> = 2.0 V, C <sub>T</sub> = 4000 pF		1	All	17.6	22.4	kHz
				2		13.2	26.9	
		V <sub>IN</sub> = 2.2 V, C <sub>T</sub> = 4000 pF		3		13.2	26.9	
		V <sub>IN</sub> = 40 V, C <sub>T</sub> = 4000 pF		1		17.6	22.4	
				2,3		13.2	26.9	
Input reference voltage	V <sub>REF</sub>	1/ 2/ 3/ I <sub>1</sub> = I <sub>2</sub> = 0 mA, 25% duty cycle	V <sub>IN</sub> = 2.0 V	1	All	0.965	1.035	V
			V <sub>IN</sub> = 2.2 V	2		0.950	1.050	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1%, 25% duty cycle	V <sub>IN</sub> = 2.0 V	3		0.950	1.050	
			V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1%, 25% duty cycle	V <sub>IN</sub> = 2.0 V	2		0.950	1.050	
			V <sub>IN</sub> = 2.2 V	3		0.950	1.050	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 75% duty cycle	V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
			V <sub>IN</sub> = 2.0 V	2		0.950	1.050	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 75% duty cycle	V <sub>IN</sub> = 2.2 V	3		0.950	1.050	
			V <sub>IN</sub> = 2.2 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1%, 75% duty cycle	V <sub>IN</sub> = 2.0 V	2		0.950	1.050	
			V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1%, 75% duty cycle	V <sub>IN</sub> = 2.2 V	3		0.950	1.050	
			V <sub>IN</sub> = 2.2 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 1/ 2/ 3/ 25% duty cycle, V <sub>IN</sub> = 40 V	V <sub>IN</sub> = 2.0 V	2,3		0.950	1.050	
			V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, 25% duty cycle, V <sub>IN</sub> = 40 V	V <sub>IN</sub> = 2.0 V	2,3		0.950	1.050	
			V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 75% duty cycle, V <sub>IN</sub> = 40 V	V <sub>IN</sub> = 2.0 V	2,3		0.950	1.050	
			V <sub>IN</sub> = 2.0 V	1		0.965	1.035	
I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, 75% duty cycle, V <sub>IN</sub> = 40 V	V <sub>IN</sub> = 2.0 V	2,3	0.950	1.050				
	V <sub>IN</sub> = 2.0 V	1	0.965	1.035				

See footnotes at end of table.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

REVISION LEVEL  
**D**

**5962-89586**

SHEET  
**4**

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T <sub>A</sub> ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Input reference voltage line regulation	V <sub>Rline</sub>	I <sub>1</sub> = I <sub>2</sub> = 0 mA, 25% duty cycle, 2.0 V ≤ V <sub>IN</sub> ≤ 40 V <u>1/ 2/</u>	1	All		±3.8	mV
			2			±7.6	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 25% duty cycle, 2.2 V ≤ V <sub>IN</sub> ≤ 40 V <u>1/ 2/</u>	3			±7.6	
			I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, <u>1/ 2/</u> 25% duty cycle, 2.0 V ≤ V <sub>IN</sub> ≤ 40 V		1		
		2				±7.6	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, <u>1/ 2/</u> 25% duty cycle, 2.2 V ≤ V <sub>IN</sub> ≤ 40 V	3			±7.6	
			I <sub>1</sub> = I <sub>2</sub> = 0 mA, 75% duty cycle, 2.0 V ≤ V <sub>IN</sub> ≤ 40 V		1		
		2				±7.6	
		I <sub>1</sub> = I <sub>2</sub> = 0 mA, 75% duty cycle, 2.2 V ≤ V <sub>IN</sub> ≤ 40 V	3			±7.6	
			I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, 75% duty cycle, 2.0 V ≤ V <sub>IN</sub> ≤ 40 V		1		
		2				±7.6	
		I <sub>1</sub> = I <sub>2</sub> = 1 mA ±1 %, 75% duty cycle, 2.2 V ≤ V <sub>IN</sub> ≤ 40 V	3			±7.6	

See footnotes at end of table.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

**5962-89586**

REVISION LEVEL  
**D**

SHEET  
**5**

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T <sub>A</sub> ≤ +125°C unless otherwise specified		Group A subgroups	Device type	Limits		Unit	
						Min	Max		
Level shift accuracy		2/ I <sub>1</sub> = I <sub>2</sub> = 1 mA, 25% duty cycle	V <sub>IN</sub> = 2 V	1	All		±75	mV	
				2			±105		
			V <sub>IN</sub> = 2.2 V	3			±105		
				1			±75		
			I <sub>1</sub> = I <sub>2</sub> = 1 mA, V <sub>IN</sub> = 40 V, 25% duty cycle			1			±105
						2,3			
		I <sub>1</sub> = I <sub>2</sub> = 1 mA, 75% duty cycle	V <sub>IN</sub> = 2 V	1			±75		
				2			±105		
			V <sub>IN</sub> = 2.2 V	3			±105		
		I <sub>1</sub> = I <sub>2</sub> = 1 mA, V <sub>IN</sub> = 40 V, 75% duty cycle		1			±75		
				2,3			±105		
		Collector saturation voltage	V <sub>SATC</sub>	I <sub>C</sub> = 750 mA, emitter grounded		V <sub>IN</sub> = 2 V	1		All
2					1.2				
V <sub>IN</sub> = 2.2 V	3					1.2			
I <sub>C</sub> = 750 mA, V <sub>IN</sub> = 40 V, emitter grounded				1		0.85			
				2		1.2			
				3		1.2			
Emitter saturation voltage	V <sub>SATE</sub>	I <sub>E</sub> = 80 mA, collector at V <sub>IN</sub> = 40 V		1	All		1.6	V	
				2,3			2.1		
Collector leakage current	I <sub>L</sub>	Collector at V <sub>IN</sub> = 40 V, emitter grounded, output off		1	All		50	μA	
				2,3			100		
Sense voltage for shutdown	V <sub>SEN</sub>	V <sub>IN</sub> = 40 V, referred to V <sub>IN</sub> 4/		1,2,3	All	95	160	mV	
		V <sub>IN</sub> = 40 V, referred to GND 4/		1,2,3		95	160		

See footnotes at end of table.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

REVISION LEVEL  
**D**

**5962-89586**

SHEET  
**6**

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T <sub>A</sub> ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Supply current	I <sub>SY</sub>	Output off, V <sub>IN</sub> = 2.0 V 5/	1	All		3.0	mA
			2			3.3	
		Output off, V <sub>IN</sub> = 2.2 V 5/ V <sub>IN</sub> = 40 V	3			3.3	
			1			3.0	
			2,3			3.3	
Collector emitter sustaining voltage	BV <sub>CEO</sub> (SUS)	V <sub>C</sub> = 60 V, T <sub>A</sub> = +25°C, I <sub>C</sub> = 200 mA	1	02	50	60	V

1/ I1 and I2 are the external sink currents at the inputs.

2/ Input terminals are protected from accidental shorts to ground. If external voltages higher than the reference voltage are applied, excessive current will flow and should be limited to less than 5 mA.

3/ The input reference voltage is that voltage which, when applied to either the inverting or noninverting inputs (measured with respect to ground), will cause the output to switch on or off.

4/ Connection of a 10 k resistor from pin 1 to pin 4 establishes the duty cycle at its maximum of 90 percent with a maximum voltage swing on the output collector of 50 V. Applying the minimum current sense voltage will not reduce the duty cycle to less than 50 percent. Applying the maximum current limit sense voltage is certain to reduce the duty cycle below 50 percent. An additional 15 mV above the sense voltage may be required to reduce the duty cycle to 0 percent.

5/ The IC power supply excluding the output transistor's collector current, with oscillator operating.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime -VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>7</b>

Device types	01 and 02
Case outline	G
Terminal number	Terminal symbol
1	-INPUT
2	+INPUT
3	OSCILLATOR
4	GND
5	EMITTER
6	COLLECTOR
7	CURRENT LIMIT
8	V <sub>IN</sub>

FIGURE 1. Terminal connections.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>8</b>



4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7, 8, 9, 10 and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>9</b>

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3
Group A test requirements (method 5005)	1, 2, 3
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

\* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime -VA.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-89586</b>
		REVISION LEVEL <b>D</b>	SHEET <b>10</b>

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 12-12-10

Approved sources of supply for SMD 5962-89586 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8958601GA	3V146	LM1578H/883
5962-8958602GA	3V146	LM1578AH/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

3V146

Vendor name  
and address

Rochester Electronics Inc.  
16 Malcolm Hoyt Drive  
Newburyport, MA 01950

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.