

SN54ALS808A, SN54AS808B, SN74ALS808A, SN74AS808B

Hex 2-Input and Drivers

The SN74ALS808A and SN54AS808B are characterized for operation over the full military temperature range of -55°C to 125°C while the SN74ALS808A and SN74AS808B are characterized for operation from 0°C to 70°C. These devices contain six independent 2-input AND drivers. They perform the Boolean functions $Y = A \cdot B$ or $Y = \overline{A} + \overline{B}$ in positive logic.

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
 - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

SN54ALS808A, SN54AS808B, SN74ALS808A, SN74AS808B HEX 2-INPUT AND DRIVERS

D2661, DECEMBER 1982 - REVISED MAY 1986

•	High Capacitive Drive Capability	SN54ALS808A, SN54AS808B J PACKAGE SN74ALS808A, SN74AS808B DW OR N PACKAGE
•	'ALS808A has Typical Delay Time of 4.8 ns	(TOP VIEW)
	(CL = 50 pF) and Typical Power Dissipation of 4.5 mW per Gate	1A 1 20 VCC 1B 2 19 6B
•	'AS808B has Typical Delay Time of 3.2 ns	1Y 3 18 6A
	(CL = 50 pF) and Typical Power Dissipation	2A 4 17 6Y
	of Less than 13 mW per Gate	2B 5 16 5B
	Package Options Include Plastic "Small	2Y 6 15 5A
•	Outline'' Packages, Ceramic Chip Carriers,	3A 🛛 7 14 🖸 5Y
	and Standard Plastic and Ceramic 300-mil	3B 🗍 8 13 🗍 4B
		3Y 9 12 4A
	DIPs	GND 10 11 4Y
	Dependable Texas Instruments Quality and	Monte volta (all the second

 Dependable Texas Instruments Quality and Reliability

description

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These devices contain six independent 2-input AND drivers. They perform the Boolean functions $Y = A \cdot B$ or $Y = \overline{A} + \overline{B}$ in positive logic.

The SN54ALS808A and SN54AS808B are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN74ALS808A and SN74AS808B are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE (each driver)

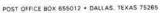
INP	UTS	OUTPUT
A	В	Y
н	н	н
L	x	L
x	L	L

logic symbol[†]

<u> </u>	80	(3) 1)
(2)		
(4)		(6)
(5)		(6) 21
(7)		(0)
(8)		(9) 31
(12)		(11)
(13)		(11) 4
(15)		(14)
(16)		(14) 5
(18)		(17)
(19)		6

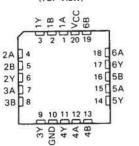
¹ This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Taxas instruments standard warrenty. Production pracessing does not necessarily include testing of all parameters.

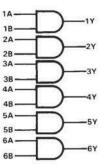


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SN54ALS808A, SN54AS808B ... FK PACKAGE (TOP VIEW)



logic diagram (positive logic)



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SN54ALS808A, SN74ALS808A **HEX 2-INPUT AND DRIVERS**

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

SN74ALS808A 0°C to 70°C

SN54ALS808A

MIN NOM MAX

5 5.5

0.7

- 12

12

125

4.5

- 55

2

SN74ALS808A

MIN NOM MAX

5 5.5

0.8

- 15

24

70

4.5

2

0

UNIT

V

V

V

mA

mA

°C

recommended operating conditions

Supply voltage

High-level input voltage

Low-level input voltage

High-level output current

Low-level output current

Operating free-air temperature

2
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Vcc

VIH

VIL

IOH

1OL

TA

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN54ALS808A			SN			
	1201 2010	TIONS	MIN	TYP [†]	MAX	MIN	TYP	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	$I_{1} = -18 \text{ mA}$			- 1.2			-1.2	V
	$V_{CC} = 4.5 V \text{ to } 5.5 V,$	$I_{OH} = -0.4 \text{ mA}$	Vcc-2			Vcc-2			-
	$V_{CC} = 4.5 V,$	10H = -3 mA	2.4	3.2		2.4	3.2		1
VOH	$V_{CC} = 4.5 V,$	IOH = -12 mA	2		30500	1	0726173	10.15	+ v
	$V_{CC} = 4.5 V$,	$I_{OL} = -15 \text{ mA}$				2			1
VOL	$V_{CC} = 4.5 V,$	IOL = 12 mA		0.25	0.4		0.25	0.4	1
	$V_{CC} = 4.5 V$,	$I_{OL} = 24 \text{ mA}$					0.35	0.5	v
h	$V_{CC} = 5.5 V_{,}$	$V_1 = 7 V$	-		0.1			0.1	mA
IH	$V_{CC} = 5.5 V$,	$V_1 = 2.7 V$			20	-	3	20	μΑ
IIL	V _{CC} = 5.5 V,	$V_1 = 0.4 V$	-	1012	-0.1		1.15	-0.1	mA
10 [‡]	V _{CC} = 5.5 V,	Vo = 2.25 V	- 30		-112	- 30		- 112	mA
ССН	V _{CC} = 5.5 V,	VI = 4.5 V		4.5	7	00	4.5	7	mA
CCL	V _{CC} = 5.5 V.	$V_1 = 0 V$		8	16		8	16	mA

[†]All typical values are at V_{CC} = 5 V. T_A = 25 °C [‡]The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOSswitching characteristics (see note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 V.$ $C_L = 50 pF.$ $R_L = 500 \Omega.$ $T_A = 25 °C$	C R	CC = 4.5 L = 50 pF $L = 500 \Omega$ A = MIN t	•	ι.	UNF			
					'ALS808A	SN54ALS808A SN74ALS80		LS808A	1		
			TYP	MIN	MAX	MIN	MAX				
TPLH TPHL	TPLH A or P	A or P	A or B Y	v	v	6	2	11	2	9	
	A U D	6.0 8 %	4	1	10	1	0	ns			

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



SN54AS808B, SN74AS808B HEX 2-INPUT AND DRIVERS

absolute maximum ratings over operating	ng free-air temperature range (unless otherwise noted)
Supply voltage Vcc	
leput voltage	
input voitage	SN54AS808B
Operating free-air temperature range:	SN34A5808B
	SN74AS808B
Storage temperature range	– 65°C to 150°C

recommended operating conditions

MIN 4.5	NOM 5	MAX 5.5	MIN 4.5	NOM	MAX	UNIT
4.5	5	5.5	45	1000		
			4.0	5	5.5	V
2			2	2516		V
		0.8			0.8	V
		-40			-48	mA
		40	1 - 164		48	mA
- 55	121	125	0		70	°C
	- 55	- 55	- 40 40	- 40 40	-40 40	-40 -48 40 48

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

19192 - 2010 - 1			SN	54AS8	08B	SM	74AS8	088	LINUT
PARAMETER	TEST CONDI	TEST CONDITIONS	MIN	TYPT	MAX	MIN	TYP [†]	MAX	UNIT
VIK	V _{CC} = 4.5 V,	$I_{I} = -18 \text{ mA}$			- 1.2			- 1.2	V
	V _{CC} = 4.5 V to 5.5 V.	10H = -2 mA	Vcc-2			V _{CC} -2			
55	V _{CC} = 4.5 V.	$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		
	V _{CC} = 4.5 V,	$I_{OH} = -40 \text{ mA}$	2		-				V
Vон	V _{CC} = 4.5 V,	$I_{OH} = -48 \text{ mA}$				2			
1000	V _{CC} = 4.5 V.	$I_{OL} = 40 \text{ mA}$		0.25	0.5				v
VOL	V _{CC} = 4.5 V.	$I_{OL} = 48 \text{ mA}$	2.4	110		-	0.35	0.5	
-4	$V_{CC} = 5.5 V_{c}$	$V_1 = 7 V$			0.1			0.1	mA
Чн	$V_{CC} = 5.5 V_{c}$	$V_{I} = 2.7 V$			20	1		20	μA
11	V _{CC} = 5.5 V.	$V_{1} = 0.4 V$			-0.5	r - 5		-0.5	mA
101	V _{CC} = 5.5 V.	$V_{O} = 2.25 V$	- 50		- 200	- 50	1011	- 200	mA
Іссн	V _{CC} = 5.5 V.	$V_1 = 4.5 V$		8	13		8	13	mA
ICCL	$V_{CC} = 5.5 V.$	V1 = 0 V		20	33		20	33	mA

[†]All typical values are at $V_{CC} = 5 V$, $T_A = 25 °C$ [‡]The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS-

switching characteristics (see note 1)

PARAMETER	FROM (INPUT)	то (оυтрит)		$V_{CC} = 4.5 V_{CL} = 50 \text{ pF.}$ $R_{L} = 500 \Omega$ $T_{A} = MIN \text{ tot}$			UNI
			SN54AS808B		SN74AS808B		
			MIN	MAX	MIN	MAX	
tpLH			1	6.5	1	6	ns
tPHL A or B	4	1	6.5	1	6] ""	

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



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