

TL510M, TL510C

Rochester Electronics®

Differential Comparators with Strobe

TL510 monolithic high-speed voltage comparator is an improved version of the TL710 with an extra stage added to increase voltage amplification and accuracy, and a strobe input for greater flexibility. Typical voltage amplification is 33,000. Since the output cannot be more positive than the strobe, a low-level input at the strobe will cause the output to go low regardless of the differential input. Component matching, inherent in integrated circuit fabrication techniques, produces a comparator with low-drift and low-offset characteristics. These circuits are particularly useful for applications requiring an amplitude discriminator, memory sense amplifier, or a high-speed limit detector.

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
 - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

LINEAR INTEGRATED CIRCUITS

TYPES TL510M, TL510C DIFFERENTIAL COMPARATORS WITH STROBE

D991, MARCH 1971-REVISED NOVEMBER 1983

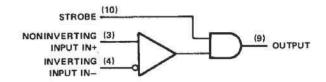
- Low Offset Characteristics
- High Differential Voltage Amplification
- Fast Response Times
- Output Compatible with Most TTL Circuits

description

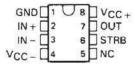
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The TL510M is characterized for operation over the full military temperature range of -55 °C to 125 °C; the TL510C is characterized for operation from 0 °C to 70 °C.

functional block diagram (positive logic)

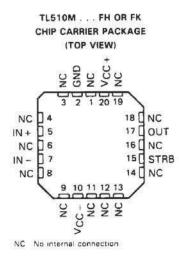


JG OR P DUAL-IN-LINE PACKAGE (TOP VIEW)



TL510M	•		•	U	FLAT	PACKAGE
	(Т	0	P	VIEW	6
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GNDL		10	INC
IN + [2	9]NC
IN - C	3	8	Vcc+
NC	4	7]STRB
Vcc - C	5	6]OUT



Voltage Comparators

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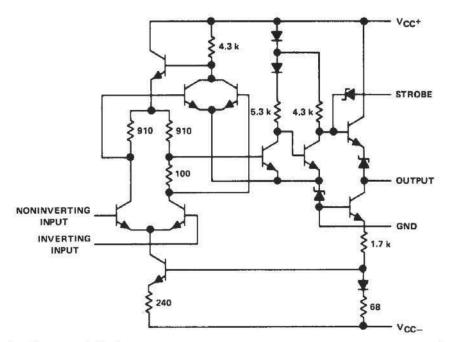
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schematic



Resistor values shown are nominal in ohms. Component values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage V _{CC+} (see Note 1)	14 V
Supply voltage VCC - (see Note 1)	-7 V
Differential input voltage (see Note 2)	±5 V
nput voltage (either input, see Note 1)	±7 V
Strobe voltage (see Note 1)	. 6 V
Peak output current ($t_w \le 1$ s)	10 mA
Continuous total power dissipation at (or below) 70 °C free-air temperature (see Note 3) 30	00 mW
Operating free-air temperature range: TL510M Circuits	125°C
TL510C Circuits	
Storage temperature range	150°C
ead temperature 1,6 mm (1/16 inch) from case for 60 seconds: FH, FK, JG, or Upackage	300 °C
ead temperature 1,6 mm (1/16 inch) from case for 10 seconds: P package	260°C

NOTES: 1. All voltage values, except differential voltages, are with respect to the network ground terminal.

2. Differential voltages are at the noninverting input terminal with respect to the inverting input terminal.

For operation of the TL510M above 70°C free-air temperature, refer to Dissipation Derating Curves, Section 2. In the JG package, TL510M chips are alloy mounted and TL510C chips are glass mounted.



DADAMETED		TEST CONDIT			TL510M			TL510C		
PARAMETER		TEST CONDITIONS [†]			MIN TYP	MAX	MIN	TYP	MAX	UNIT
VID Input offset voltage		$R_{S} \leq 200 \Omega$	25°C		0.6	2		1.6	3.5	
v10	input onset voltage	See Note 4	Full range			3			4.5	m∨
Average temperature avio coefficient of input offset voltage	$R_{S} = 50 \Omega$,	MIN to 25°C		3	10		3	20	μV/°	
	See Note 4	25°C to MAX	· · ·	3	10		3	20		
			25°C	1	0.75	3		1.8	5	
IIO Input offset current	See Note 4	MIN		1.8	7			7.5	μA	
2.5			MAX		0.25	3				1.00
αliO	Average temperature coefficient of input	See Note 4	MIN to 25 °C		15	75		24	100	nA/°(
n¢	offset current		25°C to MAX		5	25		15	50	
1000	A CONTRACTOR OF A CONTRACTOR	0 11 1	25°C		7	15		7	20	μA
IB	Input bias current	See Note 4	MIN		12	25		9	30	
I _{IH(S)}	High-level strobe current	$V_{(strobe)} = 5 V,$ $V_{ID} = -5 mV$	25°C			±100			±100	μA
IL(S)	Low-level strobe current	$V_{(strobe)} = -100 \text{ mV},$ $V_{ID} = 5 \text{ mV}$	25 °C		-1	- 2.5		-1	- 2.5	mA
VICR	Common-mode input voltage range	$V_{CC-} = -7 V$	Full range	± 5			± 5			v
VID	Differential input voltage range		Full range	± 5			± 5			v
A	Large-signal differential	No load,	25°C	12.5	33		10	33		Vim
AVD	voltage amplification	$V_0 = 0$ to 2.5 V	Full range	10		~	8			1 vm
VOH High-level output voltage	$V_{ID} = 5 \text{ mV},$ $I_{OH} = 0$	Full range		4‡	5		4‡	5	v	
	$V_{ID} = 5 \text{ mV},$ $I_{OH} = -5 \text{ mA}$	Full range	2.5	3.6 [‡]		2.5	3.6‡		v	
		$V_{ID} = -5 \text{ mV},$ $I_{OL} = 0$	Full range	- 1	-0.5‡	0§	- 1	-0.5‡	0§	v
V _{OL} Low-level output voltage	$V_{(strobe)} = 0.3 V,$ $V_{ID} = 5 mV,$ $I_{OL} = 0$	Full range	- 1		0§	- 1		O§	v	
IOL Low-level output current	$V_{ID} = -5 \text{ mV},$ $V_O = 0$	25°C	2	2.4		1.6	2.4		mA	
		MIN	1	2.3		0.5	2.4			
		MAX	0.5	2.3		0.5	2.4			
ro	Output resistance	V ₀ = 1.4 V	25 °C	-	200			200		Ω
CMRR	Common-mode rejection ratio	R _S ≤ 200 Ω	Full range	80	100 [‡]		70	100 [‡]		dB
+ 33	Supply current from VCC +	$V_{ID} = -5 \text{ mV},$	Full range		5.5‡	9	1.11	5.5 [‡]	9	mA
CC -	Supply current from VCC -	VID = -5 mV, No load	Full range		-3.5‡	- 7		-3.5‡	- 7	mA
PD	Total power dissipation		Full range		90 [‡]	150		90‡	150	mW

electrical characteristics at specified free-air temperature, V_{CC+} = 12 V, V_{CC-} = -6 V (unless otherwise noted)

[†]Unless otherwise noted, all characteristics are measured with the strobe open. Full range (MIN to MAX) for TL510M is -55°C to 125°C and for the TL510C is 0°C to 70°C. [‡]These typical values are at $T_A = 25$ °C.

The algebraic convention, where the most-positive (least negative) limit is designated as maximum, is used in this data sheet for logic levels only, e.g., when

NOTE 4: These characteristics are verified by measurements at the following temperatures and output voltage levels: for TL510M, $V_{Q} = 1.8$ V at $T_{A} = -55$ °C, $V_{Q} = 1.4$ V at $T_{A} = 25$ °C, and $V_{Q} = 1$ V at $T_{A} = 125$ °C; for TL510C, $V_{Q} = 1.5$ V at $T_{A} = 0$ °C, $V_{Q} = 1.4$ V at 25 °C, and $V_{Q} = 1.2$ V at $T_{A} = 70$ °C. These output voltage levels where selected to approximate the logic threshold voltages of the types of digital logic circuits these comparators are intended to drive.

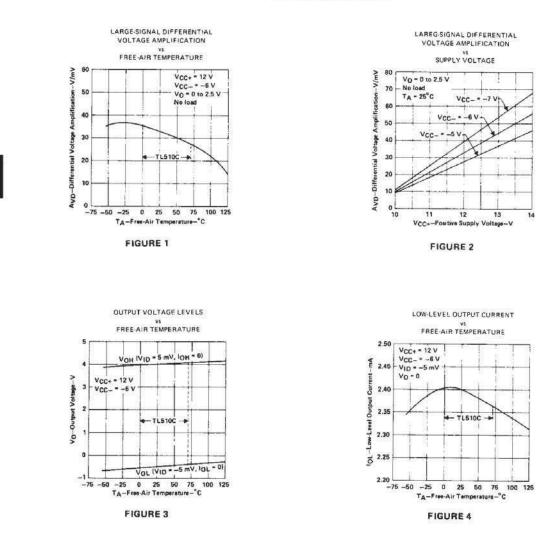
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switching characteristics, $V_{CC+} = 12 V$, $V_{CC-} = -6 V$, $T_A = 25 °C$

PARAMETER	TE	MIN	TYP	MAX	UNIT		
Response time	RL = ∞	$C_L = 5 pF$,	See Note 5		30	80	ns
Strobe release time	RL = ∞	CL = 5 pF.	See Note 6		5	25	ns

NOTES: 5. The response time specified is for a 100-mV input step with 5-mV overdrive.

6. For testing purposes, the input bias conditions are selected to produce an output voltage of 1.4 V. A 5-mV overdrive is then added to the input bias voltage to produce an output voltage that rises above 1.4 V. The time interval is measured from the 50% point of the strobe voltage curve to the point where the overdriven output voltage crosses the 1.4 V level.



TYPICAL CHARACTERISTICS

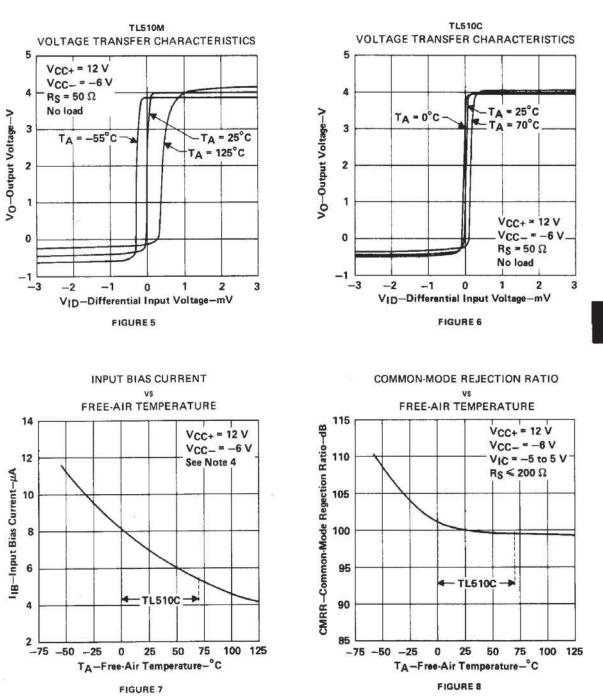
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Voltage Comparators

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TYPICAL CHARACTERISTICS

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Voltage Comparators

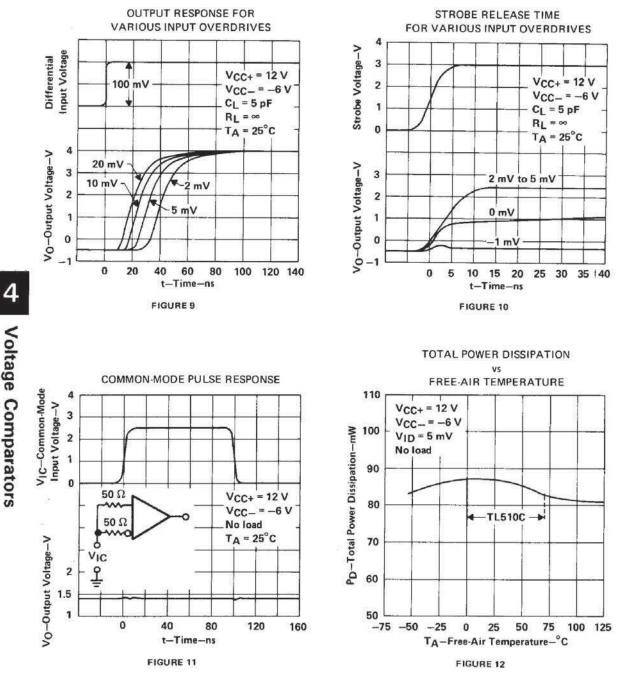
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TYPICAL CHARACTERISTICS

Voltage Comparators

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