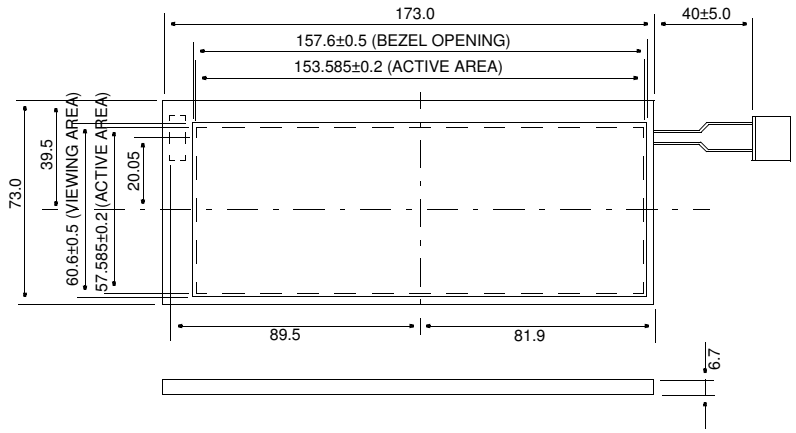
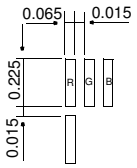
HDM6424-C

640 X 240 Dots Color Graphic w/ CCFL Backlight

Dimensional Drawing





CONNECTOR INFORMATION:
INTERFACE:FH12-20S-0.5SH (HIROSE)
MATING:FFC, N20, PITCH 0.5MM
CCFL: HV-2S-C1 (JAE)
MATING: HV-2P-HF-E1400 (JAE)

Dimension tolerance: +/-0.3mm

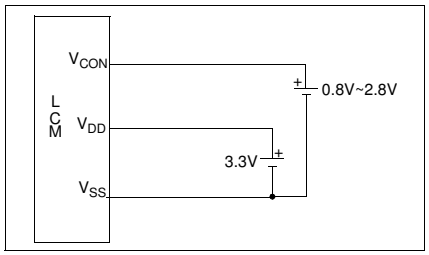
Features

Backlight.....CCFL
Options.....Color RGB
Bottom Viewing
Built-in Controller.....None

Physical Data

Module Size.....173.0W x 73.0H x 6.7T mm
Viewing Area Size.....156.6W x 60.6H mm
Dot Pitch.....0.08W x 0.24H mm
Dot Size.....0.065W x 0.225H mm
Weight.....123g

Power Supply



Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
SUPPLY VOLTAGE	V_{DD}-V_{SS}	0	6.0	V
SUPPLY VOLTAGE FOR LCD	V_{EE}-V_{SS}	0	42.0	V
INPUT VOLTAGE	V_{IN}	-0.3	V_{DD}+0.3	V
V_{CON} VOLTAGE	V_{CON}	0	V_{DD}	V
AMBIENT TEMPERATURE (OPERATING)	T_{OP}	0	50	°C
AMBIENT TEMPERATURE (STORAGE)	T_{STG}	-20	70	°C

Electrical Characteristics (VDD=3.3±0.25V 25°C)

PARAMETER	SYM	CONDITION	MIN	TYP	MAX	UNIT
INPUT HIGH VOLTAGE	V_{IH}	-	0.8 V_{DD}	-	V_{DD}	V
INPUT LOW VOLTAGE	V_{IL}	-	0	-	0.2 VDD	V
SUPPLY VOLTAGE	V_{DD}	-	3.0	3.3	3.6	V
	V_{CON}	25°C	0.8	1.95	2.8	V
LOGIC CURRENT	I_{DD}	V_{DD}=3.3V	-	60	150	mA
SURFACE LUMINANCE	L	IL=1.5mA	-	77	-	NIT
DRIVE METHOD	1/240 DUTY					

Pin Connections

PIN NO.	SYMBOL	LEVEL	FUNCTION
DATA CONNECTOR			
1	FLM	H	First Line Marker
2	M	H/L	AC signal for LCD
3	CL1	H/L	Latch pulse
4	V_{SS}	0V	Ground
5	CL2	H/L	Data shift clock
6	V_{SS}	0V	Ground
7	DISPOFF	H/L	H=On, L=Off
8	V_{DD}	3.3V	Logic Power Supply
9	V_{CON}	-	Contrast Adjustment
10	V_{SS}	0V	Ground
11	D0	H/L	Display Data
12	D1	H/L	
13	D2	H/L	
14	D3	H/L	
15	V_{SS}	0V	Ground
16	D4	H/L	Display Data
17	D5	H/L	
18	D6	H/L	
19	D7	H/L	
20	V_{SS}	0V	Ground
CCFL CONNECTOR			
1	V_{CFL}	-	Power supply for CCFL
2	V_{CFL}	-	CCFL Ground