



Product data sheet

### 1. Product profile

#### 1.1 General description

Planar passivated four quadrant triac in a SOT78 plastic package intended for use in general purpose bidirectional switching and phase control applications.

#### **1.2 Features and benefits**

- High blocking voltage capability
- Least sensitive gate for highest noise immunity

#### **1.3 Applications**

General purpose motor control

#### 1.4 Quick reference data

- Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants
- General purpose switching

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	-	600	V
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 $ °C; $t_p = 20$ ms; see <u>Figure 4</u> ; see <u>Figure 5</u>	-	-	65	A
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 102 ℃; see <u>Figure 1</u> ; see <u>Figure 2;</u> see <u>Figure 3</u>	-	-	8	A
Static cha	aracteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	5	50	mA
		$V_D = 12 V; I_T = 0.1 A; T2+ G-;$ $T_j = 25 °C; see Figure 7$	-	8	50	mA
		$V_D = 12 V; I_T = 0.1 A; T2-G-;$ $T_j = 25 °C; see Figure 7$	-	11	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>i</sub> = 25 °C; see <u>Figure 7</u>	-	30	100	mA





# 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		N 1
2	T2	main terminal 2	mb	T2-T1
3	G	gate		Sym051
mb	Τ2	mounting base; main terminal 2		
			SOT78 (TO-220AB)	

## 3. Ordering information

#### Table 3.Ordering information

Type number	Package		
	Name	Description	Version
BT137-600G	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

BT137-600G

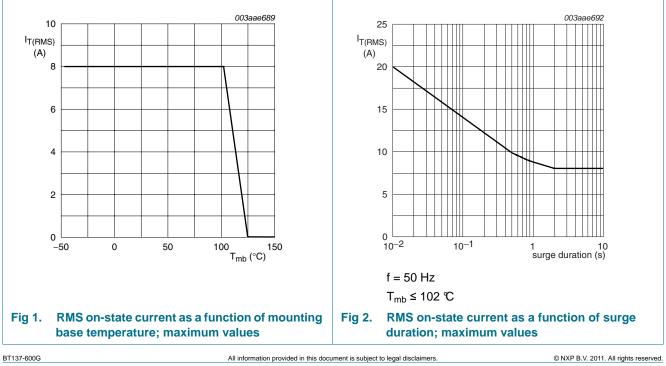
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## 4. Limiting values

#### Table 4. Limiting values

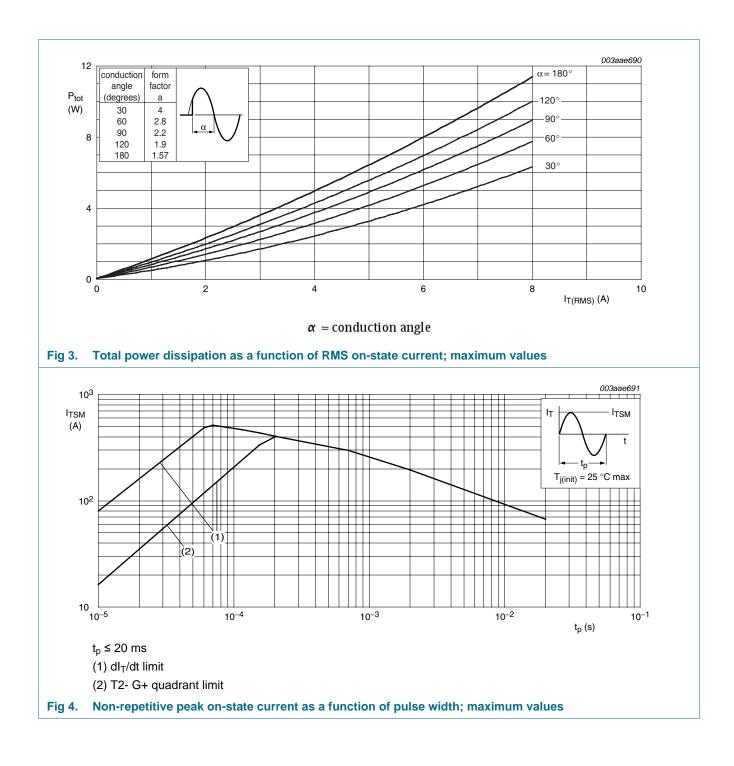
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 102 ℃; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	8	A
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; see <u>Figure 4</u> ; see <u>Figure 5</u>	-	65	А
		full sine wave; $T_{j(init)} = 25 \ C$ ; $t_p = 16.7 \ ms$	-	71	А
l <sup>2</sup> t	l <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	-	21	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>T</sub> = 12 A; I <sub>G</sub> = 0.2 A; dI <sub>G</sub> /dt = 0.2 A/μs; T2+ G+	-	50	A/µs
		$I_T$ = 12 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2+ G-	-	50	A/µs
		$I_T$ = 12 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2- G-	-	50	A/µs
		$I_T$ = 12 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2- G+	-	10	A/µs
I <sub>GM</sub>	peak gate current		-	2	А
V <sub>GM</sub>	peak gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	C
Tj	junction temperature		-	125	C



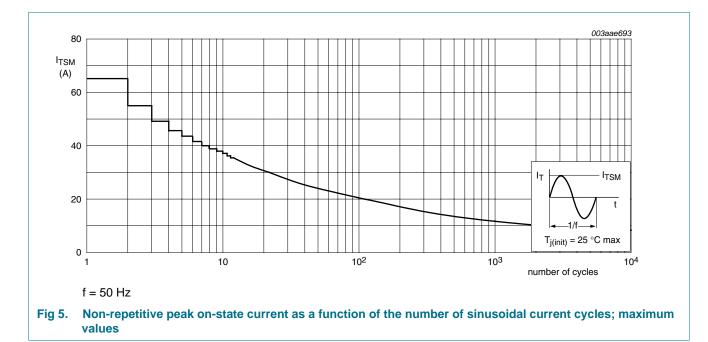
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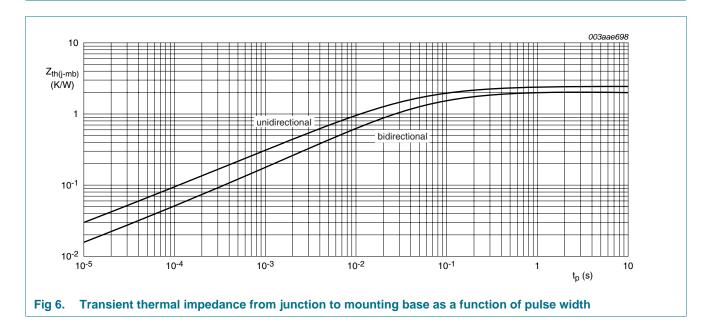
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### 5. Thermal characteristics

#### Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance from junction to mounting base	half cycle; see Figure 6	-	-	2.4	K/W
		full cycle; see Figure 6	-	-	2	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W



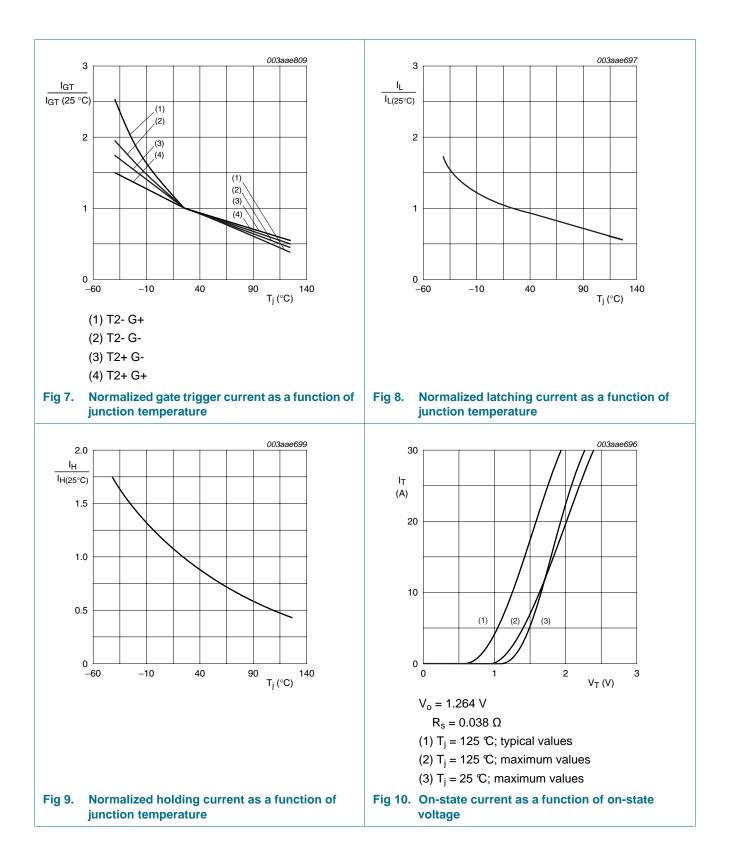
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### 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static characteristics						
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 ℃; see <u>Figure 7</u>	-	5	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 ℃; see <u>Figure 7</u>	-	8	50	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ C}; \text{ see } \frac{\text{Figure 7}}{2}$	-	11	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 ℃; see <u>Figure 7</u>	-	30	100	mA
IL	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 ℃; see <u>Figure 8</u>	-	7	45	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 ℃; see <u>Figure 8</u>	-	16	60	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 ℃; see <u>Figure 8</u>	-	5	45	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 ℃; see <u>Figure 8</u>	-	7	60	mA
I <sub>H</sub>	holding current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ C}; \text{ see } Figure 9$	-	5	40	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 10 A; T <sub>j</sub> = 25 °C; see <u>Figure 10</u>	-	1.3	1.65	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 ℃; see <u>Figure 11</u>	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ C};$ see <u>Figure 11</u>	0.25	0.4	-	V
I <sub>D</sub>	off-state current	$V_D = 600 \text{ V}; \text{ T}_j = 125 \text{ °C}$	-	0.1	0.5	mA
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; exponential waveform; gate open circuit	200	250	-	V/µs
dV <sub>com</sub> /dt	rate of change of commutating voltage	$V_D = 400 \text{ V}; \text{ T}_j = 95 \text{ C};$ $dI_{com}/dt = 3.6 \text{ A/ms}; \text{ I}_T = 8 \text{ A}; \text{ gate open circuit}$	10	20	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$\begin{split} I_{TM} &= 12 \text{ A};  V_{\text{D}} = 600  \text{V};  I_{\text{G}} = 0.1  \text{mA}; \\ \text{d} I_{\text{G}}/\text{d} t = 5  \text{A}/\mu\text{s} \end{split}$	-	2	-	μs

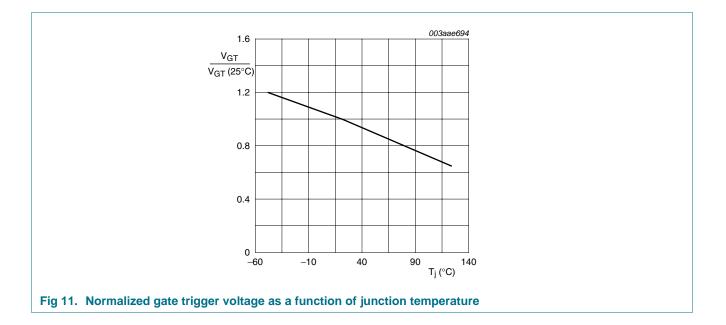
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#### **Package outline** 7.

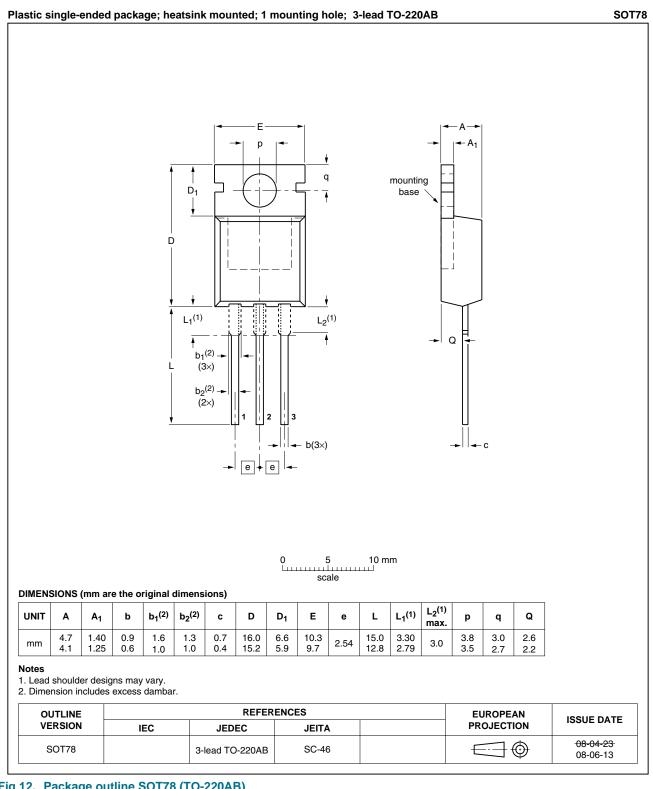


Fig 12. Package outline SOT78 (TO-220AB)

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# 8. Revision history

Table 7.Revision	history				
Document ID	Release date	Data sheet status	Change notice	Supersedes	
BT137-600G v.4	20110324	Product data sheet	-	BT137_SERIES_3	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> </ul>				
	<ul> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>				
	<ul> <li>Type number</li> </ul>	BT137-600G separated from	data sheet BT137_SE	ERIES_3.	
BT137_SERIES_3	20010601	Product specification	-	BT137_SERIES_2	

### 9. Legal information

#### 9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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