**Product data sheet** 



# 1. Product profile

### 1.1 General description

Planar passivated four quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

### **1.2 Features and benefits**

- High blocking voltage capability
- Least sensitive gate for highest noise immunity
- Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants

### **1.3 Applications**

- General purpose motor control
- General purpose switching

## 1.4 Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DRM</sub>	repetitive peak off- state voltage		-	-	600	V
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 20 \text{ ms}$ ; <u>Fig. 4</u> ; <u>Fig. 5</u>	-	-	95	A
Tj	junction temperature		-	-	125	°C
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 99 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	12	A
Static chara	acteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	5	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	8	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	10	50	mA

#### Table 1. Quick reference data





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#### 4Q Triac

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ T <sub>j</sub> = 25 °C; Fig. 7	-	22	100	mA
Dynamic char	acteristics					,
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	200	250	-	V/µs

# 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2-2-T1
2	T2	main terminal 2		sym051
3	G	gate		
mb	Τ2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

# 3. Ordering information

Table 3. Ordering inf	formation		
Type number	Package		
	Name	Description	Version
BT138-600G	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

## 4. Limiting values

#### Table 4.Limiting values

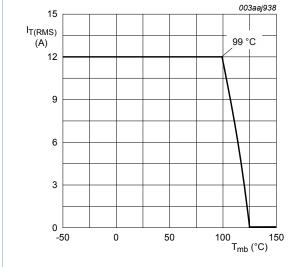
In accordance with the Absolute Maximum Rating System (IEC 60134).

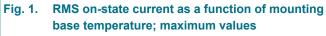
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 99 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	12	A
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	95	A

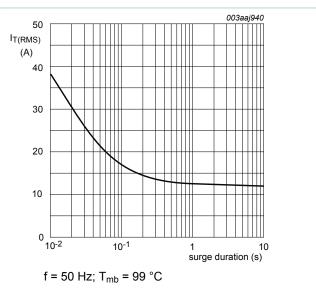
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Symbol	Parameter	Conditions	Min	Мах	Unit
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ t <sub>p</sub> = 16.7 ms	-	105	A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	-	45	A <sup>2</sup> s
dI <sub>T</sub> /dt	rate of rise of on-state current	$I_T$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2+ G+	-	50	A/µs
		$I_T$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2+ G-	-	50	A/µs
		$I_T$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2- G-	-	50	A/µs
		$I_T$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs; T2- G+	-	10	A/µs
I <sub>GM</sub>	peak gate current		-	2	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



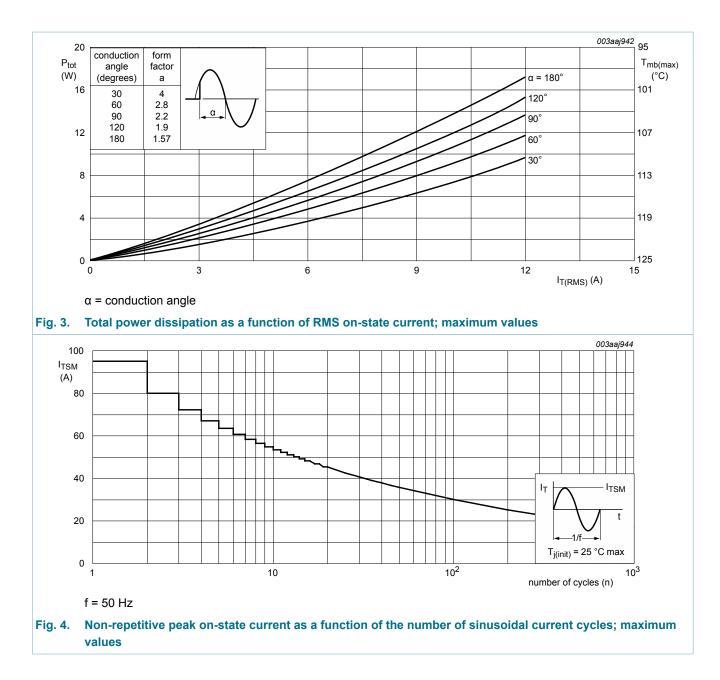






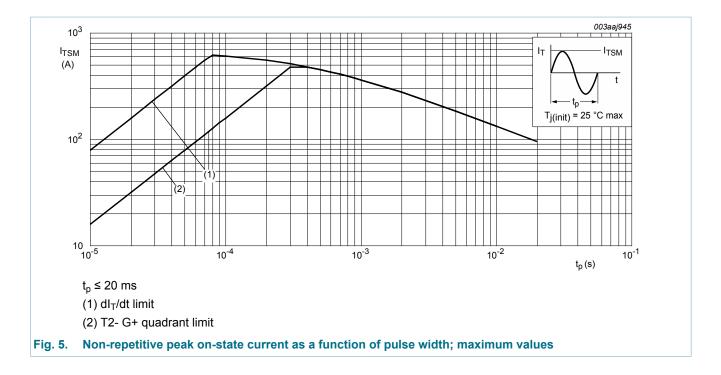


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#### 4Q Triac



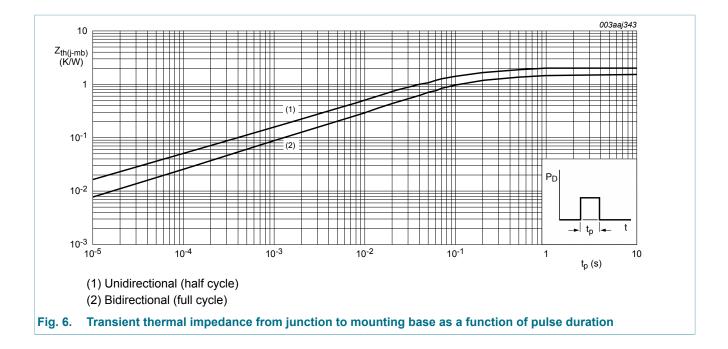
## 5. Thermal characteristics

#### Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
R <sub>th(j-mb)</sub>	thermal resistance	full cycle; <u>Fig. 6</u>	-	-	1.5	K/W
	from junction to mounting base	half cycle; <u>Fig. 6</u>	-	-	2	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W

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# 6. Characteristics

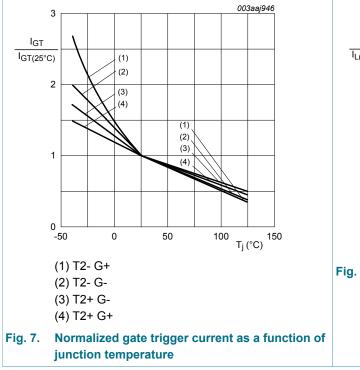
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static char	acteristics	,				_
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	5	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	8	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	10	50	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	22	100	mA
IL	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	7	60	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	20	90	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	8	60	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	10	90	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	6	60	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 15 A; T <sub>i</sub> = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	0.7	1.5	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 125 °C; Fig. 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic chara	acteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	200	250	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM}$ = 16 A; V <sub>D</sub> = 600 V; I <sub>G</sub> = 0.1 A; dI <sub>G</sub> / dt = 5 A/µs	-	2	-	μs



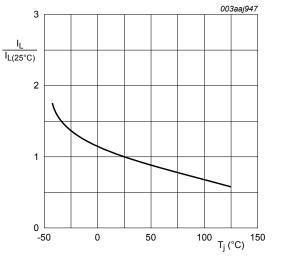
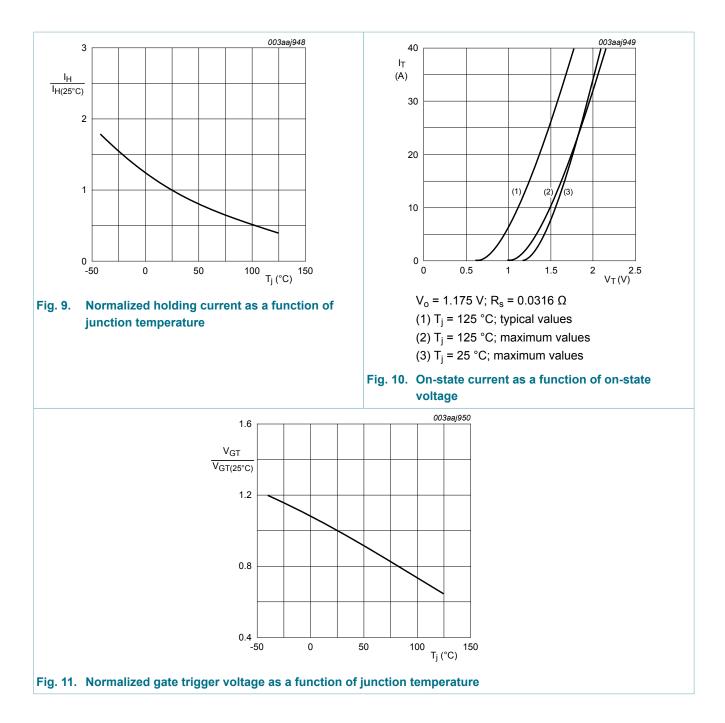


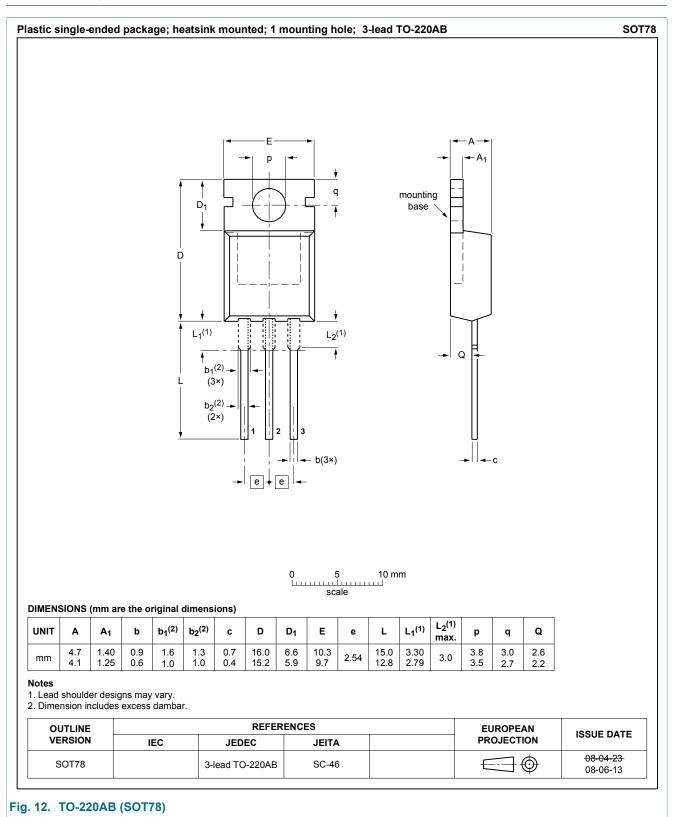
Fig. 8. Normalized latching current as a function of junction temperature

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## 7. Package outline



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## 8. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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