



ACE520C

Dual 300mA High PSRR Linear Regulator

Description

ACE520C dual, low-noise, low-dropout regulator delivers at least 300mA of continuous output current. The output voltage for each regulator is set independently by trimming. Output voltages are selectable in 100mV steps within a range of 1.0V to 4.5V. Typical output noise is 47uV_{rms}, and PSRR is 73dB at 100Hz. The ACE520C includes two independent logic-controlled shutdown inputs and allows the output of each regulator to be turned off independently.

The ACE520C includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module.

The ACE520C has excellent load and line transient response and good temperature characteristics, which can assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$.

Features

- Two low dropout voltage regulators
- 300mA output current for each LDO
- 35uA operating supply current per LDO
- Low 47uV_{RMS} output noise
- Standby Mode: 0.1uA
- Low 300mV dropout at 300mA load
- 73dB PSRR at 100Hz
- Excellent Line regulation: 0.05%/V
- Independent Shutdown controls
- 1.0V to 4.5V Factory-Preset Output
- Output Current Limit
- Highly Accurate: $\pm 2\%$ ($\pm 1\%$ customized)

Application

- Cellular phones
- Cordless phones and radio communication equipment
- Battery Powered equipment
- Notebook and hand-ole equipment
- Wireless LAN
- GPS receivers

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Input voltage	V _{IN}	8	V
Power Dissipation SOT-23-6		250	mW
Junction temperature	T _J	125	°C
Storage temperature	T _S	- 45 to 150	°C
Ambient Temperature	T _A	-40 to 85	°C

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

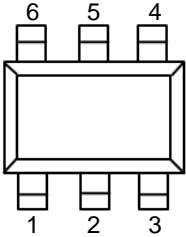


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Packaging Type

SOT-23-6



SOT-23-6(D)	SOT-23-6(E)	Description	Function
6	6	V_{out1}	Output 1 pin
2	5	V_{dd}	Input pin
4	1	V_{out2}	Output 2 pin
3	3	CE2	Chip Enable pin2
5	2	GND	Ground pin
1	4	CE1	Chip Enable pin1

Ordering information

ACE520C X XXX + H

Halogen - free

Pb - free

DGM : SOT-23-6(D)

EGM : SOT-23-6(E)

Output Voltage :

B:1.5V(Output1),2.8V(Output2)

C:1.5V(Output1),3.0V(Output2)

D:1.5V(Output1),3.3V(Output2)

E:1.5V(Output1),4.0V(Output2)

G:1.8V(Output1),2.5V(Output2)

H:1.8V(Output1),2.8V(Output2)

I: 1.8V(Output1),3.0V(Output2)

J:1.8V(Output1),3.3V(Output2)

M:2.8V(Output1),1.8V(Output2)

O:2.8V(Output1),2.8V(Output2)

P:2.8V(Output1),3.0V(Output2)

Q:3.0V(Output1),2.5V(Output2)

R:3.0V(Output1),3.0V(Output2)

S:3.0V(Output1),3.3V(Output2)

T:3.3V(Output1),1.8V(Output2)

U:3.3V(Output1),3.3V(Output2)

Y:2.8V(Output1),3.3V(Output2)

a:1.2V(Output1),2.5V(Output2)

b:2.8V(Output1),1.2V(Output2)

c:2.8V(Output1),1.3V(Output2)

d:2.5V(Output1),2.8V(Output2)

e:2.5V(Output1),3.3V(Output2)

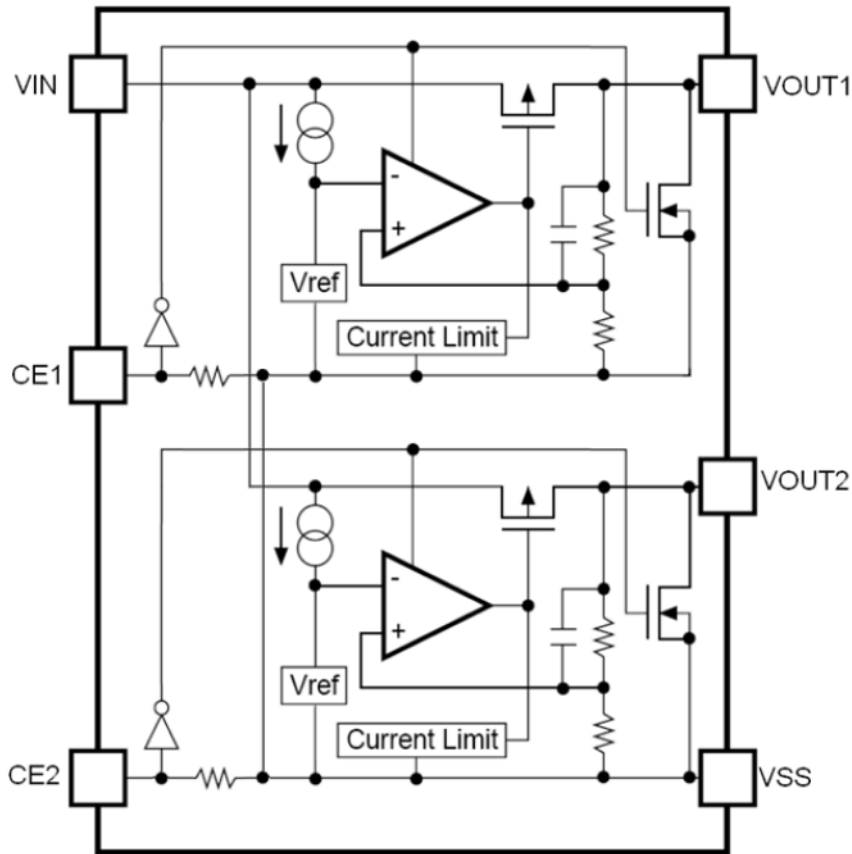
g: 1.3V(Output1),2.8V(Output2)

x: 2.8V(Output1),1.5V(Output2)

h: 3.3V(Output1),2.8V(Output2)



Block Diagram



Explanation

ACE520C series are highly accurate, dual, low noise, CMOS low dropout voltage regulators.

The output voltage for each regulator is set independently by trimming. Voltages are selectable in 100mV steps within a range of 1.2V to 4.5V. It also can be customized on command.

ACE520C includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module.

ACE520C has excellent load and line transient response and good temperature characteristics, which can assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$.

Recommended Work Conditions

Item	Min	Max	Unit
Input Voltage Range		6	V
Ambient Temperature	-40	85	°C



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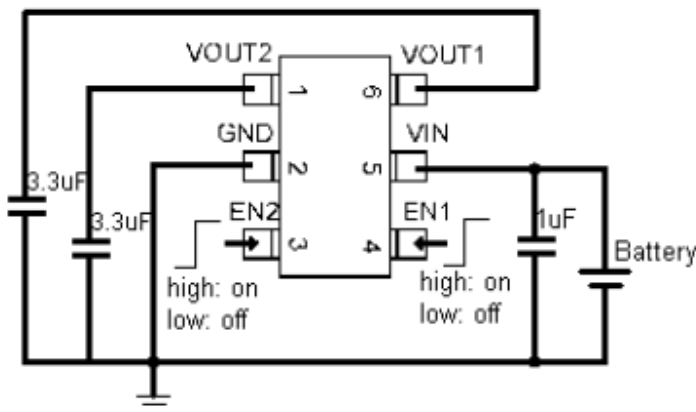
Electrical Characteristics

(Test Conditions: $C_1=1\mu\text{F}$, $C_{out}=3.3\mu\text{F}$, $T_A=25^\circ\text{C}$, unless otherwise specified.)

ACE520C, For arbitrary output voltage

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Input Voltage	V_{IN}				6	V
Output Voltage	V_{OUT}	$V_{in}=\text{Set } V_{out}+1\text{V}$ $1\text{mA} \leq I_{out} \leq 30\text{mA}$	$V_{out} \times 0.98$	$V_{out}1$	$V_{out} \times 1.02$	V
Maximum Output Current	$I_{OUT}(\text{Max.})$	$V_{in}-V_{out}=1\text{V}$	300			mA
Dropout Voltage, $V_{out} \geq 2.8\text{V}$	V_{drop}	$I_{out}=100\text{mA}$		100	150	mV
		$I_{out}=300\text{mA}$		300	400	
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN}} \cdot V_{OUT}$	$I_{out}=40\text{mA}$ $2.8\text{V} \leq V_{in} \leq 6\text{V}$		0.05	0.2	%/V
Load Regulation	ΔV_{OUT}	$V_{in}=\text{Set } V_{out}+1\text{V}$ $1\text{mA} \leq I_{out} \leq 300\text{mA}$		50	80	mV
Supply Current	I_{ss}	$V_{in}=\text{Set } V_{out}+1\text{V}$		35	800	μA
Supply Current (Standby)	$I_{standby}$	$V_{in}=\text{Set } V_{out}+1\text{V}$ $V_{ce}=\text{Gnd}$		0.1	1.0	μA
Output Voltage Temperature Coefficiency	$\frac{\Delta V_{OUT}}{\Delta T} \cdot V_{OUT}$	$I_{out}=30\text{mA}$		± 100		ppm/ $^\circ\text{C}$
Ripple Rejection	PSRR	$F=100\text{Hz}$ Ripple=0.5Vp-p $V_{in}=\text{Set } V_{out}+1\text{V}$		70		dB
Short Current Limit	I_{lim}	$V_{out}=0\text{V}$	300			mA
CE Input Voltage "H"	V_{ceh}		1.5		V_{in}	V
CE Input Voltage "L"	V_{cel}		0		0.25	V
Output Noise	e_n	$\text{BW}=10\text{Hz} \sim 100\text{kHz}$		47		μVrms
Discharge Resistor	$R_{discharge}$	$\text{CE}=0\text{V}$, $V_{out}=3.0\text{V}$		1.5K		Ω
CE pin pull down resistor	R_{cepd}	$\text{CE}=V_{in}=5\text{V}$		500K		Ω

Typical Application Circuit

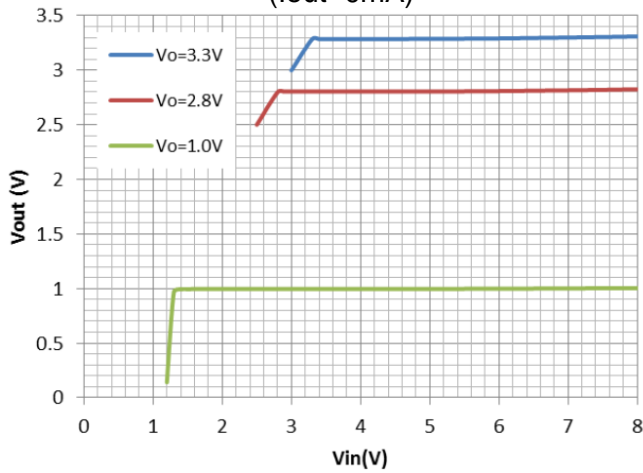


Note: Input capacitor($C_{in}=1\mu\text{F}$) and Output capacitor($C_{out}=1\mu\text{F}/3.3\mu\text{F}$) are recommended in all application circuit.



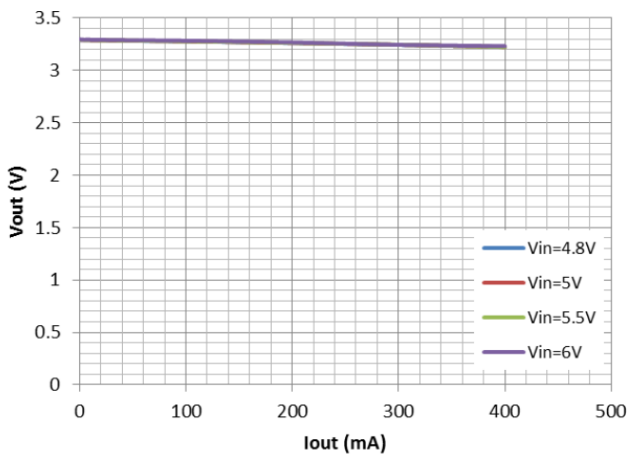
Electrical Characteristics

**Line Regulation
($I_{out}=0mA$)**

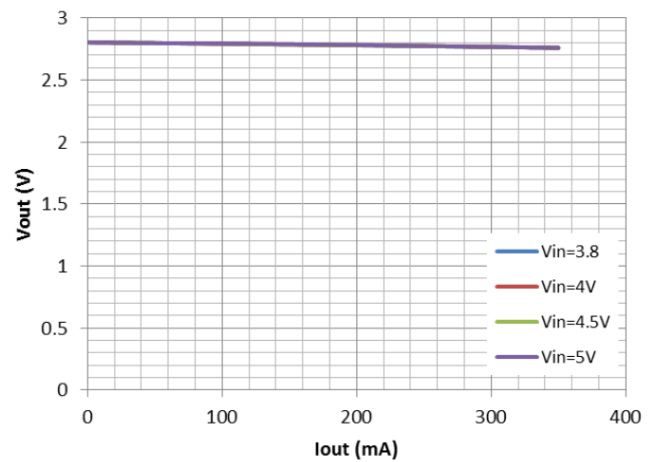


Typical Performance Characteristics ($T_A=25^{\circ}C$)

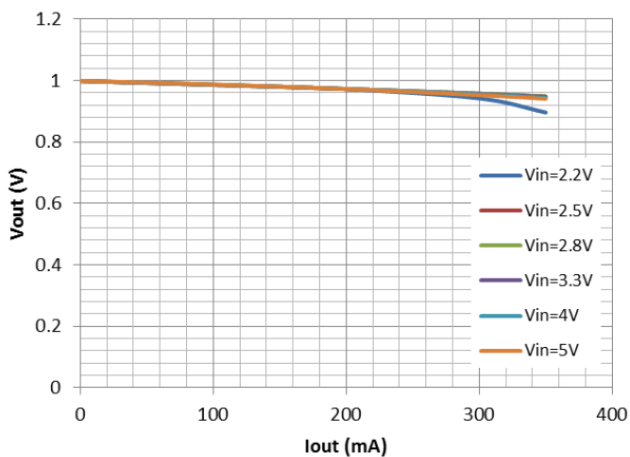
**Load Regulation
($V_{out}=3.3V$)**



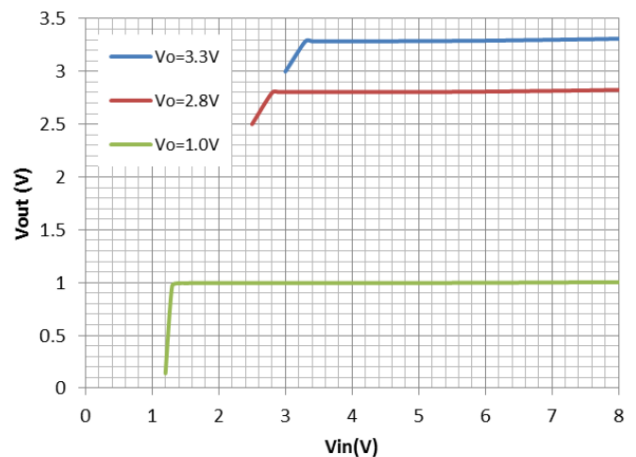
**Load Regulation
($V_{out}=2.8V$)**



**Load Regulation
($V_{out}=1.0V$)**



**Line Regulation
($I_{out}=0mA$)**



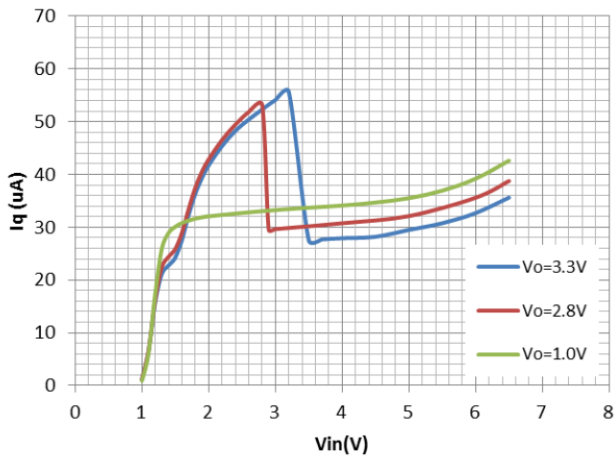


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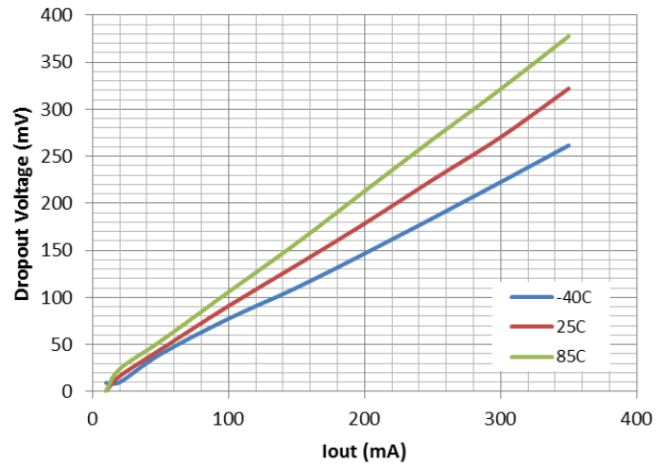
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Typical Performance Characteristics ($T_A=25^\circ\text{C}$)

Quiescent Current
($I_{out}=0\text{mA}$ and $CE=\text{high}$)

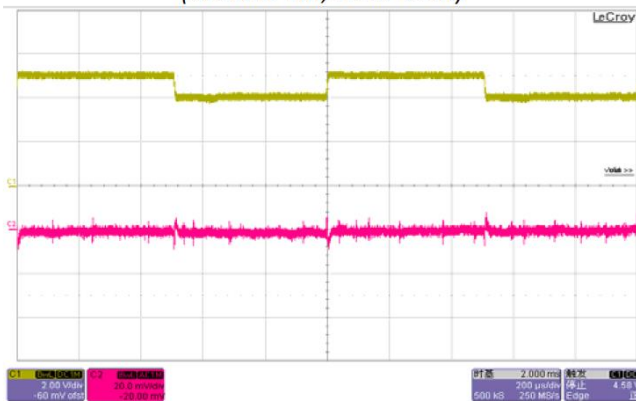


Dropout Voltage
($V_{out}=3.3\text{V}$)



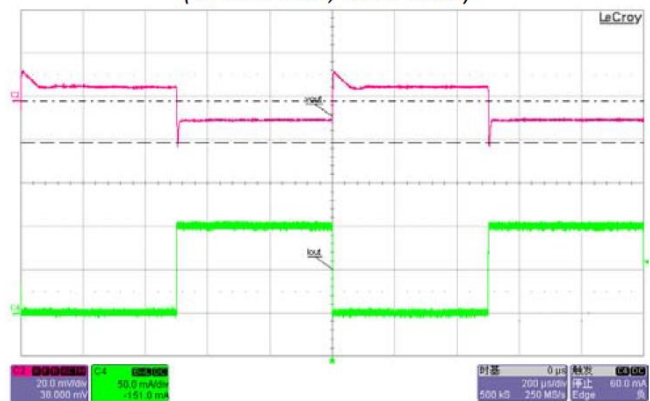
Line Transient Response

$V_{out}=3.3\text{V}$, $I_{out}=20\text{mA}$
(brown: V_{in} ; Red: V_{out})

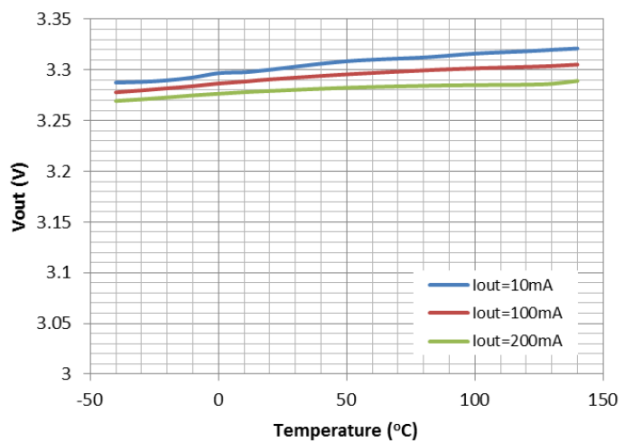


Load Transient Response

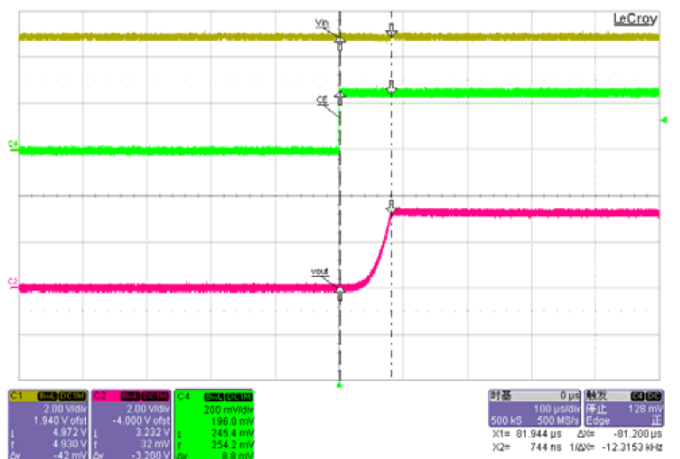
$V_{in}=5\text{V}$, $V_{out}=3.3\text{V}$, $I_{out}=1\text{mA}$ - 100mA
(Green: I_{out} ; Red: V_{out})



V_{out} Temperature Coefficient
($V_{out}=3.3\text{V}$)



CE Chip Enable Response



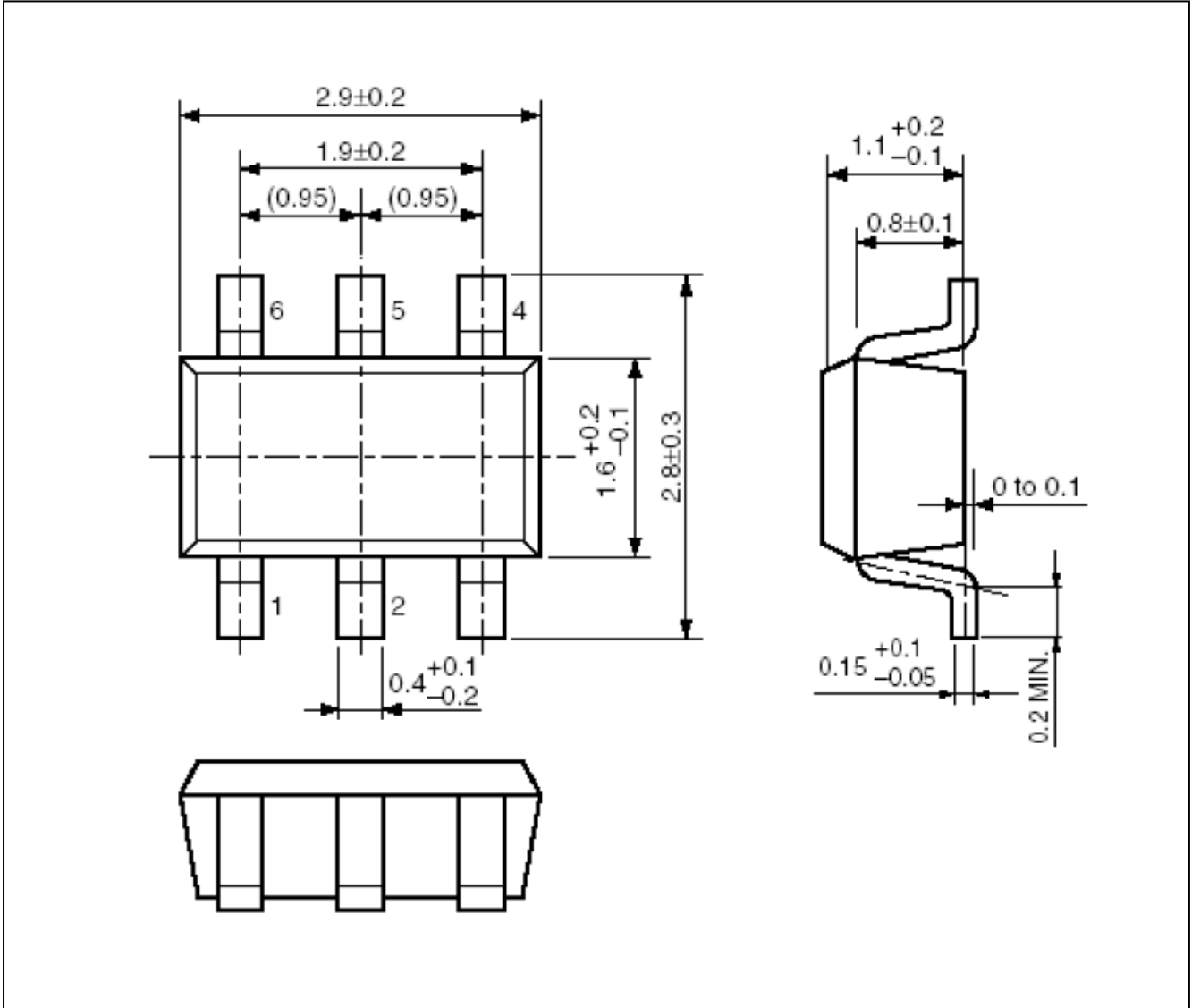


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Packing Information

SOT-23-6





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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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