ACE2302

N-Channel Enhancement Mode MOSFET

Description

The ACE2302 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and Battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

Features

- 20V/3.6A, $R_{DS(ON)}$=80mΩ@V_{GS}=4.5V
- 20V/3.1A, $R_{DS(ON)}$=95mΩ@V_{GS}=2.5V
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

Application

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

Absolute Maximum Ratings

Parameter		Symbol	Max	Unit
Drain-Source Voltage		V_{DSS}	20	V
Gate-Source Voltage		V_{GSS}	±12	V
Continuous Drain Current (T_J=150℃)	T_A=25℃	I_D	3.2	A
	T_A=70℃		2.6	
Pulsed Drain Current		I_{DM}	10	A
Continuous Source Current (Diode Conduction)		I_S	1.6	A
Power Dissipation	T_A=25℃	P_D	1.25	W
	T_A=70℃		0.8	
Operating Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	100	°C/W

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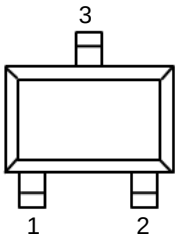
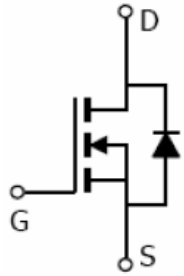
Packaging Type

SOT-23-3L



SOT-23-3L	Description
1	Gate
2	Source
3	Drain



Ordering information

ACE2302 XX + H

- Halogen - free
- Pb - free
- BM : SOT-23-3L

Electrical Characteristics

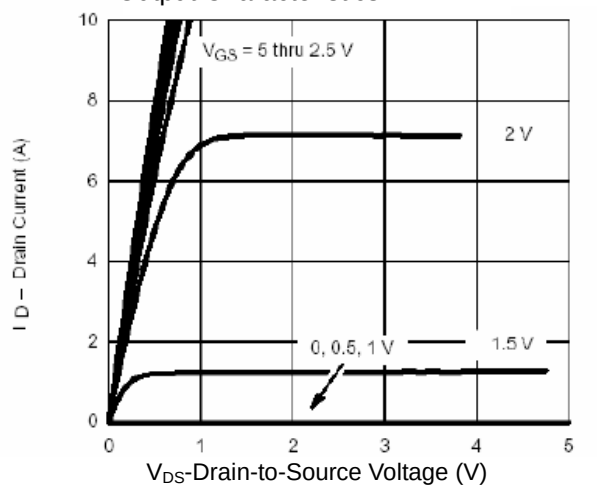
T_A=25℃, unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250 uA	20			V
Gate Threshold Voltage	$V_{GS(th)}$	V_D=V_{GS}, I_D=250uA	0.45		1.2	
Gate Leakage Current	I_{GSS}	V_{DS}=0V,V_{GS}=±12V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=20V, V_{GS}=0V			1	uA
		V_{DS}=20V, V_{GS}=0V T_J=55℃			10	
On-State Drain Current	$I_{D(ON)}$	VDS≧5V, V_{GS}=4.5V	6			A
		VDS≧5V, V_{GS}=2.5V	4			
Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=4.5V, I_D=3.6A		0.050	0.080	Ω
		V_{GS}=2.5V, I_D=3.1A		0.070	0.095	
Forward	gfs	V_{DS}=5V,I_D=3.6A		10		S

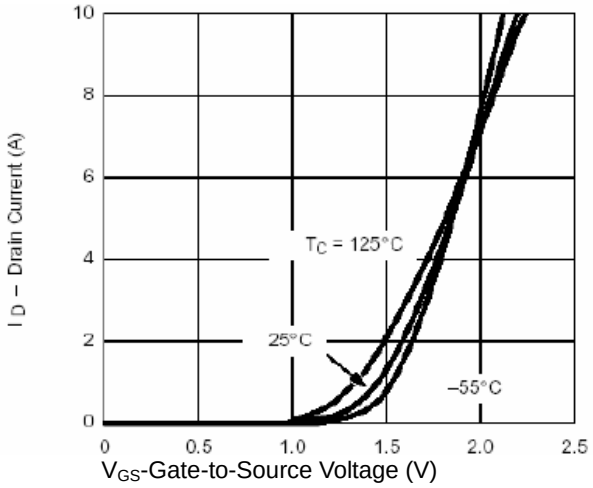
Transconductance						
Diode Forward Voltage	V_{SD}	I_S=1.6A, V_{GS}=0V		0.85	1.2	V
Dynamic						
Total Gate Charge	Q_g	V_{DS}=10V, V_{GS}=4.5V, I_D=3.6A		5.4	10	nC
Gate-Source Charge	Q_{gs}			0.65		
Gate-Drain Charge	Q_{gd}			1.4		
Input Capacitance	Ciss	V_{DS}=10V, V_{GS}=0V, f=1MHz		340		pF
Output Capacitance	Coss			115		
Reverse Transfer Capacitance	Crss			33		
Turn-On Time	td(on)	V_{DD}=10V, R_L=5.5Ω, I_D=3.6A, V_{GEN}=4.5V, R_G=6Ω		12	25	nS
	tr			36	60	
Turn-Off Time	td(off)			34	60	
	tf			10	25	

Typical Performance Characteristics

Output Characteristics

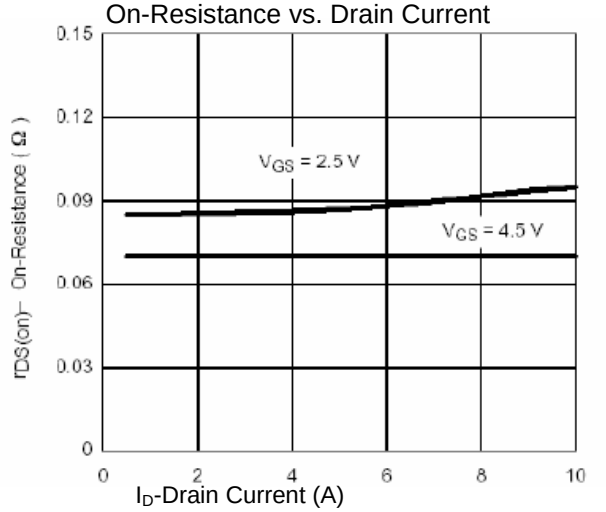


Transfer Characteristics

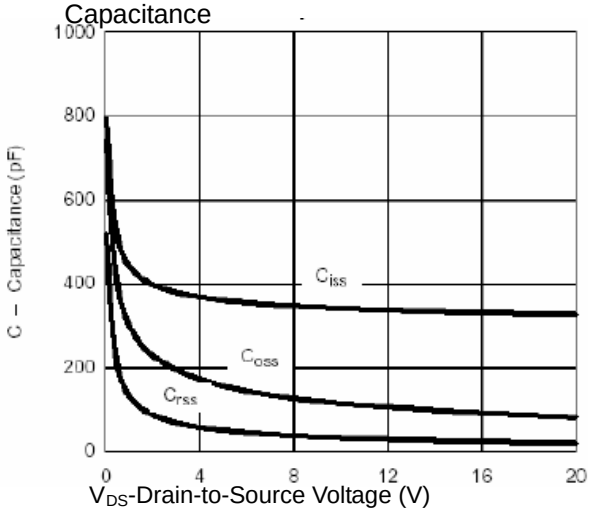


Typical Performance Characteristics

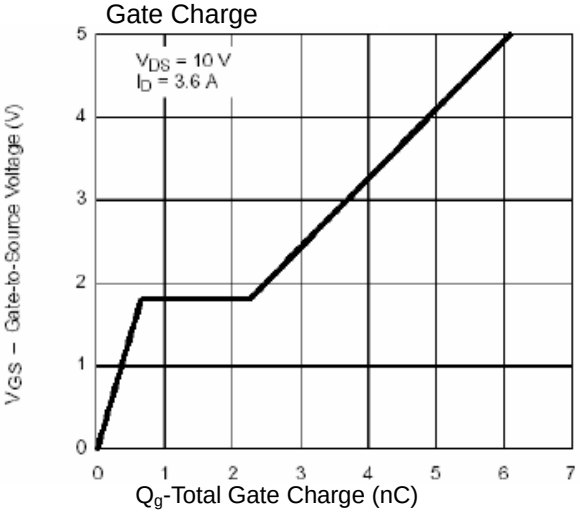
On-Resistance vs. Drain Current



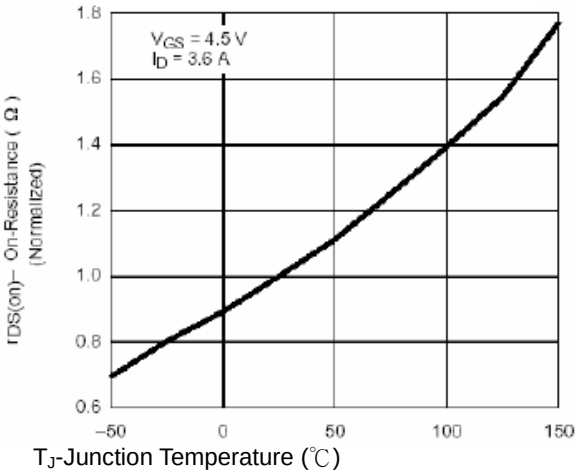
Capacitance



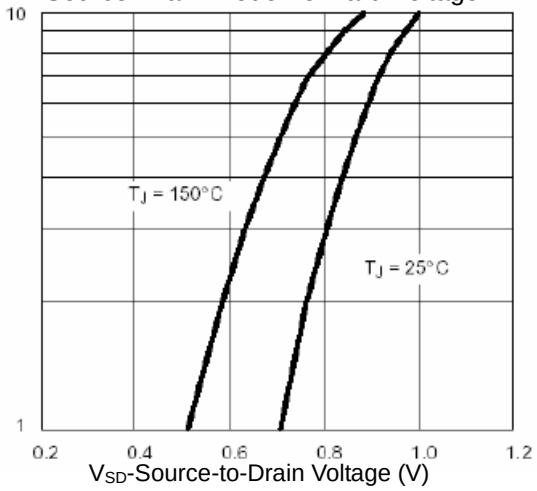
Gate Charge



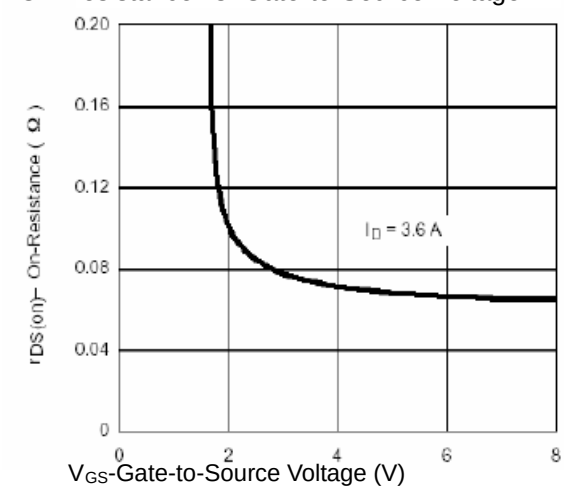
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

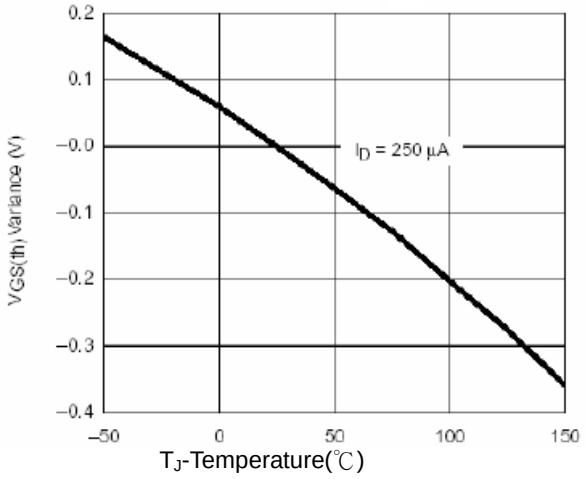


On-Resistance vs. Gate-to-Source Voltage

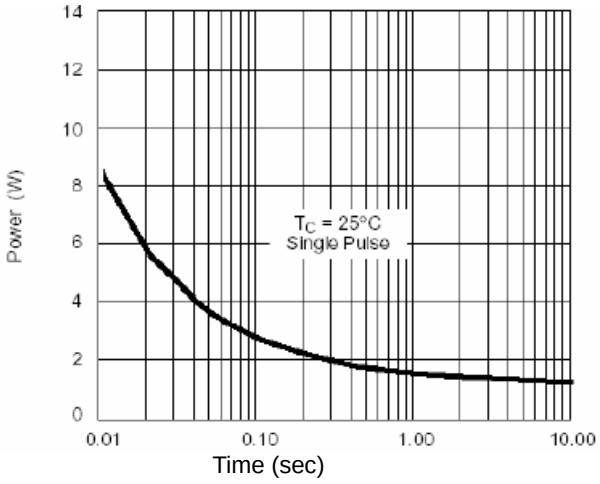


Typical Performance Characteristics

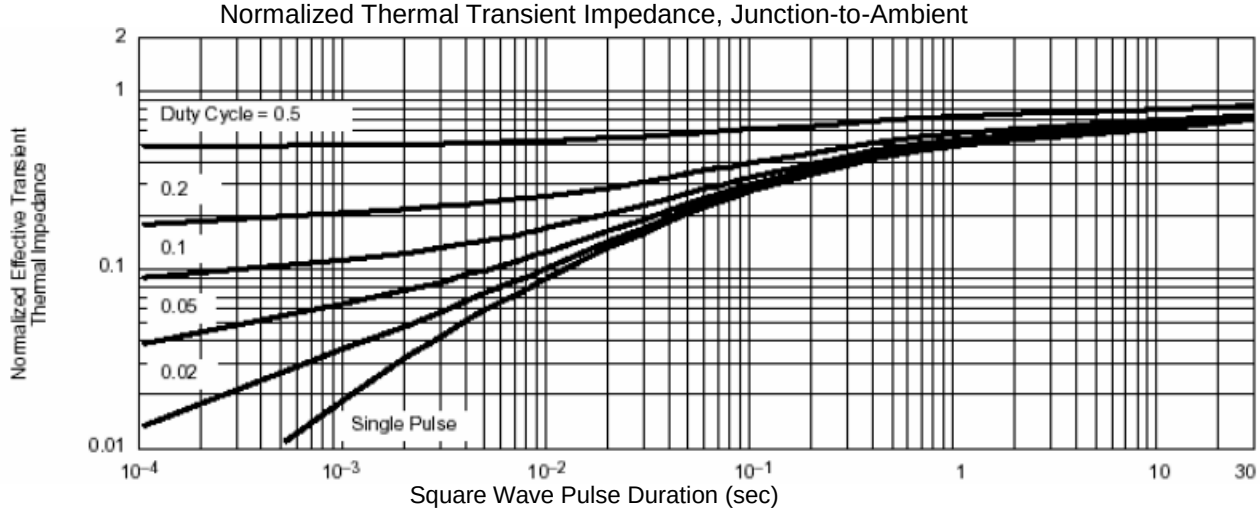
Threshold Voltage



Single Pulse Power

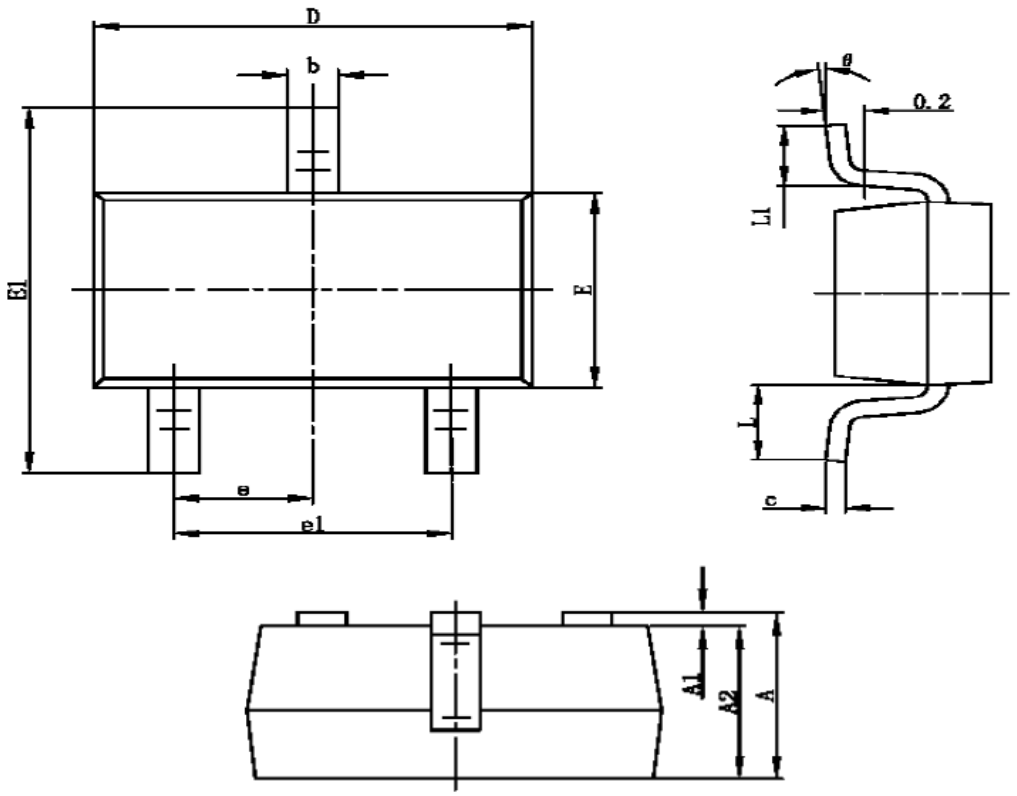


Normalized Thermal Transient Impedance, Junction-to-Ambient



Packing Information

SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.700REF		0.028REF	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Notes

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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