

DIFFUSED-JUNCTION SILICON RECTIFIERS

*absolute maximum ratings at specified ambient temperature

	1N2069	1N2070	1N2071	1N2069A	1N2070A	1N2071A	UNIT
$V_{RM}(avg)$ Working Peak Reverse Voltage at (or below) 100°C (See Note 1)	200	400	600	200	400	600	V
V_R Steady State Reverse Voltage at (or below) 100°C	200	400	600	200	400	600	V
I_O Average Rectified Forward Current at (or below) 25°C (See Note 1)	750						mA
I_{O100} Average Rectified Forward Current at (or below) 100°C (See Note 1)	500						mA
$I_{FM(rep)}$ Repetitive Peak Forward Current, 10 Cycles, at (or below) 25°C (See Notes 2 and 3)	6						A
$I_{FM(surge)}$ Peak Surge Current, One Cycle, at (or below) 100°C (See Note 2)	22						A
$T_{A(oper)}$ Operating Ambient Temperature Range	-30 to 100			-35 to 100			°C
T_{stg} Storage Temperature Range	-30 to 100			-35 to 100			°C
Lead Temperature 1/2 Inch from Case for 5 Seconds (See Note 4)	240						°C

NOTES 1. These values may be applied continuously under single-phase, 60-c/s, half-sine-wave operation with resistive load. Above 25°C derate I_O according to Figure 1.

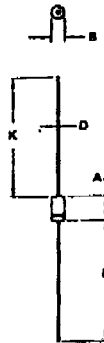
2. These values apply for 60-c/s half sine waves when the device is operating at (or below) rated values of peak reverse voltage and average rectified forward current. Surge may be repeated after the device has returned to original thermal equilibrium.

3. Derate linearly to 4 A at 100°C.

4. It is recommended that a heat sink, such as long-nose pliers, be applied between the point of soldering and the rectifier body.

*indicates JEDEC registered data.

[†]The ambient temperature is measured at a point 2 inches below the device. Natural air cooling shall be used.



- NOTES:
1. POLARITY DENOTED BY CATHODE BAND.
 2. LEAD DIAMETER NOT CONTROLLED WITHIN "C" DIMENSION.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.37	0.50	0.015	0.200
B	2.75	3.00	0.110	0.120
C	0.75	0.80	0.030	0.032
K	7.62	-	0.300	-

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