

DATA SHEET

SMP1321-040LF: 0402 Surface Mount PIN Diode

Applications

- WLAN, WiMAX, cellular handsets
- Cellular infrastructure
- RFID readers
- Test instruments
- High isolation switches
- Series diode switches



Features

- Very low parasitic inductance and capacitance
- Low-thermal impedance
- Industry-standard 0402 footprint
- Packages rated MSL1, 260 °C per JEDEC J-STD-020

NEW



Skyworks Green™ products are RoHS (Restriction of Hazardous Substances)-compliant, conform to the EIA/EICTA/JEITA Joint Industry Guide (JIG) Level A guidelines, are halogen free according to IEC-61249-2-21, and contain <1,000 ppm antimony trioxide in polymeric materials.

Description

The SMP1321-040LF is a plastic packaged, surface mountable PIN diode designed for high volume switch applications from 10 MHz to beyond 2 GHz. The low capacitance of this diode (0.25 pF) combined with its low resistance (2.0 Ω maximum at 10 mA) make the SMP1321-040LF particularly well suited for high-isolation, series-connected PIN diode switches in battery-operated circuits.

The SMP1321-040LF is also available in a variety of plastic packages and configurations that include a low inductance (0.4 nH) SOT-23 (the SMP1321-007), a small footprint SC-79, and a miniature SC-70. The SMP1321-508 has been specifically designed for WLAN 802.11 a/b/g applications.

Design information for high power switches may be found in the Skyworks Application Note, *Design With PIN Diodes* (document number 200312).

Table 1. SMP1321-040LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Maximum | Units |
|------------------------------|------------------|---------|---------|-------|
| Reverse voltage | V _R | | 100 | V |
| Forward current | I _F | | 150 | mA |
| Dissipated power @ 25 °C | P _D | | 750 | mW |
| Solder interface temperature | T _S | -40 | +85 | °C |
| Storage temperature | T _{STG} | -65 | +150 | °C |
| Junction temperature | T _J | | +175 | °C |

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times. The SMP1321-040LF is rated as a Human Body Model (HBM) ESD Class 1B device.

Table 2. SMP1321-040LF Electrical Specifications (Note 1)
(T_S = +25 °C, Unless Otherwise Noted)

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
|---------------------------|----------------|-------------------------------------|-----|---------|------|-------|
| Reverse current (Note 2) | I _R | V _R = 100 V | | | 10 | μA |
| Total capacitance | C _T | V _R = 30 V, f = 1 MHz | | | 0.25 | pF |
| Series resistance | R _S | I _F = 1 mA, f = 100 MHz | | 3 | | Ω |
| | | I _F = 10 mA, f = 100 MHz | | | 2 | Ω |
| Series inductance | L _S | | | 0.45 | | nH |
| Forward voltage | V _F | I _F = 10 mA | | 0.85 | | V |
| Minority carrier lifetime | T _L | I _F = 10 mA | | 0.4 | | μs |
| I region width | W | | | 15 | | μm |

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Note 2: It is not recommended to drive a PIN diode into avalanche breakdown. Permanent damage may result.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SMP1321-040LF are provided in Table 1. Electrical specifications are provided in Table 2.

Typical performance characteristics of the SMP1321-040LF are illustrated in Figures 1, 2, and 3.

Package Dimensions

The PCB layout footprint for the SMP1321-040LF is provided in Figure 4. Typical case markings are shown in Figure 5. Package dimensions for the SMP1321-040LF are provided in Figure 6. Tape and reel dimensions are provided in Figure 7.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMP1321-040LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

Typical Performance Characteristics

(Ts = 25 °C, Unless Otherwise Noted)

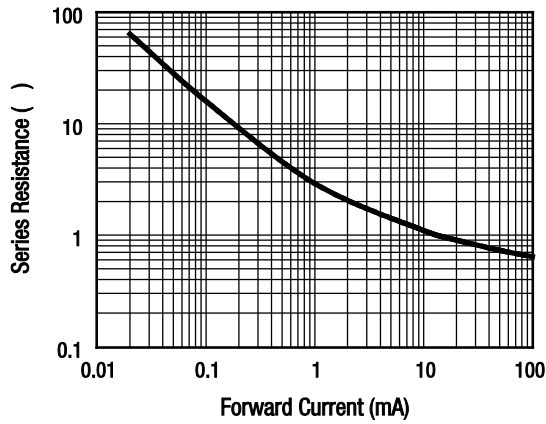


Figure 1. Series Resistance vs Current @ 100 MHz

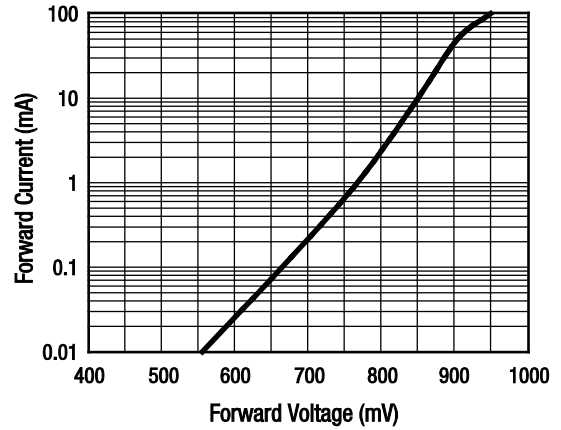


Figure 2. DC Characteristic

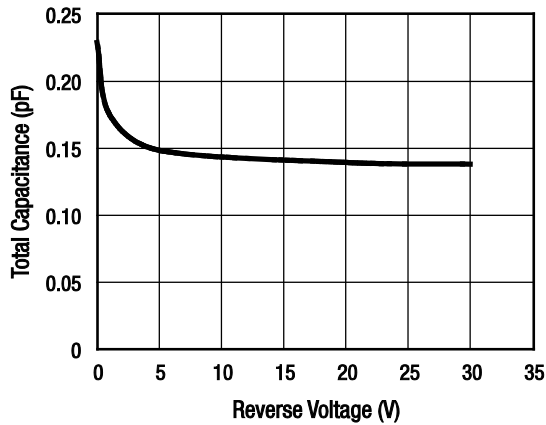
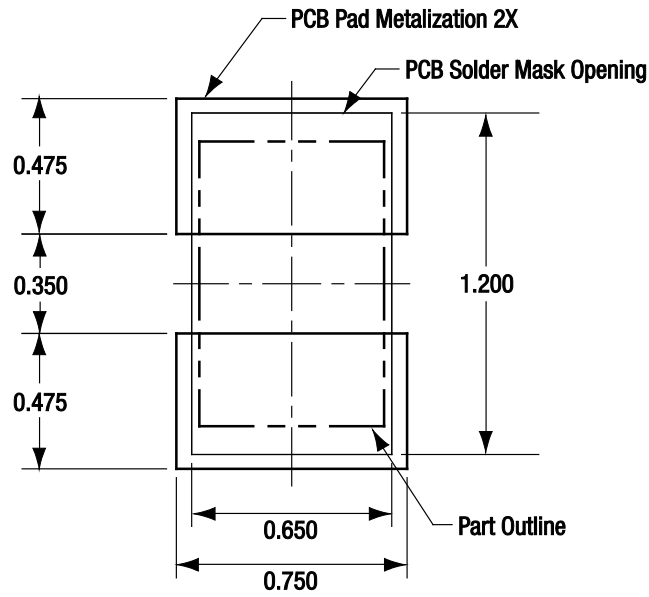


Figure 3. Capacitance vs Reverse Voltage (@ 1.0 MHz)



All measurements in millimeters

S1997

Figure 4. SMP1321-040LF PCB Layout Footprint

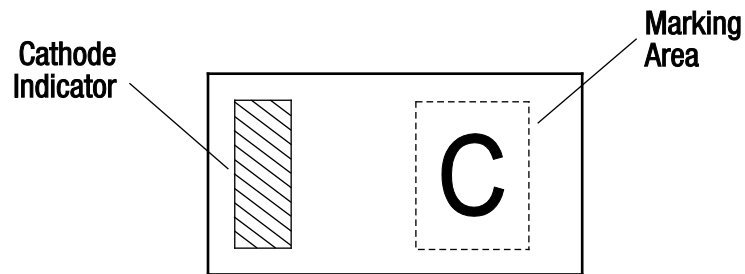
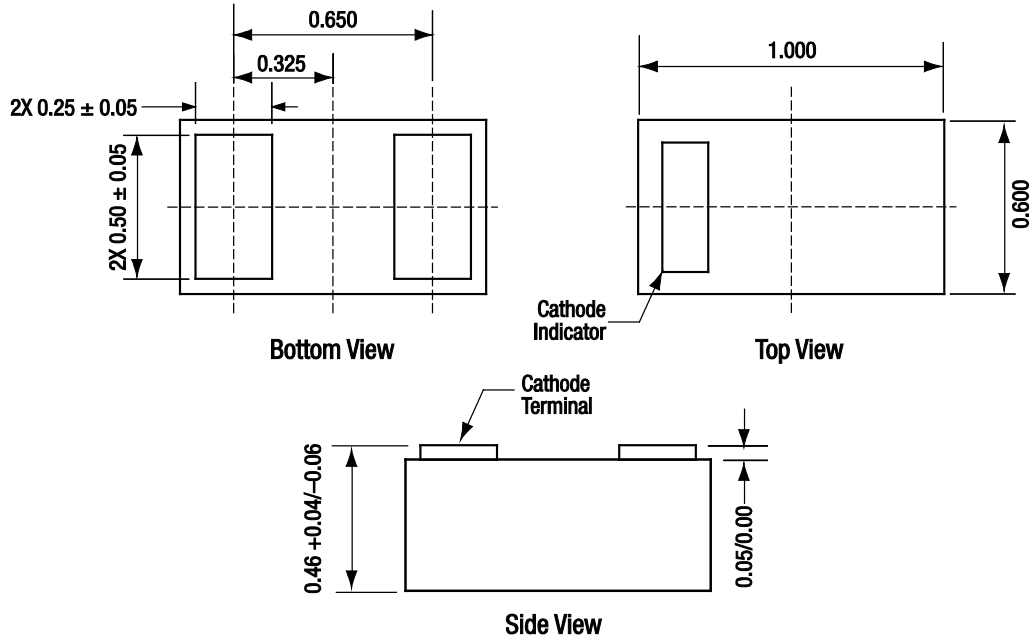


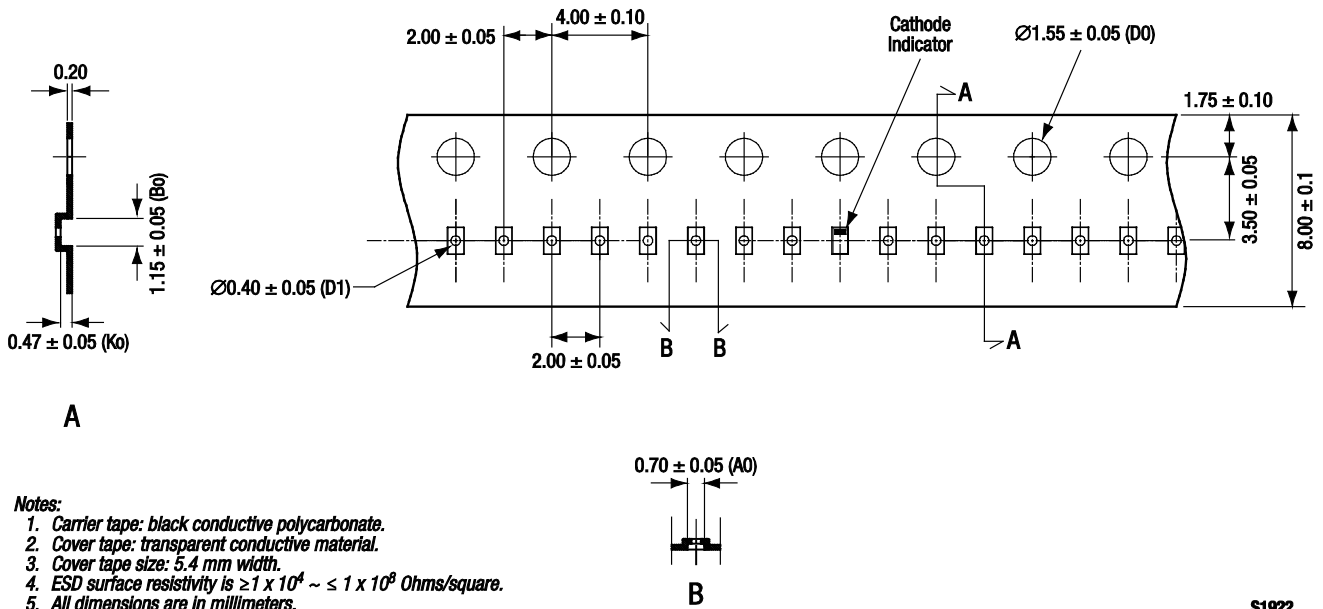
Figure 5. Typical Case Markings (Top View)



All dimensions in millimeters

S1892

Figure 6. SMP1321-040LF Package Dimensions



Notes:

1. Carrier tape: black conductive polycarbonate.
2. Cover tape: transparent conductive material.
3. Cover tape size: 5.4 mm width.
4. ESD surface resistivity is $\geq 1 \times 10^4 \sim \leq 1 \times 10^8$ Ohms/square.
5. All dimensions are in millimeters.

S1922

Figure 7. SMP1321-040LF Tape and Reel Dimensions

Ordering Information

| Model Name | Manufacturing Part Number |
|---------------------------------|---------------------------|
| SMP1321-040LF Silicon PIN Diode | SMP1321-040LF |

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