



Parameter	Ratings	Units
Blocking Voltage	250	V _p
Load Current	150	mA
Max R _{ON}	15	Ω

Features

- Current Limited 1-Form-A Relay
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount, Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

LBA127L comprises two independent 250V, 150mA, 15Ω solid state relays: one current-limited, single-pole, normally open (1-Form-A), relay and one single-pole, normally closed (1-Form-B) relay.

The relay outputs are constructed with efficient MOSFET switches and photovoltaic die that use Clare's patented OptoMOS architecture, while the inputs, highly efficient GaAIAs infrared LEDs, provide the optically coupled control.

LBA127L is designed to provide an ideal solution where a complementary Form-A/Form-B relay pair is required in a space-saving single package.

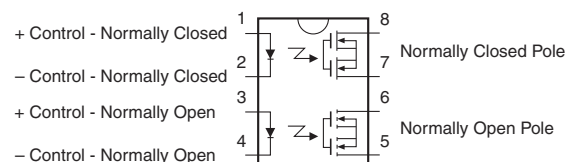
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component:
TUV Certificate B 09 07 49410 004

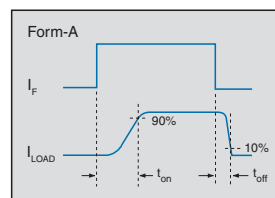
Ordering Information

Part #	Description
LBA127L	8-Pin DIP (50/Tube)
LBA127LS	8-Pin Surface Mount (50/Tube)
LBA127LSTR	8-Pin Surface Mount (1,000/Reel)

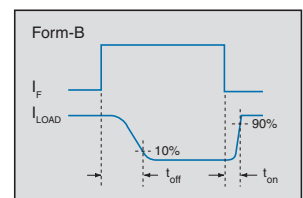
Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Switching Characteristics of Normally Closed (Form B) Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	250	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

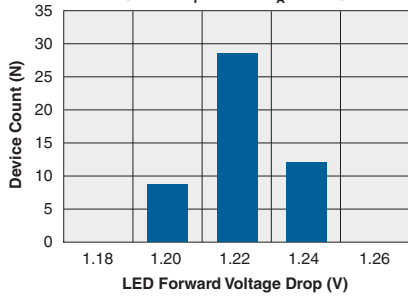
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current						
Continuous ¹	-	I _L	-	-	150	mA
Peak (Form-B Only)	t = 10ms	I _{LPK}	-	-	400	
Load Current Limiting (Form-A Only)	I _F =5mA, V _L =8V	I _{CL}	190	200	280	
On-Resistance ²	I _L =150mA	R _{ON}	-	6.8	15	Ω
Off-State Leakage Current	V _L =250V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	-	5	ms
Turn-Off		t _{off}	-	-	5	
Output Capacitance	V _L =50V, f=1MHz	C _{OUT}	-	110	-	pF
Input Characteristics						
Input Control Current	I _L =150mA	I _F	-	-	5	mA
Input Dropout Current	-	-	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

¹ If both poles operate the load current must be derated so as not to exceed the package power dissipation value.

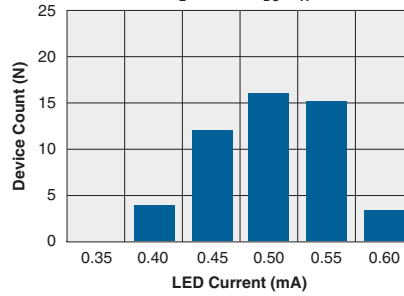
² Measurement taken within 1 second of on-time.

COMMON PERFORMANCE DATA*

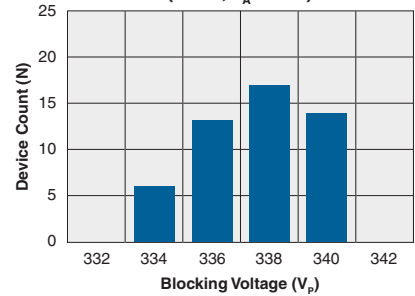
Typical LED Forward Voltage Drop
(N=50, $I_F=5\text{mA}$, $T_A=25^\circ\text{C}$)



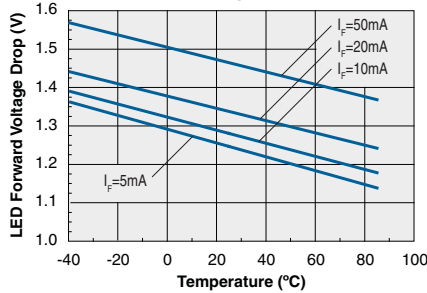
Typical I_F for Switch Operation
(N=50, $I_L=150\text{mA}_{DC}$, $T_A=25^\circ\text{C}$)



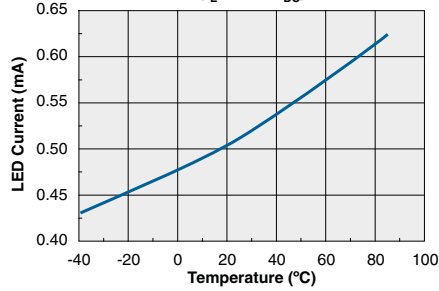
Typical Blocking Voltage Distribution
(Form-A: $I_F=0\text{mA}$, Form-B: $I_F=5\text{mA}$)
(N=50, $T_A=25^\circ\text{C}$)



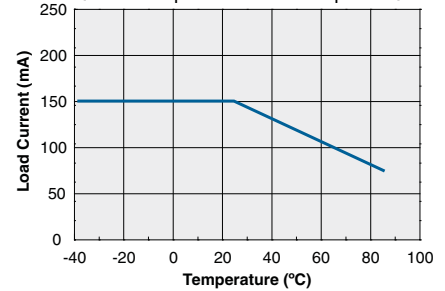
Typical LED Forward Voltage Drop vs. Temperature



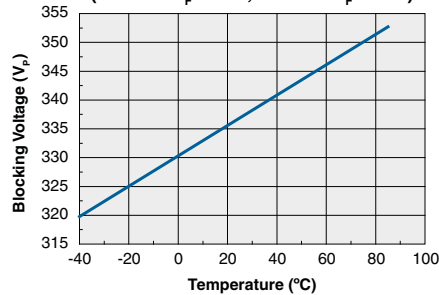
Typical I_F for Switch Operation vs. Temperature
($I_L=75\text{mA}_{DC}$)



Load Current vs. Load Temperature
(Form-A: $I_F=5\text{mA}$, Form-B: $I_F=0\text{mA}$)

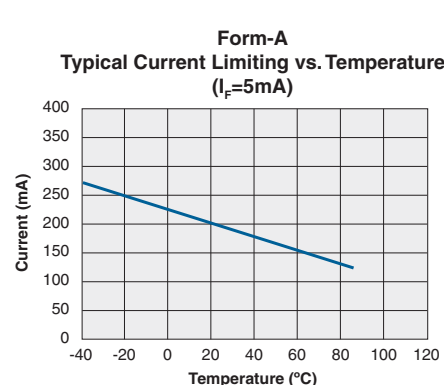
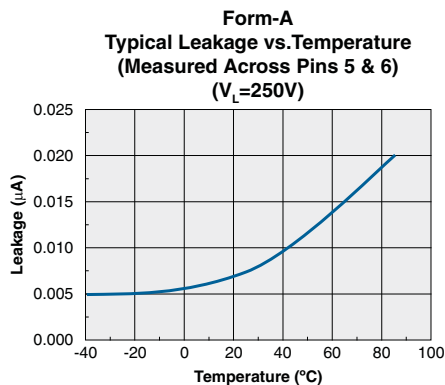
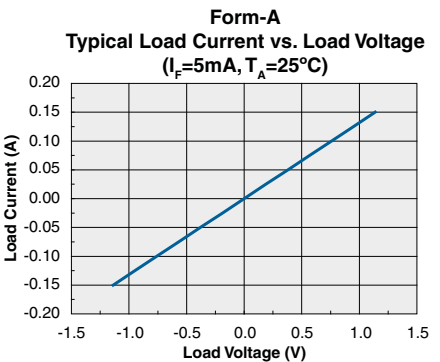
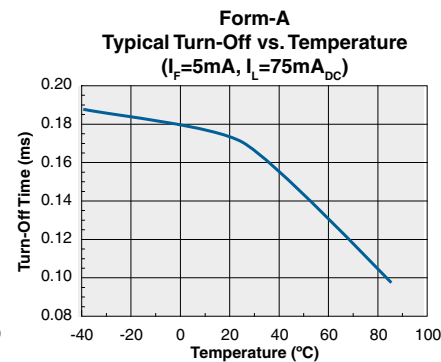
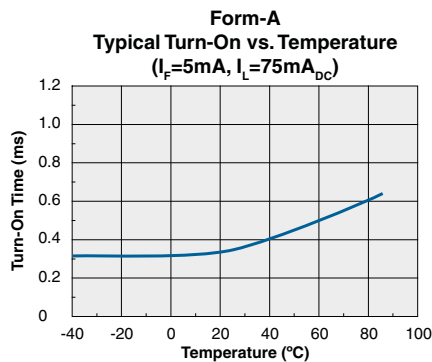
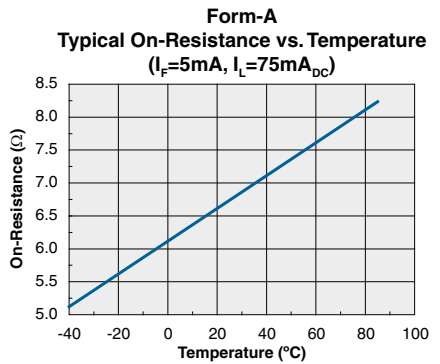
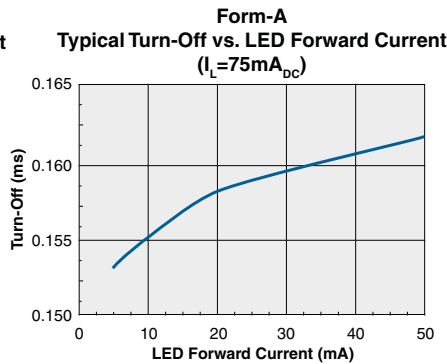
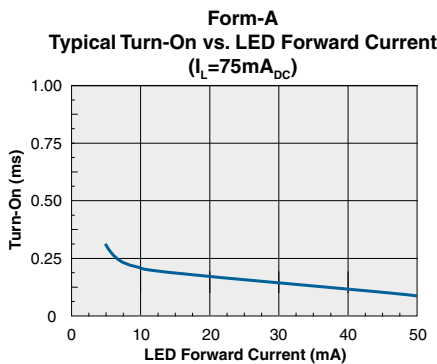
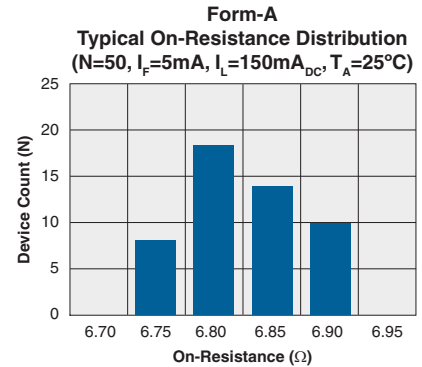
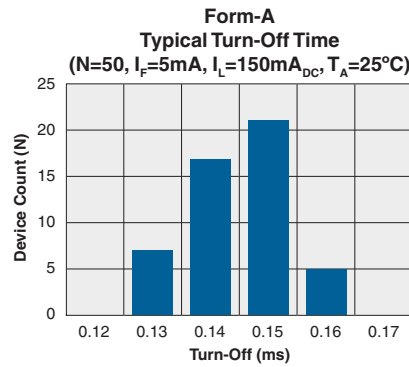
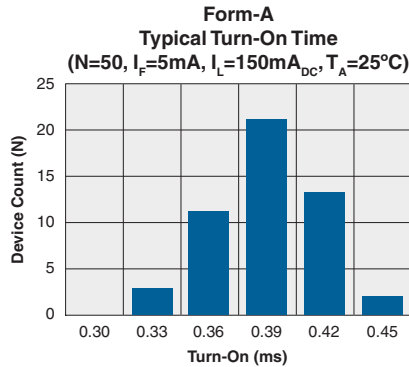


Typical Blocking Voltage vs. Temperature
(Form-A: $I_F=0\text{mA}$, Form-B: $I_F=5\text{mA}$)



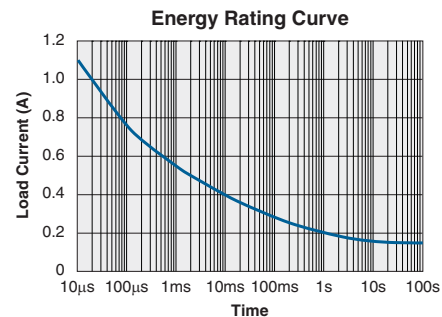
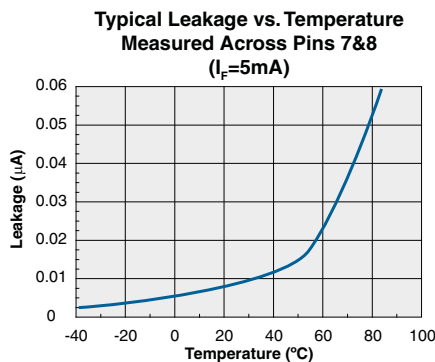
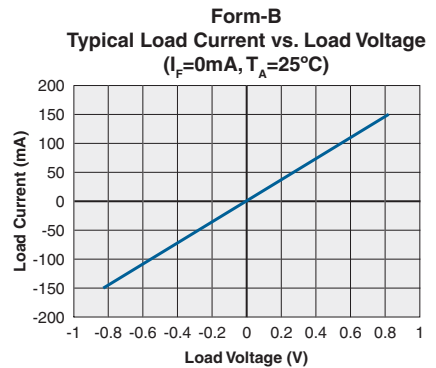
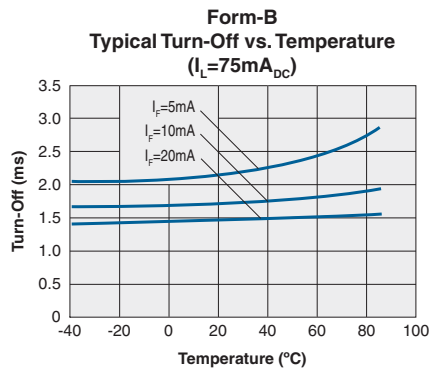
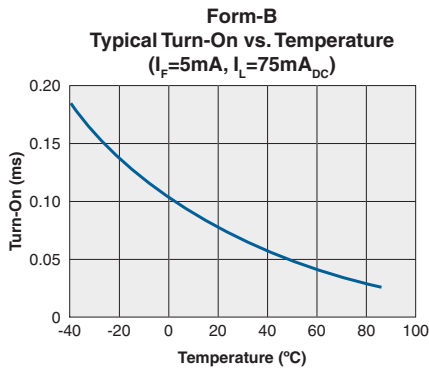
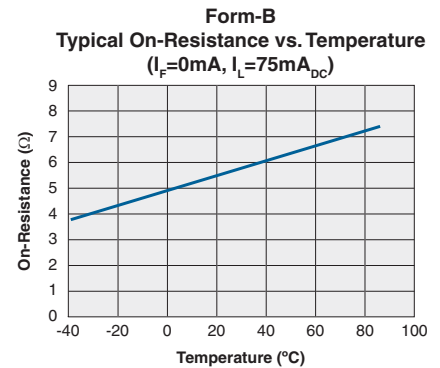
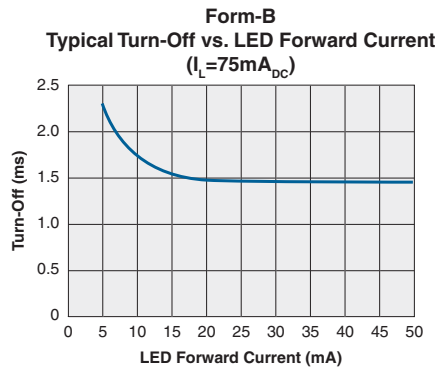
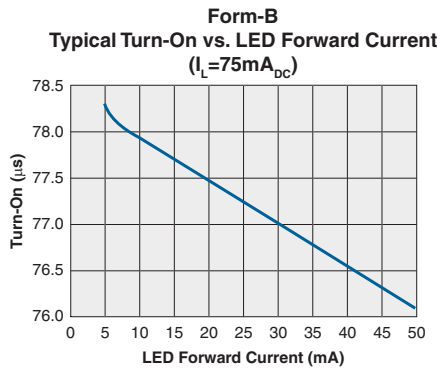
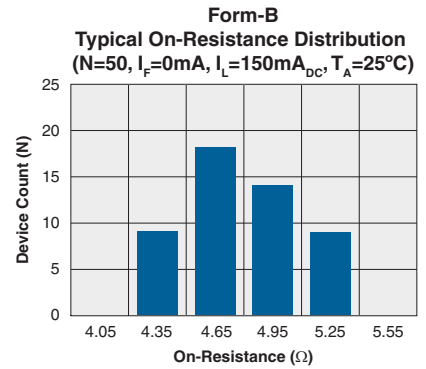
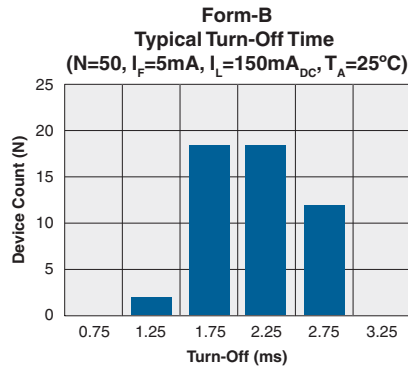
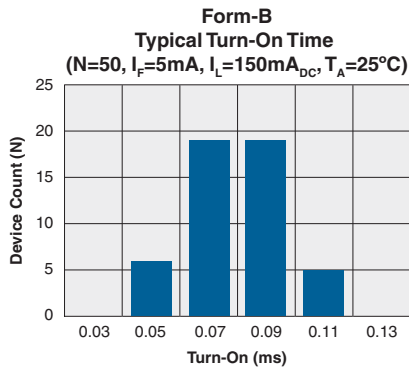
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA for 1-Form-A Relay*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA for 1-Form-B Relay*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
LBA127L / LBA127LS	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
LBA127L / LBA127LS	250°C for 30 seconds

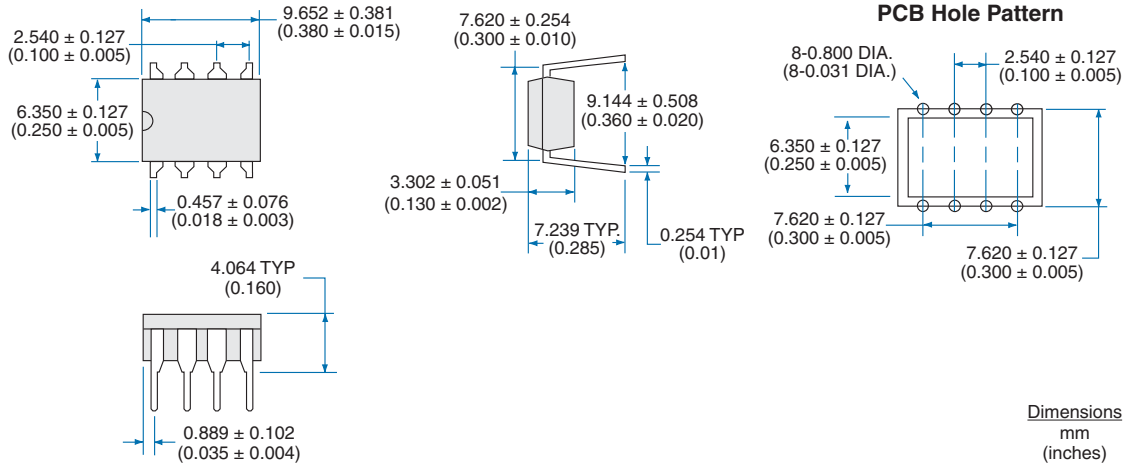
Board Wash

Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake may be necessary if a wash is used after solder reflow processes. Chlorine-based or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

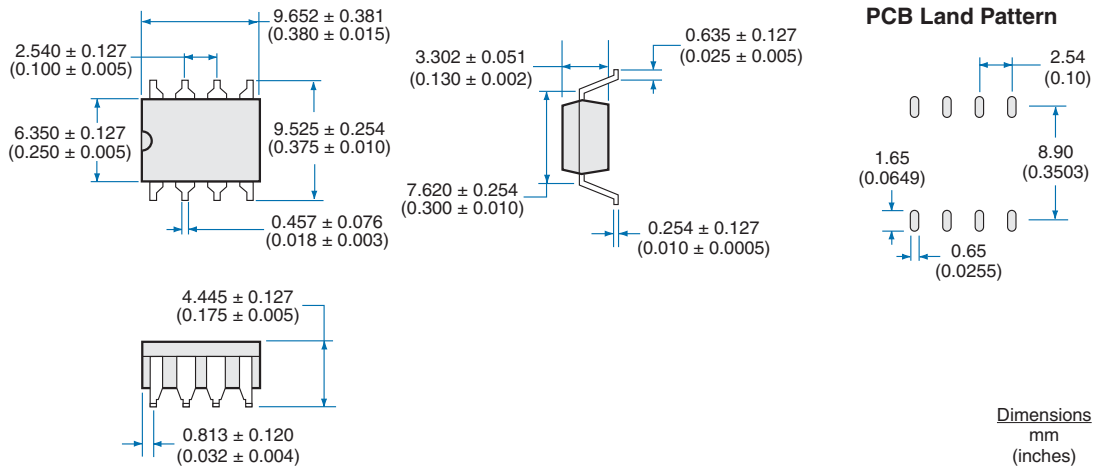


MECHANICAL DIMENSIONS

LBA127L

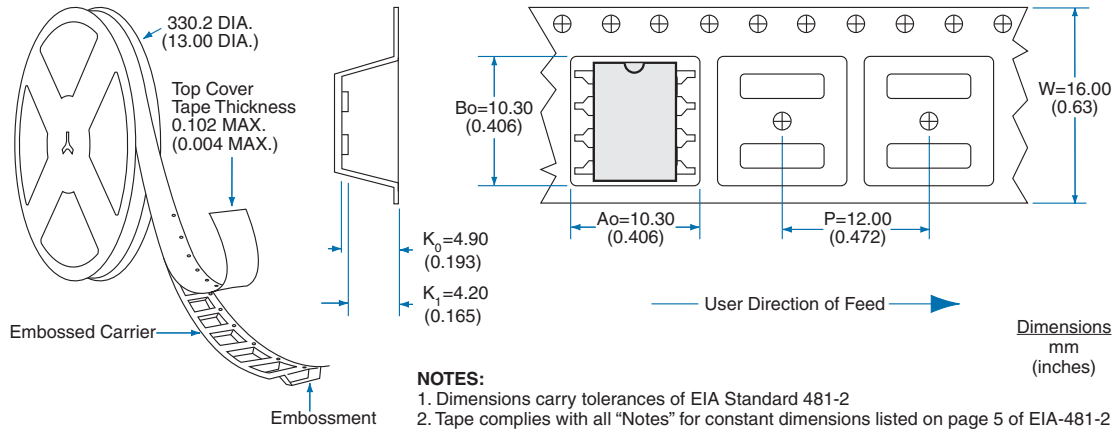


LBA127LS



MECHANICAL DIMENSIONS

LBA127LS Tape & Reel



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