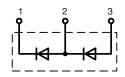
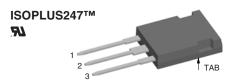


Phase-leg Rectifier Diode

 $V_{RRM} = 1600 V$ $I_{F(RMS)} = 2x70 A$ $I_{F(AV)M} = 2x45 A$

V _{RSM}	V_{RRM}	Туре
V	V	
1700 1600		DSP 45-16AR





1 = Cathode, 2 = Anode/Cathode, 3 = Anode

Symbol	Conditions	Maximum Ra	atings
I _{F(RMS)}	$T_{VJ} = T_{VJM}$ $T_{C} = 100^{\circ}C$; 180° sine	70 43	A A
I _{FSM}	$T_{VJ} = 45$ °C; $t = 10 \text{ ms}$ (50 Hz), sine $t = 8.3 \text{ ms}$ (60 Hz), sine	480 510	A A
	$T_{VJ} = 150$ °C; $t = 10$ ms (50 Hz), sine $t = 8.3$ ms (60 Hz), sine	420 450	A A
l²t	$T_{VJ} = 45^{\circ}C$ $t = 10 \text{ ms } (50 \text{ Hz}), \text{ sine}$ $t = 8.3 \text{ ms } (60 \text{ Hz}), \text{ sine}$	1150 1090	A ² s A ² s
	$T_{VJ} = 150$ °C; $t = 10$ ms (50 Hz), sine $t = 8.3$ ms (60 Hz), sine	880 855	A ² s A ² s
T _{VJ} T _{VJM} T _{stg}		-40+150 +150 -40+150	°C °C
F _c	Clip mounting force	20120	N
V _{ISOL}	50/60 Hz, RMS, t = 1 minute, leads-to-tab	3000	٧~
Weight		6	g

Symbol	Conditions	Characteristic V	/alues
I _R	$T_{VJ} = 150^{\circ}C V_{R} = V_{RRM}$	≤ 3	mA
V _F	$I_F = 40 \text{ A}; T_{VJ} = 25^{\circ}\text{C}$	≤ 1.23	V
V _{T0}	For power-loss calculations only	0.8	V
\mathbf{r}_{T}	$T_{VJ} = T_{VJM}$	11	$m\Omega$
R _{thJC}	DC current	0.7	K/W
R_{thCH}	DC current (with heatsink compound)	0.2	K/W
а	Maximum allowable acceleration	50	m/s²

Data according to IEC 60747 and refer to a single diode

Features / Advantages

- Planar passivated chips
- Very low leakage current
- Very low forward voltage drop
- Improved thermal behaviour

Applications

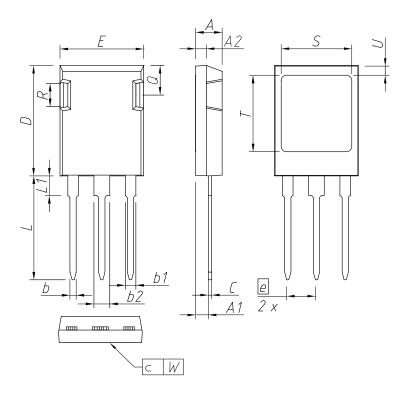
- Diode for main rectification
- For single and three phase bridge configuration

Package

- Industry standard outline
- DCB isolated backside
- Isolation voltage 3000 V
- Epoxy meets UL 94V-0UL registered E72873
- RoHS compliant



ISOPLUS247™



DIM.	MILLIMETER		INCHES	
	MIN	MAX	MIN	MAX
A	4,83	5,21	0,190	0,205
A 1	2,29	2,54	0,090	0,100
A2	1,91	2,16	0,075	0,085
Ь	1, 14	1,40	0,045	0,055
Ь1	1,91	2,15	0,075	0,085
<i>b2</i>	2,92	3,20	0,115	0,126
(0,61	0,83	0,024	0,033
D	20,80	21,34	0,819	0,840
E	15, 75	16,13	0,620	0,635
е	5,45 BSC		0,215 BSC	
L	19,81	20,60	0,780	0,811
L 1	3,81	4,38	0,150	0,172
Q	5,59	6,20	0,220	0,244
R	4,32	4,85	0,170	0,191
S	13,21	13,72	0,520	0,540
T	15, 75	16,26	0,620	0,640
U	1,65	2,03	0,065	0,080
W	-	0,10	_	0,004

The convex bow of substrate is typ. < 0.04 mm over plastic surface level of device bottom side

This drawing will meet all dimensions requirement of JEDEC outline TO-247 AD except screw hole and except Lmax.