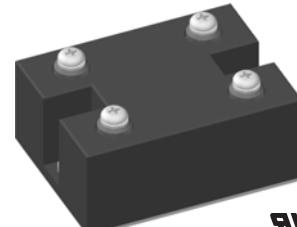
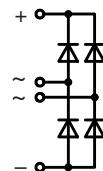


Single Phase Rectifier Bridge

I_{dAVM} = 124 A
V_{RRM} = 800-1600 V

V _{RSM} V	V _{RRM} V	Type
900	800	VBO 125-08N07
1300	1200	VBO 125-12N07
1700	1600	VBO 125-16N07



RL

Symbol	Conditions	Maximum Ratings		
I _{dAVM}	T _C = 85°C, module	124	A	
I _{FSM}	T _{VJ} = 45°C; V _R = 0	1800 1950	A A	
	T _{VJ} = T _{VJM} V _R = 0	1600 1800	A A	
I ² t	T _{VJ} = 45°C V _R = 0	16200 16000	A ² s A ² s	
	T _{VJ} = T _{VJM} V _R = 0	12800 13600	A ² s A ² s	
T _{VJ}		-40...+150	°C	
T _{VJM}		150	°C	
T _{stg}		-40...+150	°C	
V _{ISOL}	50/60 Hz, RMS I _{ISOL} ≤ 1 mA	2500 3000	V~ V~	
M _d	Mounting torque (M5)	5 ±15% 44 ±15%	Nm lb.in.	
	Terminal connection torque (M5)	5 ±15% 44 ±15%	Nm lb.in.	
Weight	typ.	225	g	

Symbol	Conditions	Characteristic Values		
I _R	V _R = V _{RRM} ; T _{VJ} = 25°C	≤ 0.3	mA	
	V _R = V _{RRM} ; T _{VJ} = T _{VJM}	≤ 8.0	mA	
V _F	I _F = 150 A; T _{VJ} = 25°C	≤ 1.3	V	
V _{TO}	For power-loss calculations only	0.8	V	
r _T	T _{VJ} = T _{VJM}	3	mΩ	
R _{thJC}	per diode; 180°	0.83	K/W	
	per module; 180°	0.138	K/W	
R _{thJK}	per diode; 180°	1.13	K/W	
	per module; 180°	0.188	K/W	

Data according to IEC 60747 refer to a single diode unless otherwise stated.

Features

- Package with screw terminals
- Isolation voltage 3000 V~
- Planar passivated chips
- Blocking voltage up to 1800 V
- Low forward voltage drop
- UL registered E 72873

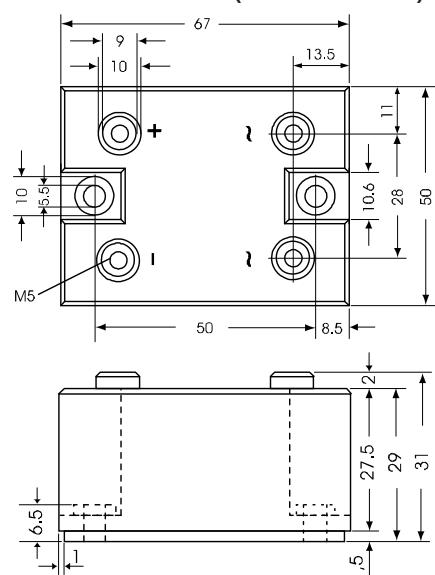
Applications

- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Advantages

- Easy to mount with two screws
- Space and weight savings
- Improved temperature and power cycling

Dimensions in mm (1 mm = 0.0394")



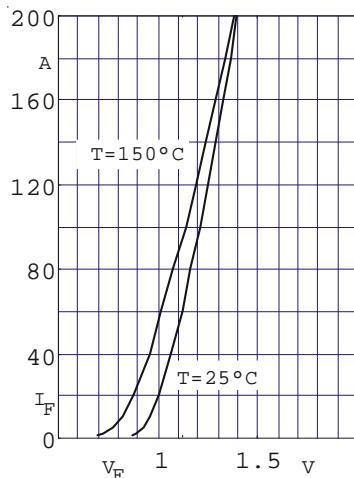


Fig. 1 Forward current versus voltage drop per diode

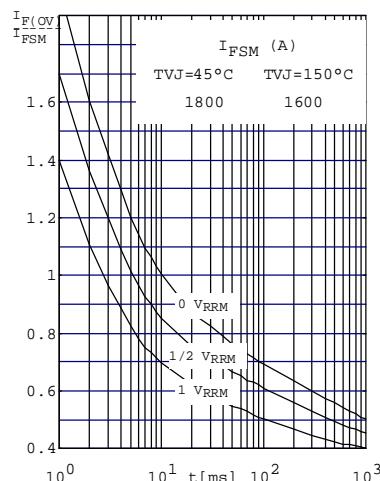


Fig. 2 Surge overload current per diode I_{FSM} : Crest value. t: duration

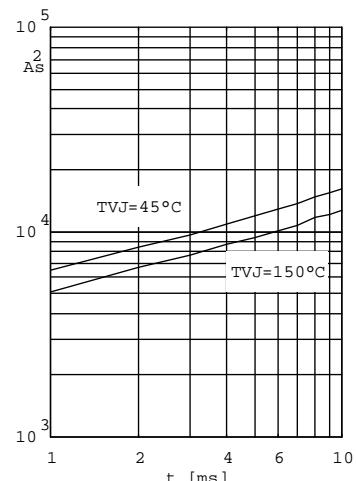


Fig. 3 I^2dt versus time (1-10ms) per diode or thyristor

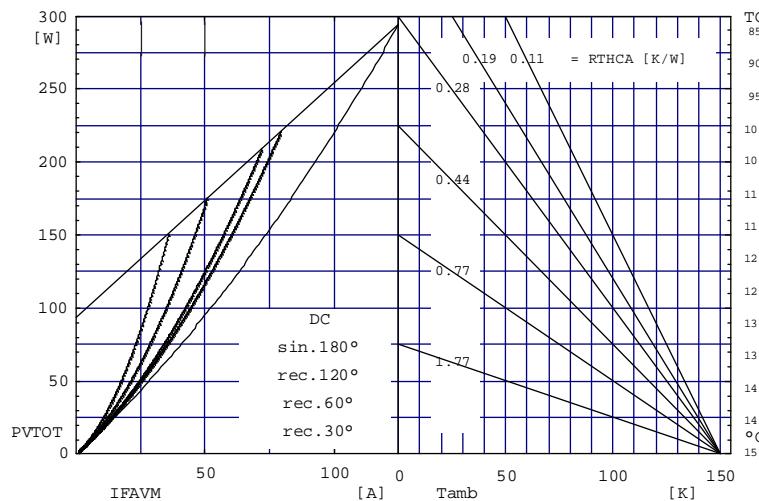


Fig. 4 Power dissipation versus direct output current and ambient temperature

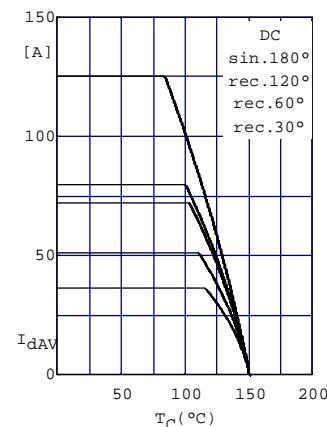


Fig. 5 Maximum forward current at case temperature

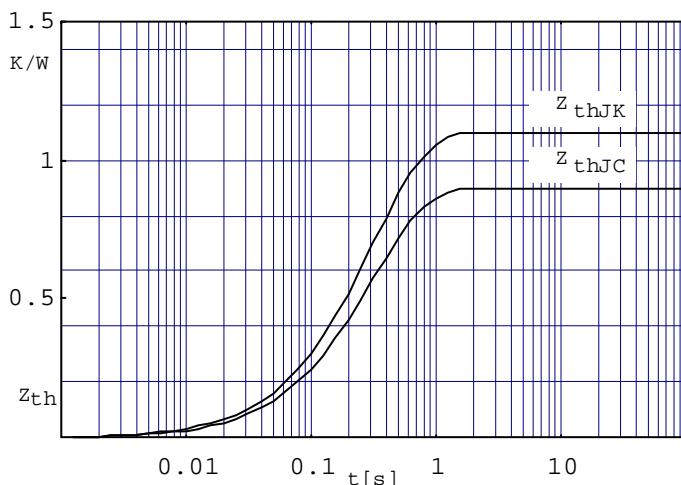


Fig. 6 Transient thermal impedance per diode/thyristor, calculated