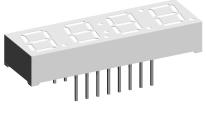


TDCG10..m, TDCR10..m, TDCY10..m

Vishay Semiconductors

Clock Display



16770

DESCRIPTION

Four digit display, with 10 mm digit charactersize. Designed as clock display with active colon between digit two and three.

FEATURES

- High efficient AlInGAP technology
- Dark surface, white segments
- Common anode (TDC.1050m)
- Common cathode (TDC.1060m)
- Multiplex mode
- Recommended viewing distance up to 7 m
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

• Clock modules for video/audioequipment, instrumentation, set top boxes

PRODUCT GROUP AND PACKAGE DATA

- Product group: display
- Package: 10 mm clock
- Product series: standard
- Angle of half intensity: ± 50°

PARTS TABLE			
PART	COLOR	LUMINOUS INTENSITY AT 10 mA	CIRCUITRY
TDCG1050m	Green	I _V = (2800 to 4000) μcd	Common anode
TDCG1060m	Green	I _V = (2800 to 4000) μcd	Common cathode
TDCR1050m	Red	l _V = (4000 to 6000) μcd	Common anode
TDCR1060m	Red	I _V = (4000 to 6000) μcd	Common cathode
TDCY1050m	Super yellow	I _V = (4000 to 8000) μcd	Common anode
TDCY1060m	Super yellow	I _V = (4000 to 8000) μcd	Common cathode

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾ TDCG1050m, TDCG1060m, TDCR1050m, TDCR1060m, TDCY1050m, TDCY1060m

,				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage per segment		V _R	5	V
DC forward current per segment		I _F	25	mA
Peak forward current per segment	Duty 1/10 at 1 kHz	I _{FM}	160	mA
Power dissipation		PV	60	mW
Operating temperature range		T _{amb}	- 40 to + 85	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature		T _{sd}	260 ± 5	°C

Note

 $^{(1)}~T_{amb}$ = 25 °C, unless otherwise specified



RoHS

COMPLIANT

TDCG10..m, TDCR10..m, TDCY10..m

Vishay Semiconductors

Clock Display



OPTICAL AND ELECTRICAL CHARACTERISTICS ⁽¹⁾ TDCG1050m, TDCG1060m, GREEN								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$I_F = 2 \text{ mA}$	TDCG1050m	- I _V	-	1000	-	und	
Luminous intensity per segment ⁽²⁾		TDCG1060m					μcd	
	I _F = 10 mA	TDCG1050m	- I _V	2800	4000	-	und	
		TDCG1060m					μcd	
	$I_F = 2 \text{ mA}$	TDCG1050m	- I _V	-	200	-	und	
Luminous intensity of color		TDCG1060m					μcd	
Luminous intensity of colon	I _F = 10 mA	TDCG1050m	- I _V	500	1200	-	und	
		TDCG1060m					μcd	
Dominant wavelength	l _F = 20 mA		λ_d	562	573	575	nm	
Peak wavelength	l _F = 20 mA	TDCG1050m, TDCG1060m	λρ	-	575	-	nm	
Spectral bandwidth	l _F = 20 mA		Δλ	-	20	-	nm	
Forward voltage per segment or DP	l _F = 20 mA		V _F	-	2	2.4	V	
Reverse current per segment or DP	V _R = 5 V		I _R	-	-	10	μA	

Notes

⁽¹⁾ $T_{amb} = 25$ °C, unless otherwise specified

(2) I_{Vmin} and I_V groups are mean values of all segments (a to g, D1 to D4), matching factor within segments is \ge 0.5, excluding decimal points and colon.

OPTICAL AND ELECTRICAL CHARACTERISTICS ⁽¹⁾ TDCR1050m, TDCR1060m, RED								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$I_{\rm F} = 2 \rm mA$	TDCR1050m	- I _V	-	1500	-	und	
Luminous intensity per compart (2)	$i_F = 2 IIIA$	TDCR1060m					µcd	
Luminous intensity per segment ⁽²⁾	I _F = 10 mA	TDCR1050m		4000	6000	-	und	
		TDCR1060m	I _V				μcd	
	I _F = 2 mA	TDCR1050m	- I _V	-	400	-	µcd	
		TDCR1060m						
Luminous intensity of colon	I _F = 10 mA	TDCR1050m	- I _V	500	800	-	μcd	
		TDCR1060m						
Dominant wavelength	I _F = 20 mA		λ _d	-	631	-	nm	
Peak wavelength	I _F = 20 mA	TDCR1050m, TDCR1060m	λρ	-	639	-	nm	
Spectral bandwidth	I _F = 20 mA		Δ_{λ}	-	20	-	nm	
Forward voltage per segment or DP	I _F = 20 mA		V _F	-	2	2.4	V	
Reverse current per segment or DP	V _R = 5 V		I _R	-	-	10	μA	

Notes

 $^{(1)}~T_{amb}$ = 25 °C, unless otherwise specified

(2) $I_{Vmin.}$ and I_V groups are mean values of all segments (a to g, D1 to D4), matching factor within segments is \ge 0.5, excluding decimal points and colon.



Clock Display

Vishay Semiconductors

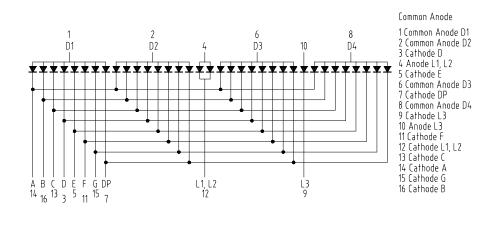
OPTICAL AND ELECTRICAL CHARACTERISTICS ⁽¹⁾ TDCY1050m, TDCY1060m, SUPER YELLOW								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	I _F = 2 mA	TDCY1050m	- I _V	-	1500	-	μcd	
Luminous intensity per segment ⁽²⁾		TDCY1060m						
Luminous intensity per segment -	10 10	TDCY1050m		4000	8000	-	und	
	I _F = 10 mA	TDCY1060m	l∨ Iv				μcd	
	I _F = 2 mA	TDCY1050m	- I _V	-	400	-	und	
		TDCY1060m					µcd	
Luminous intensity of colon	I _F = 10 mA	TDCY1050m	- I _V	500	1000	-	und	
		TDCY1060m					µcd	
Dominant wavelength	I _F = 20 mA		λ_d	-	589	-	nm	
Peak wavelength	I _F = 20 mA	TDCY1050m, TDCY1060m	λρ	-	591	-	nm	
Spectral bandwidth	I _F = 20 mA		Δλ	-	15	-	nm	
Forward voltage per segment or DP	I _F = 20 mA		V _F	-	2	2.4	V	
Reverse current per segment or DP	$V_R = 5 V$		I _R	-	-	10	μA	

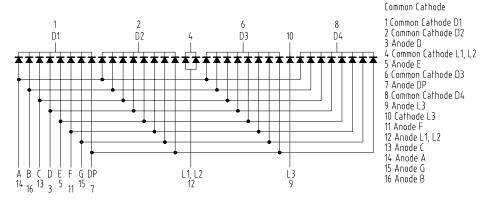
Notes

 $^{(1)}~T_{amb}$ = 25 °C, unless otherwise specified

(2) I_{Vmin}, and I_V groups are mean values of all segments (a to g, D1 to D4), matching factor within segments is ≥ 0.5, excluding decimal points and colon.

PINNING





Drawing-No.: 6.544-5332.01-4 Bl. 2 Issue: 1; 20.02.02

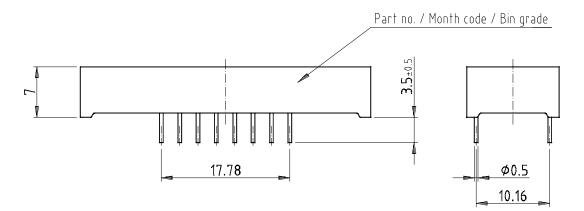
TDCG10..m, TDCR10..m, TDCY10..m

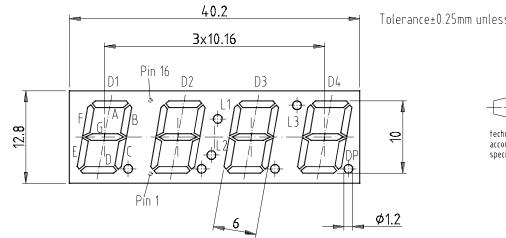
Vishay Semiconductors

Clock Display



PACKAGE DIMENSIONS in millimeters





Drawing-No.: 6.544-5332.01-4 Bl. 1 Issue: 3; 27.02.02

Tolerance±0.25mm unless otherwise noted



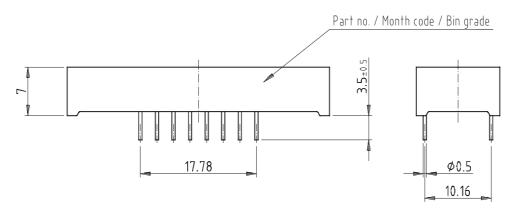
technical drawings according to DIN specifications

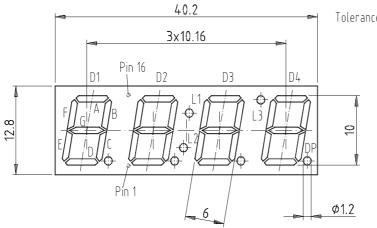


Vishay Semiconductors

Display-10 mm Clock Multiplex

Package Dimensions in mm





Tolerance±0.25mm unless otherwise noted



technical drawings according to DIN specifications

Drawing-No.: 6.544-5332.01-4 Bl. 1 Issue: 3; 27.02.02

Display-10 mm Clock Multiplex

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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