



Buffered H-Bridge

DESCRIPTION

The Si9986 is an integrated, buffered H-bridge with TTL compatible inputs and the capability of delivering a continuous 1.0 A at $V_{DD}=12~V$ (room temperature) at switching rates up to 200 kHz. Internal logic prevents the upper and lower outputs of either half-bridge from being turned on simultaneously. Unique input codes allow both outputs to be forced low (for braking) or forced to a high impedance level.

The Si9986 is available in both standard and lead (Pb)-free, 8-pin SOIC packages, specified to operate over a voltage range of 3.8 V to 13.2 V, and the commercial temperature range of 0 to 70 $^{\circ}$ C (C suffix) and the industrial temperature range of - 40 to 85 $^{\circ}$ C (D suffix).

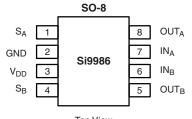
FEATURES

- 1.0 A H-Bridge
- · 200 kHz Switching Rate
- · Shoot-Through Limited
- · TTL Compatible Inputs
- 3.8 to 13.2 V Operating Range
- Surface Mount Packaging

APPLICATIONS

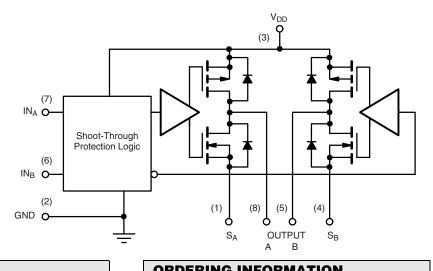
- VCM Driver
- · Brushed Motor Driver
- · Stepper Motor Driver
- Power Converter
- · Optical Disk Drives
- Power Supplies
- High Performance Servo

FUNCTIONAL BLOCK DIAGRAM, PIN CONFIGURATION AND TRUTH TABLE



Top View

TRUTH TABLE					
INA	IN _B	OUTA	OUTB		
1	0	1	0		
0	1	0	1		
0	0	0	0		
1	1	HiZ	HiZ		



PIN DESCRIPTION				
Pin Number	Name	Function		
1	S _A	Source of the low-side MOSFET on bridge arm A		
2	GND	Ground		
3	V_{DD}	IC power supply		
4	S _B	Source of the low-side MOSFET on bridge arm B		
5	OUTB	Center tap of bridge arm B. Connects to one end of the load		
6	IN _B	Input signal to control bridge arm B		
7	IN _A	Input signal to control bridge arm A		
8	OUT _A	Center tap of bridge arm A. Connects to the other end of the load		

ORDERING INFORMATION				
Temperature Range	Package			
0 to 70 °C	Tape and Reel			
- 40 to 85 °C	Tape and neer			
0 to 70 °C	Lead (Pb)-free			
- 40 to 85 °C	Tape and Reel			
0 to 70 °C	Bulk (tubes)			
- 40 to 85 °C	bulk (tubes)			
	Temperature Range 0 to 70 °C - 40 to 85 °C 0 to 70 °C - 40 to 85 °C 0 to 70 °C			

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

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ABSOLUTE MAXIMUM RATINGS ^a				
Parameter		Limit	Unit	
Voltage on any Pin with Respect to Ground		- 0.3 to V _{DD} + 0.3		
Voltage on Pins 5, 8 with Respect to GND		- 1 to V _{DD} + 1	V	
Voltage on Pins 1, 4		- 0.3 to GND + 1		
Peak Output Current		1.5	A	
Storage Temperature		- 65 to 150	°C	
Maximum Junction Temperature (T _J)		150		
Maximum V _{DD}		15	V	
Power Dissipation ^b		1	W	
Θ_{JA}		100	°C/W	
Operating Temperature Range	Si9986CY	0 to 70	°C	
Operating remperature riange	Si9986DY	- 45 to 85		

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING RANGE					
Parameter	Limit	Unit			
V_{DD}	3.8 to 13.2	V			
Maximum Junction Temperature (T _J)	125	°C			

SPECIFICATIONS								
		Test Conditions Unless Otherwise Specified $V_{DD} = 3.8 \text{ to } 13.2 \text{ V}$ S_A at GND, S_B at GND			Limits Suffix, 0 to 70 Iffix, - 40 to 8			
Parameter	Symbol			Min ^a	Typ ^b	Max ^a	Unit	
Input					'			
Input Voltage High	V _{INH}			2			V	
Input Voltage Low	V_{INL}					1	V	
Input Current with Input Voltage High	I _{INH}	V _{IN} = 2 V				1		
Input Current with Input Voltage Low	I _{INL}	V _{IN} = 0 V		- 1			μΑ	
Output								
	V _{OUTH}	I _{OUT} = - 500 mA	V _{DD} = 10.8 V	10.5	10.7			
Output Voltage High			$V_{DD} = 4.5 \text{ V}$	4.1	4.3			
		I _{OUT} = - 300 mA, V _{DD} = 3.8 V		3.4	3.7		V	
	V _{OUTL}	I _{OUT} = 500 mA	V _{DD} = 10.8 V		0.2	0.3]	
Output Voltage Low			V _{DD} = 4.5 V		0.2	0.4		
		I _{OUT} = 300 mA, V _{DD} = 3.8 V			0.1	0.4		
Output Leakage Current High	I _{OLH}	$IN_A = IN_B \ge 2 \text{ V}, \text{ V}_{OU}$	_T = V _{DD} = 13.2 V	- 10	0		μΑ	
Output Leakage Current Low	I _{OLL}	$V_{OUT} = 0, V_{DI}$	₀ = 13.2 V		0	10	μΑ	
Output V Clamp High	V _{CLH}	$IN_A = IN_B \ge 2 \text{ V}$	I _{OUT} = 100 mA		V _{DD} + 0.7		V	
Output V Clamp Low	V _{CLL}	1114 - 111B = 5 A	I _{OUT} = - 100 mA		- 0.7			
Supply								
V _{DD} Supply Current	1	$IN = 100 \text{ kHz}, V_{DD} = 5 \text{ V}$			2		mA	
VDD cupply current	I _{DD}	$IN_A = IN_B = 4.5 \text{ V}, V_{DD} = 5.5 \text{ V}$				300	μΑ	
Dynamic								
Propogation Delay Time	T _{PLH}	V _{DD} = 1	V _{DD} = 5 V		300		nS	
Topogation Delay Time	T _{PHL}	v _{DD} = 3 v			100	·	113	

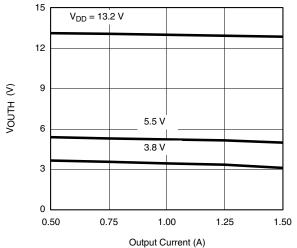
a. Device Mounted with all leads soldered or welded to PC board. b. Derate 10 mW/°C above 25 °C.

a. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.

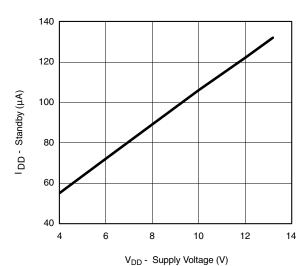
b. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.



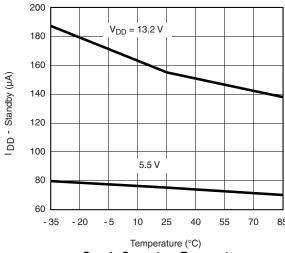
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



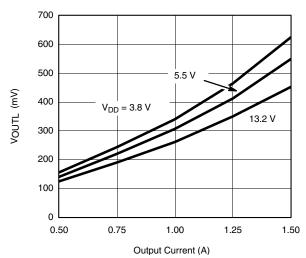
Output High Voltage vs. Output Current



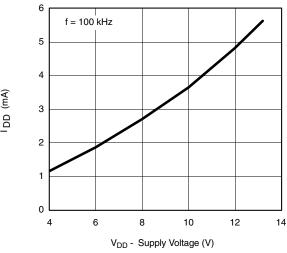
Supply Current vs. Supply Voltage



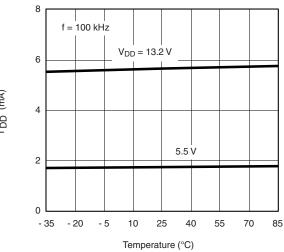
Supply Current vs. Temperature



Output Low Voltage vs. Output Current



Supply Current vs. Supply Voltage

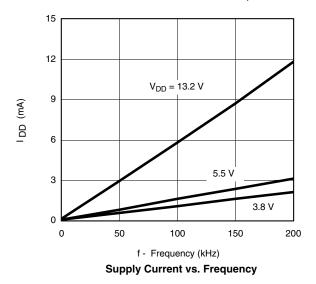


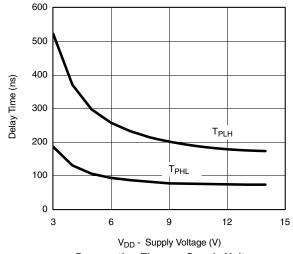
Supply Current vs. Temperature

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



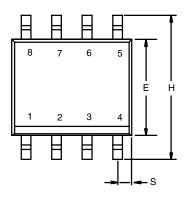


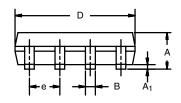
Propagation Time vs. Supply Voltage

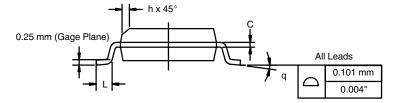
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS INCHES		HES		
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050) BSC	
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
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