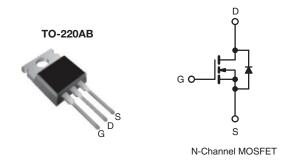


Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	100	100		
$R_{DS(on)}(\Omega)$	V _{GS} = 5.0 V	0.27		
Q _g (Max.) (nC)	12			
Q _{gs} (nC)	3.0			
Q _{gd} (nC)	7.1			
Configuration	Single			



FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Logic-Level Gate Drive
- R_{DS(on)} Specified at V_{GS} = 4 V and 5 V
- 175 °C Operating Temperature
- Fast Switching
- · Ease of Paralleling
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRL520PbF
Leau (FD)-iree	SiHL520-E3
SnPb	IRL520
SIFD	SiHL520

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 10	V	
Continuous Drain Current	V_{GS} at 5.0 V $T_C = 25 ^{\circ}C$	I_	9.2	
Continuous Drain Current	V _{GS} at 5.0 V T _C = 100 °C	ID	6.5	Α
Pulsed Drain Current ^a	I _{DM}	36		
Linear Derating Factor		0.40	W/°C	
Single Pulse Avalanche Energy ^b	E _{AS}	170	mJ	
Avalanche Current ^a	I _{AR}	9.2	Α	
Repetitive Avalanche Energy ^a		E _{AR}	6.0	mJ
Maximum Power Dissipation	T _C = 25 °C	P _D	60	W
Peak Diode Recovery dV/dt ^c	dV/dt	5.5	V/ns	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 175	°C	
Soldering Recommendations (Peak Temperature)	for 10 s		300 ^d	
Mounting Torque	6-32 or M3 screw		10	lbf ⋅ in
Mounting Torque	0-32 OF IVIS SCIEW		1.1	N⋅m

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. V_{DD} = 25 V, starting T_J = 25 °C, L = 3.0 mH, R_q = 25 Ω , I_{AS} = 9.2 A (see fig. 12).
- c. $I_{SD} \le 9.2$ A, $dI/dt \le 110$ A/ μ s, $V_{DD} \le V_{DS}$, $T_J \le 175$ °C.
- d. 1.6 mm from case.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	62	
Case-to-Sink, Flat, Greasd Surface	R _{thCS}	0.50	-	°C/W
Maximum Junction-to-Case (Drain)	R _{thJC}	-	2.5	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} =	= 0 V, I _D = 250 μA	100	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I _D = 1 mA	-	0.12	-	V/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	- V _{GS} , I _D = 250 μA	1.0	-	2.0	V	
Gate-Source Leakage	I _{GSS}	,	V _{GS} = ± 10 V	-	-	± 100	nA	
Zone Onto Voltago Dunio Ocument	I _{DSS}	V _{DS} =	V _{DS} = 100 V, V _{GS} = 0 V		-	25		
Zero Gate Voltage Drain Current		V _{DS} = 80 V	V _{GS} = 0 V, T _J = 150 °C	-	-	250	μA	
Dunin Course On Chata Basistana	Б	V _{GS} = 5.0 V	I _D = 5.5 A ^b	-	-	0.27		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 4.0 V	I _D = 4.6 A ^b	-	-	0.38	Ω	
Forward Transconductance	9 _{fs}	V _{DS} :	= 50 V, I _D = 5.5 A	3.2	-	-	S	
Dynamic								
Input Capacitance	C _{iss}		V _{GS} = 0 V,	-	490	-		
Output Capacitance	C _{oss}	1	$V_{DS} = 25 \text{ V},$	-	150	-	pF	
Reverse Transfer Capacitance	C _{rss}	f = 1.	0 MHz, see fig. 5	-	30	-		
Total Gate Charge	Qg			-	-	12		
Gate-Source Charge	Q _{gs}	$V_{GS} = 5.0 \text{ V}$	$I_D = 9.2 \text{ A}, V_{DS} = 80 \text{ V},$ see fig. 6 and 13 ^b	-	-	3.0	nC	
Gate-Drain Charge	Q_{gd}		gramma ra	-	-	7.1		
Turn-On Delay Time	t _{d(on)}			-	9.8	-		
Rise Time	t _r	$V_{DD} = 50 \text{ V}, I_D = 9.2 \text{ A}, \\ R_g = 9.0 \Omega, R_D = 5.2 \Omega, \text{ see fig. } 10^{\text{b}}$		-	64	-	ns	
Turn-Off Delay Time	t _{d(off)}			-	21	-		
Fall Time	t _f				27	-		
Internal Drain Inductance	L_D	6 mm (0.25") 1	Between lead, 6 mm (0.25") from		4.5	-	nH	
Internal Source Inductance	L _S	package and die contact	center of	_	7.5	-		
Drain-Source Body Diode Characteristic	s							
Continuous Source-Drain Diode Current	I _S	MOSFET sym showing the		-	ı	9.2	A	
Pulsed Diode Forward Current ^a	I _{SM}	integral reverse p - n junction diode		-	-	36		
Body Diode Voltage	V_{SD}	$T_J = 25 ^{\circ}\text{C}, I_S = 9.2 \text{A}, V_{GS} = 0 \text{V}^{\text{b}}$		-	-	2.5	V	
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 9.2 A, dl/dt = 100 A/μs ^b		_	130	190	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			-	0.83	1.0	μC	
Forward Turn-On Time	t _{on}	Intrinsic tu	rn-on is dominated by L_S and L_D)			L _D)		

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width $\leq 300~\mu s;$ duty cycle $\leq 2~\%.$



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

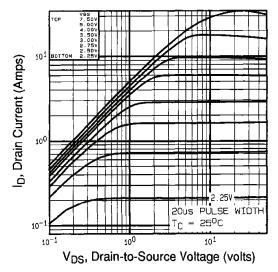


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

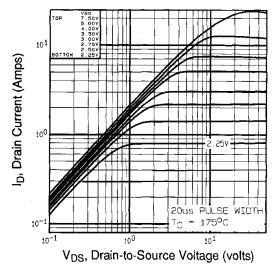


Fig. 2 - Typical Output Characteristics, $T_C = 175$ °C

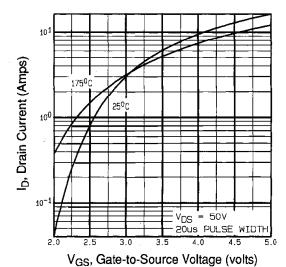


Fig. 3 - Typical Transfer Characteristics

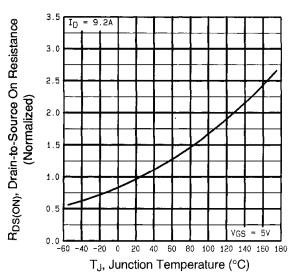


Fig. 4 - Normalized On-Resistance vs. Temperature



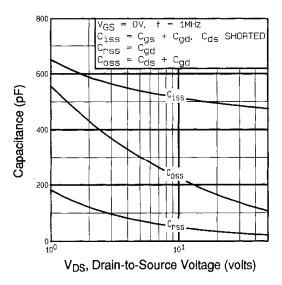


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

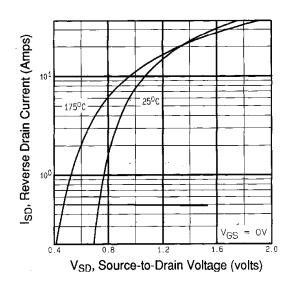


Fig. 7 - Typical Source-Drain Diode Forward Voltage

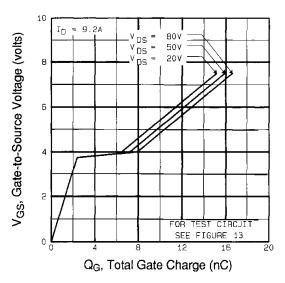


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

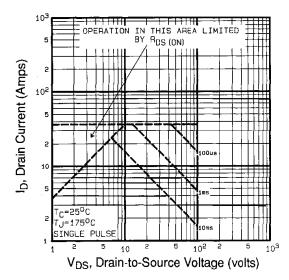


Fig. 8 - Maximum Safe Operating Area





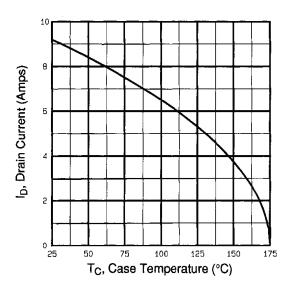


Fig. 9 - Maximum Safe Operating Area

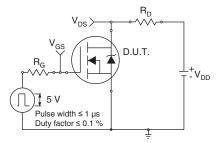


Fig. 10a - Switching Time Test Circuit

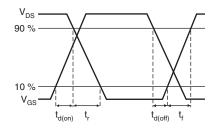


Fig. 10b - Switching Time Waveforms

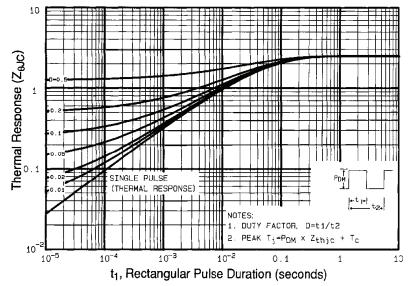
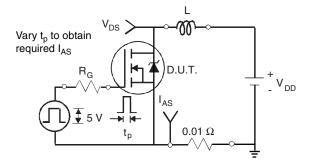


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case





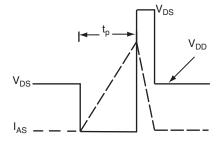


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

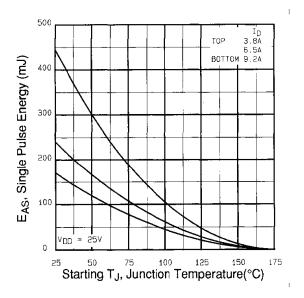


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

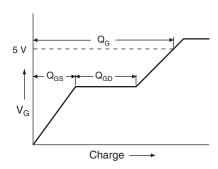


Fig. 13a - Basic Gate Charge Waveform

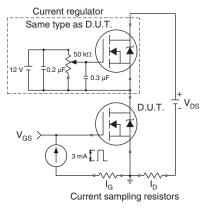
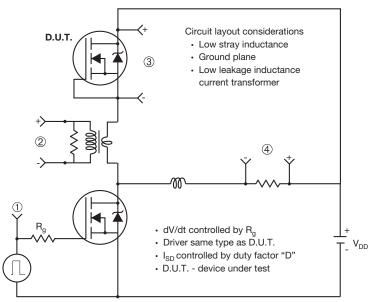


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



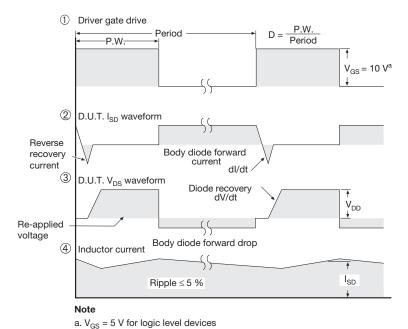


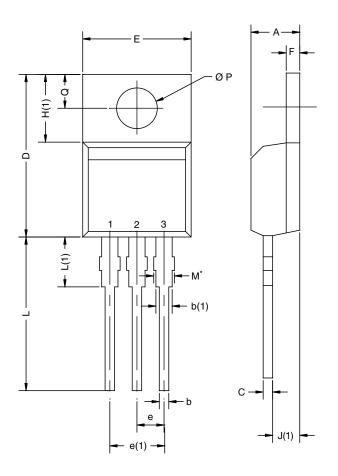
Fig. 14 - For N-Channel

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TO-220AB



	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
	0208-Rev. N,		0.102	0.118	

Notes

- * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM
- · Xi'an and Mingxin actual photo





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Vishay

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