

## Surge Arrester

## 2-Electrode-Arrester

A71-H45X

Ordering code: B88069X2590xxxx <sup>a)</sup>

| DC spark-over voltage <sup>1) 2)</sup>  | 4500  | V           |
|---|---|-------------|
|   | ± 20  | %           |
| Impulse spark-over voltage  |   |             |
| at 100 V/µs - for 99 % of measured values<br>- typical values of distribution                         | < 5800<br>< 5700  | V<br>V      |
| at 1 kV/µs - for 99 % of measured values<br>- typical values of distribution                          | < 6000<br>< 5800  | V<br>V      |
| Nominal impulse discharge current (wave 8/20 µs)<br>Single impulse discharge current (wave 8/20 µs)   | 2.5<br>2.5  | kA<br>kA    |
| Nominal alternating discharge current (50 Hz, 1 s)<br>Alternating discharge current (50 Hz, 9 cycles) | 2.5<br>2.5  | A<br>A      |
| Insulation resistance at 100 $V_{dc}$   | > 10  | GΩ          |
| Capacitance at 1 MHz  | < 1   | pF          |
| Arc voltage at 1 A<br>Glow to arc transition current<br>Glow voltage                                  | ~ 20<br>~ 1<br>~ 180  | V<br>A<br>V |
| Weight  | ~ 1.5   | g           |
| Operation and storage temperature   | -40 +90   | °C          |
| Climatic category (IEC 60068-1)   | 40/ 90/ 21  |             |
| Marking, green  | EPCOS4500 YY O4500- Nominal voltageYY- Year of productionO- Non radioactive |             |

<sup>a)</sup> xxxx = S102 (100 pcs on 5 taped stripes) = T502 (500 pcs on tape and reel)

At delivery AQL 0.65 level II, DIN ISO 2859
In ionized mode

Terms in accordance with ITU-T Rec. K.12 and DIN 57845/VDE0845

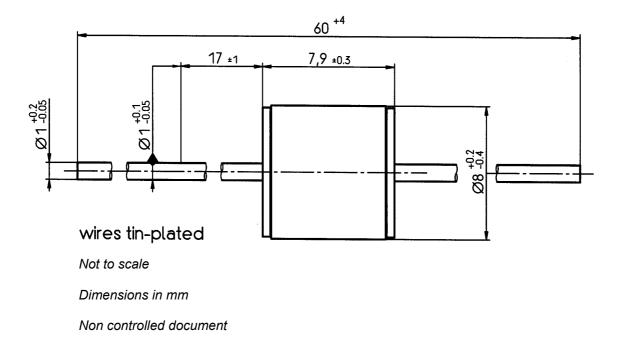


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