

SIOV metal oxide varistors

Leaded varistors, AdvanceD-MP, S10 series

Series/Type: B722*

Date: April 2011

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AdvanceD-MP, S10 series

Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire

Features

- Wide operating voltage range 175 ... 680 V_{RMS}
- All types duty cycle @ 6 kV/ 3 kA = >10 pulses, according to IEC 60950-1 Annex Q; IEC 61051-2
- All types duty cycle @ 6 kV/ 3 kA = >15 pulses, according to UL 1449, 3rd edition, hybrid generator (1.2/50 µs: 8/20 µs), Type 3 listed
- Multiple pulse handling capability

Approvals

- UL
- CSA
- VDF
- IFC

Delivery mode

- Bulk (standard), taped versions on reel or in Ammo pack upon request.
- For further details refer chapter "Taping, packaging and lead configuration" for leaded varistors.

General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to IEC 61051	40 + 85	°C
Storage temperature		40 +125	°C
Electric strength	to IEC 61051	³ 2.5	kV _{RMS}
Insulation resistance	to IEC 61051	³ 100	MW
Response time		< 25	ns



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Electrical specifications and ordering codes Maximum ratings (T $_{\rm A}$ = 85 °C)

Ordering code	Туре	V_{RMS}	V_{DC}	i _{max}	W_{max}	P_{max}
•	(untaped)			(8/20 µs)	(2 ms)	
	SIOV-	V	V	Α	J	W
B72210P2171K101	S10K175E2K1	175	225	3500	40.0	0.40
B72210P2271K101	S10K275E2K1	275	350	3500	60.0	0.40
B72210P2301K101	S10K300E2K1	300	385	3500	65.0	0.40
B72210P2321K101	S10K320E2K1	320	420	3500	72.0	0.40
B72210P2351K101	S10K350E2K1	350	460	3500	77.0	0.40
B72210P2381K101	S10K385E2K1	385	505	3500	82.0	0.40
B72210P2421K101	S10K420E2K1	420	560	3500	87.0	0.40
B72210P2461K101	S10K460E2K1	460	615	3500	92.0	0.40
B72210P2511K101	S10K510E2K1	510	670	3500	92.0	0.40
B72210P2551K101	S10K550E2K1	550	745	3500	97.0	0.40
B72210P2621K101	S10K620E2K1	625	825	3500	105.0	0.40
B72210P2681K101	S10K680E2K1	680	895	3500	115.0	0.40

Characteristics (T _A = 25 °C)

Ordering code	Туре	V_{v}	DV_v	V _{c,max}	i _c	C_{typ}
	(untaped)	(1 mA)	(1 mA)	(i _c)		(1 kHz)
	SIOV-	V	%	V	Α	pF
B72210P2171K101	S10K175E2K1	270	±10	455	25.0	500
B72210P2271K101	S10K275E2K1	430	±10	710	25.0	315
B72210P2301K101	S10K300E2K1	470	±10	775	25.0	285
B72210P2321K101	S10K320E2K1	510	±10	840	25.0	265
B72210P2351K101	S10K350E2K1	560	±10	910	25.0	240
B72210P2381K101	S10K385E2K1	620	±10	1025	25.0	230
B72210P2421K101	S10K420E2K1	680	±10	1120	25.0	210
B72210P2461K101	S10K460E2K1	750	±10	1240	25.0	190
B72210P2511K101	S10K510E2K1	820	±10	1355	25.0	180
B72210P2551K101	S10K550E2K1	910	±10	1500	25.0	160
B72210P2621K101	S10K620E2K1	1000	±10	1650	25.0	150
B72210P2681K101	S10K680E2K1	1100	±10	1815	25.0	135

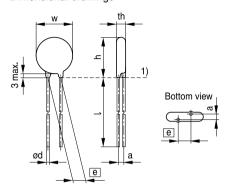




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Dimensional drawings



1) Seating plane to IEC 60717

VAR0408-C-E

Weight

Nominal diameter	V_{RMS}	Weight
mm	V	g
10	175 680	1.7 3.5

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The weight of varistors in between these voltage classes can be interpolated.

Dimensions

Ordering code	[e] ±1	a ±1	W _{max}	th _{max}	h _{max}	I _{min}	d ±0.05
	mm	mm	mm	mm	mm	mm	mm
B72210P2171K101	7.5	2.4	12.0	5.1	16.0	25.0	0.8
B72210P2271K101	7.5	3.2	12.0	5.9	16.0	25.0	0.8
B72210P2301K101	7.5	3.5	12.0	6.1	16.0	25.0	0.8
B72210P2321K101	7.5	3.7	12.0	6.3	16.0	25.0	0.8
B72210P2351K101	7.5	4.0	12.5	6.7	16.5	25.0	0.8
B72210P2381K101	7.5	4.3	12.5	7.7	16.5	25.0	0.8
B72210P2421K101	7.5	4.6	12.5	8.1	16.5	25.0	0.8
B72210P2461K101	7.5	5.0	12.5	8.4	16.5	25.0	0.8
B72210P2511K101	7.5	5.3	13.0	8.8	17.0	25.0	0.8
B72210P2551K101	7.5	5.8	13.0	9.3	17.0	25.0	0.8
B72210P2621K101	7.5	6.3	13.0	9.8	17.0	25.0	0.8
B72210P2681K101	7.5	6.9	13.0	10.4	17.0	25.0	0.8



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Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with	To meet the specified value
	the specified measuring current applied is called V_V (1 mA _{DC} @ 0.2 2 s).	
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper	1000 h at UCT	DV/V (1 mA) £10%
category temperature	After having continuously applied the maximum allowable AC voltage at UCT ± 2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured.	
Surge current derating,	10 surge currents (8/20 ms), unipolar,	DV/V (1 mA) £10%
8/20 ms	interval 30 s, amplitude corresponding	(measured in direction of
	to derating curve for 10 impulses at 20 ms	surge current)
2		No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding	DV/V (1 mA) £10%
2 1115	to derating curve for 10 impulses at	(measured in direction of
	2 ms	surge current)
		No visible damage
Electric strength	IEC 61051-1, test 4.9.2	No breakdown
	Metal balls method, 2500 V _{RMS} , 60 s	
	The varistor is placed in a container	
	holding 1.6 ±0.2 mm diameter metal	
	balls such that only the terminations of	
	the varistor are protruding. The specified voltage shall be applied	
	between both terminals of the specimen	
	connected together and the electrode	
	inserted between the metal balls.	



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Test Test methods/conditions Requirement Climatic sequence The specimen shall be subjected to: |DV/V (1 mA)| £10% a) dry heat at UCT, 16 h, IEC R_{ins} 3 100 MW 60068-2-2, test Ba b) damp heat, 1st cycle: 55 °C, 93% r. H., 24 h, IEC 60068-2-30, test Db c) cold, LCT, 2 h, IEC 60068-2-1, test Aa d) damp heat, additional 5 cycles: 55 °C/25 °C, 93% r. H., 24 h/cycle, IEC 60068-2-30, test Db. Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured. Thereafter, insulation resistance R_{ins} shall be measured at V = 500Rapid change of IEC 60068-2-14, test Na, LCT/UCT, |DV/V (1 mA)| £5% dwell time 30 min, 5 cycles temperature No visible damage Damp heat, steady state IEC 60068-2-78, test Ca |DV/V (1 mA)| £10% The specimen shall be subjected to R_{ins} 3 100 MW 40 ±2 °C, 90 to 95% r. H. for 56 days without load / with 10% of the maximum continuous DC operating voltage V_{DC}. Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured. Thereafter, insulation resistance R_{ins} shall be measured at V = 500V (insulated varistors only).





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Test	Test methods/conditions	Requirement
Solderability	IEC 60068-2-20, test Ta,	The inspection shall be
	method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s:	carried out under adequate light with normal eyesight or
	After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s:	DV/V (1 mA) £5%
	Each lead shall be dipped into a solder bath having a temperature of 260 ± 5 °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for 10 ± 1 s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V_V shall be measured and the specimen shall be visually examined.	No visible damage
Tensile strength	IEC 60068-2-21, test Ua1	DV/V (1 mA) £5%
	After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage.	No break of solder joint, no wire break
	Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	





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Test	Test methods/conditions	Requirement
Vibration	IEC 60068-2-6, test Fc, method B4	DV/V (1 mA) £5%
	Frequency range: $10 \dots 55 \text{ Hz}$ Amplitude: 0.75 mm or 98 m/s^2 Duration: $6 \text{ h} (3 \cdot 2 \text{ h})$ Pulse: sine wave After repeatedly applying a single harmonic vibration according to the table above. The change of V_V shall be measured and the specimen shall be visually examined.	No visible damage
Bump	IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400 m/s² Number of bumps: 4000 Pulse: half sine	DV/V (1 mA) £5% No visible damage
Fire hazard	IEC 60695-11-5 (needle flame test) Severity: vertical 10 s	5 s max.

Note:

$$\begin{split} & \text{UCT} = \text{Upper category temperature} \\ & \text{LCT} = \text{Lower category temperature} \\ & R_{\text{ins}} = \text{Insulation resistance} \end{split}$$

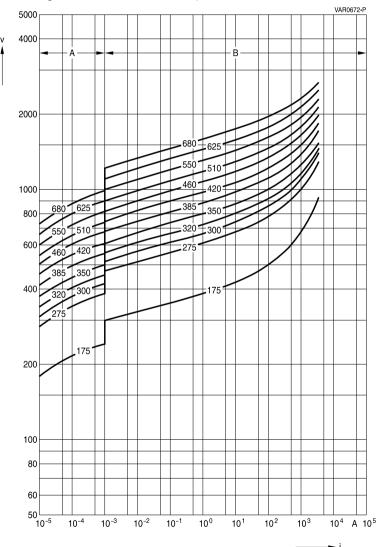


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v/i characteristics

v = f(i) - for explanation of the characteristics refer to "General technical information", 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



SIOV-S10 ... E2K1



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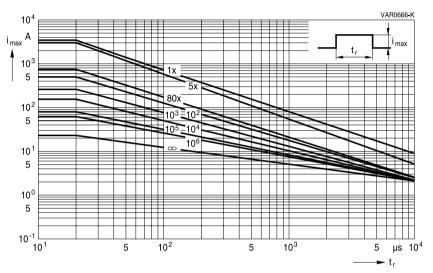
Leaded varistors

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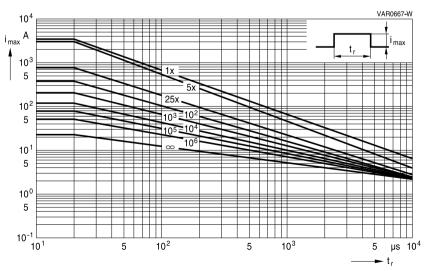
Derating curves

Maximum surge current $i_{max} = f(t_r, pulse train)$

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-S10K175 ... K460E2K1



SIOV-S10K510 ... K680E2K1



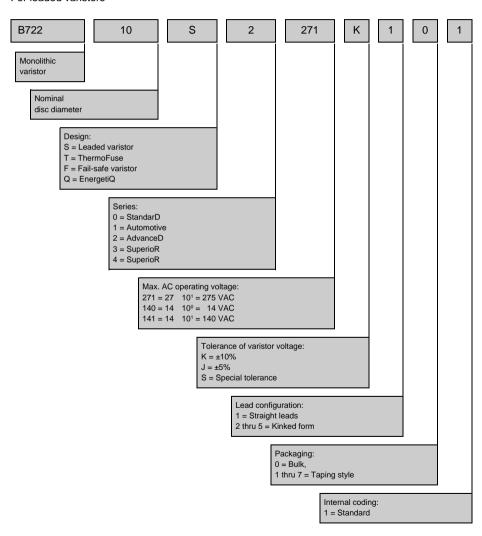
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Taping, packaging and lead configuration

1 EPCOS ordering code system

For leaded varistors





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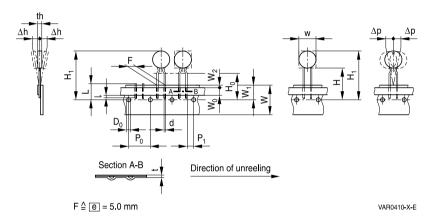
Leaded varistors

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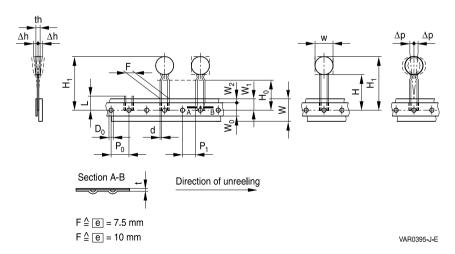
2 Taping and packaging of leaded varistors

Tape packaging for lead spacing = 5 fully conforms to IEC 60286-2, while for lead spacings = 7.5 and 10 the taping mode is based on this standard.

2.1 Taping in accordance with IEC 60286-2 for lead spacing 5.0 mm



2.2 Taping based on IEC 60286-2 for lead spacing 7.5 and 10 mm





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2.3 Tape dimensions (in mm)

Sym-	<i>e</i> = 5.0	Tolerance	<i>e</i> = 7.5	Tolerance	<i>e</i> = 10.0	Tolerance	Remarks
bol							
W		max.		max.		max.	see tables in
							each series
th		max.		max.		max.	under
							"Dimensions"
d	0.6	±0.05	0.8	±0.05	1.0	±0.05	
P_0	12.7	±0.3	12.71)	±0.3	12.7	±0.3	±1 mm/20
							sprocket holes
P_1	3.85	±0.7	8.95	±0.8	7.7	±0.8	
F	5.0	+0.6/ 0.1	7.5	±0.8	10.0	±0.8	
Dh	0	±2.0	depends o	n s	depends on	S	measured at
Dр	0	±1.3	0	±2.0	0	±2.0	top of compo-
							nent body
W	18.0	±0.5	18.0	±0.5	18.0	±0.5	
W_{o}	5.5	min.	11.0	min.	11.0	min.	Peel-off
							force ≥ 5 N
W_1	9.0	±0.5	9.0	+0.75/ 0.5	9.0	+0.75/ 0.5	
W_2	3.0	max.	3.0	max.	3.0	max.	
Н	18.0	+2.0/ 0	18.0	+2.0/ 0	18.0	+2.0/ 0	2)
H₀	16.0	±0.5	16.0	±0.5	16.0	±0.5	3)
	(18.0)		(18.0)				
H_1	32.2	max.	45.0	max.	45.0	max.	
$\overline{D_0}$	4.0	±0.2	4.0	±0.2	4.0	±0.2	
t	0.9	max.	0.9	max.	0.9	max.	without lead
L	11.0	max.	11.0	max.	11.0	max.	
1	4.0	max.					

¹⁾ Taping with $P_0 = 15.0$ mm upon request

²⁾ Applies only to uncrimped types

³⁾ Applies only to crimped types ($H_0 = 18$ upon request)



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2.4 Taping mode

Example: B72210S0271K1 5 1

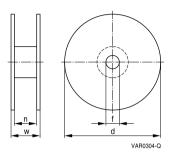
Digit 14	Taping	Reel type	Seating plane height H ₀	Seating plane height H	Pitch distance
	mode		for crimped types	for uncrimped types	P_0
			mm	mm	mm
0		Bulk			
1	G	1	16	18	12.7
2	G2	1	18		12.7
3	G3	II	16	18	12.7
4	G4	II	18		12.7
5	G5	Ш	16	18	12.7
6	GA	Ammo pack	16	18	12.7
7	G2A	Ammo pack	18		12.7
Internal	coding fo	r special tapin	g		
	G6	Ш	18		12.7
	G10	II	16	18	15.0
	G11	II	18		15.0
	G10A	Ammo pack	16	18	15.0
	G11A	Ammo pack	18		15.0

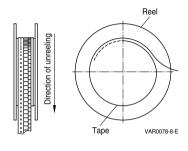


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2.5 Reel dimension



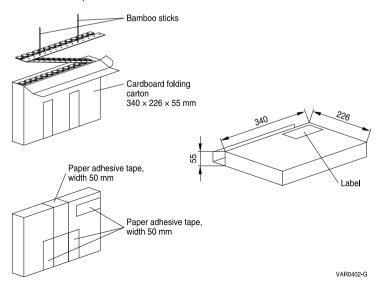


Dimensions (in mm)

Reel type	d	f	n	w
I	360 max.	31 ±1	approx. 45	54 max.
II	360 max.	31 ±1	approx. 55	64 max.
III	500 max.	23 ±1	approx. 59	72 max.

If reel type III is not compatible with insertion equipment because of its large diameter, nominal disk diameter 10 mm and 14 mm can be supplied on reel II upon request (taping mode G3).

2.6 Ammo pack dimensions



Please rea@autions and warniagsi Important notes the end of this document. Page 15 of 22



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3 Lead configuration

Straight leads are standard for disk varistors. Other lead configurations as crimp style or customer-specific lead wire length according to 3.1, 3.2, 3.3 and 3.4 are optional. Crimped leads (non-standard) are differently crimped for technical reasons; the individual crimp styles are denoted by consecutive numbers (S, S2 through S5) as shown in the dimensional drawings below.

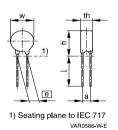
The crimp styles of the individual types can be seen from the type designation in the ordering tables.

3.1 Crimp style mode

Digit 13 of ordering code	Crimp style	Figure			
1	Standard, straight leads	1			
2	S2	2			
3	S3	3			
4	S4	4			
5	S5	5			
Available upon request					
Internal coding		6			

3.2 Standard leads and non-standard crimp styles

Standard, straight leads



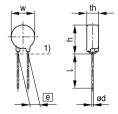
ød

Non-standard,

crimp style S2

1) Seating plane to IEC 60717 VAR0411-F-E

Non-standard, crimp style S3



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1) Seating plane to IEC 60717 VAR0396-R-E

Figure 3

Figure 2

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Figure 1

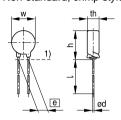


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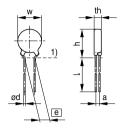


Non-standard, crimp style S4



1) Seating plane to IEC 60717 VAR0404-W-E

Non-standard, crimp style S5



1) Seating plane to IEC 60717 VAR0412-N-E

Figure 4

Figure 5

3.3 Component height (h max) for crimped versions (non-standard)

Due to technical reasons the component height (h_{max}) increases if a crimp is added. The maximum height of the crimped component can be found in the table below.

Nominal diameter	V_{RMS}	Crimp style	е	h _{max}
mm	V	, ,	mm	mm
5	11 175	S2	5.0	10.0
5	210 460	S3	5.0	10.0
7	11 175	S2	5.0	12.0
7	210 460	S3	5.0	12.0
10	11 300	S5	7.5	15.5
10	320 460	S3/S5	7.5	16.5
10	510	S3/S5	7.5	17.5
10	Automotive	S5	7.5	17.0
10	Automotive (D1 types)	S5	7.5	16.0
10	11 175	S4	5.0	16.5
10	210 460	S3	5.0	16.5
14	11 300	S5	7.5	20.0
14	320 460	S3/S5	7.5	20.0
14	510	S3/S5	7.5	21.5
14	Automotive	S5	7.5	21.0
14	Automotive (D1 types)	S5	7.5	20.0
20	11 320	S5	10.0	27.0
20	385 510	S5	10.0	27.5



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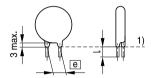
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3.4 Trimmed leads (non-standard)

Varistors with cut leads available upon request.

Lead length tolerances:



1) Seating plane to IEC 60717

VAR0642-U-E

Figure 6



Leaded varistors B722³

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Cautions and warnings

General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

- 1. Store SIOVs only in original packaging. Do not open the package before storage.
- 2. Storage conditions in original packaging:

Storage temperature: 25 °C ... +45 °C,

Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Dew precipitation: is to be avoided.

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS, -B, -SFS 24 months ETFV 12 months.

Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.



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Mounting

- Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.



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Symbols and terms

Symbol	Term
C	Capacitance
C_{typ}	Typical capacitance
i i	Current
i _c	Current at which V _{c, max} is measured
I _{leak}	Leakage current
i _{max}	Maximum surge current (also termed peak current)
I _{max}	Maximum discharge current to IEC 61643-1
I _{nom}	Nominal discharge current to IEC 61643-1
LCT	Lower category temperature
L_{typ}	Typical inductance
P _{max}	Maximum average power dissipation
R _{ins}	Insulation resistance
R_{min}	Minimum resistance
T_A	Ambient temperature
t _r	Duration of equivalent rectangular wave
UCT	Upper category temperature
V	Voltage
V_{clamp}	Clamping voltage
V _{c, max}	Maximum clamping voltage at specified current i _c
V_{DC}	DC operating voltage
V_{jump}	Maximum jump start voltage
V_{max}	Maximum voltage
V_{op}	Operating voltage
V_{RMS}	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
V_{surge}	Super imposed surge voltage
V_{V}	Varistor voltage
DV_{V}	Tolerance of varistor voltage
W_{LD}	Maximum load dump
W_{max}	Maximum energy absorption
е	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

Important notes

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